

#### DISTRIBUTED SECONDARY CONTROL OF HYBRID AC/DC MICROGRIDS

### TESIS PARA OPTAR AL GRADO DE DOCTOR EN INGENIERÍA ELÉCTRICA EN COTUTELA CON LA UNIVERSIDAD DE WATERLOO, CANADA

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FECHA: 2021

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This Ph.D. thesis focuses on studying hybrid ac/dc-microgrids, composed of an ac-microgrid and a dc-microgrid, connected by Interlinking Converters (ICs). Specifically, this work is focused on developing distributed secondary controllers that consider the hybrid microgrid as a single entity and not as three independent systems interacting with each other. The main goal of the secondary controller is augmented with one of the following objectives: (i) achieving active power consensus between ac-DGs and dc-DGs (and ICs), or (ii) minimizing the operation cost of the microgrid. The additional control task is performed with information from the nearest neighbours from both sides of the microgrid. Two analytical closed-loop models for a hybrid microgrid are obtained in this thesis. The first one considers the consensus-based distributed control strategy proposed for active power consensus. In contrast, the second one considers the strategy proposed for minimizing the operation cost of the microgrid. The closed-loop models are used to perform several analyses of the control systems proposed. Several tests are carried out to validate the performance of the proposed control strategies in two different topologies: a 24kW prototype with a single IC and a 33kW prototype with multiple ICs.

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Esta tesis de doctorado estudia micro-redes híbridas ac/dc, las que están compuestas por una micro-red ac y una micro-red dc, las cuales se interconectan con interlinking converters (ICs). Específicamente, este trabajo desarrolla controladores secundarios distribuidos que consideran a la micro-red híbrida como una sola entidad y no como tres sistemas independientes que interactúan entre si. El objetivo principal del control secundario es amplificado con uno de los siguientes objetivos adicionales: (i) alcanzar un consenso de potencia entre las unidades ac y las unidades de (y los ICs), o (ii) minimizar el costo total de operación de la micro-red. La acción de control adicional se realiza con información de los vecinos más cercanos a ambos lados de la micro-red. Dos modelos analíticos de lazo cerrado para una micro-red híbrida se obtienen en esta tesis. El primero considera la estrategia propuesta para obtener consenso de potencia activa, mientras que el segundo considera la estrategia propuesta para minimizar el costo total de operación de la micro-red. Los modelos de lazo cerrado son utilizados para diferentes tipos de análisis a los sistemas de control propuestos. Se realizan numerosas pruebas para validar el desempeño de los controladores propuestos en dos diferentes topologías: un prototipo de 24kW con un IC y un prototipo de 33 kW con múltiples ICs.

To Nicole and Marcelo, the most im	nportant people in my life, in recognition of all their love support and encouragement to achieve all of our goals
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## List of Abbreviations

BESS Battery Energy Storage System.

**DAPI** Distributed-Averaging Proportional-Integral.

DES Distributed Energy Storage.DG Distributed Generator.

**DiGen** Diesel Generator.

**DSC** Distributed Secondary Control.

ED Economic Dispatch.
ESS Energy Storage System.

**EV** Electrical Vehicle.

**GPS** Global Power Sharing.

**HBC** Half Bridge Converter.

IC Interlinking Converter.

ICC Incremental Cost Consensus.

INV Inverter.

KKT Karush-Kuhn-Tucker.

**LPF** Low-Pass-Filter.

LPS Local Power Sharing.

MAS Multi-Agent Systems.

**PCC** Point of Common Coupling.

PI Proportional-Integral.
PLL Phase Locked-Loop.
PR Proportional-Resonant.

PV Photo-Voltaic.

**SPS** Storage Power Sharing.

SoC State of Charge.

VC-VSC Voltage Controlled Voltage Source Converter.

VSC Voltage Source Converter.

**WT** Wind Turbine.



## List of Symbols

 $\omega_r$  Resonant frequency, proportional-resonant controller.  $k_r$  Resonant gain, proportional-resonant controller.

 $k_p$  Proportional gain, proportional-integral controller.

 $k_{\rm i}$  Integral gain, proportional-integral controller.  $M_{\rm ac-i}$  The i<sup>th</sup> ac-DG's active power droop gain.

 $\omega_{\rm i}$  The i<sup>th</sup> ac-DG's frequency.

 $\omega_{MIN}$  The ac-DG's minimum frequency.  $P_{MAX}$  The ac-DG's maximum active power.

 $N_{
m ac-i}$  The i<sup>th</sup> ac-DG's reactive power droop gain.

 $E_{\rm i}$  The i<sup>th</sup> ac-DG's voltage.

 $E_{MIN}, E_{MAX}$  The ac-DG's minimum/maximum voltage.

 $Q_{MIN}, Q_{MAX}$  The  $ac ext{-DG}$ 's minimum/maximum reactive power.

 $M_{\rm dc-i}$  The  $j^{th}$  dc-DG's active power droop gain.

 $V_{\rm i}$  The i<sup>th</sup> dc-DG's voltage.

 $V_{MIN}$  The dc-DG's minimum voltage.

 $\hat{V}_{
m dc}$  Normalized voltage at the dc-side of the IC.  $\hat{\omega}_{
m ac}$  Normalized frequency at the ac-side of the IC.

 $a_{ij}$  Element of the adjacency matrix **A**.

 $\mathrm{d}_{i}$  Diagonal in-degree of the adjacency matrix A.

 $\mathbb{N}_{ac}$  Set of ac-DGs in the ac-microgrid.  $\mathbb{N}_{dc}$  Set of dc-DGs in the dc-microgrid.

 $\mathbb{N}_{IC}$  Set of ICs in the hybrid ac/dc-microgrid.

 $\delta\omega_{\rm D}$  Control action of ac-DG's active power droop controller.  $\delta E_{\rm D}$  Control action of ac-DG's reactive power droop controller.

 $P_{\text{ac-i}}, Q_{\text{ac-i}}$  Instantaneous real/reactive power generated by the i<sup>th</sup> ac-DG.

 $\psi_{i}$  Control action of frequency restoration controller.  $\psi_{ac-i}$  Control action of power consensus with  $ac ext{-DGs}$ .  $\psi_{dc-i}$  Control action of power consensus with  $dc ext{-DGs}$ .  $\sigma_{i}$  Time constant of frequency restoration controller.  $\chi_{i}$  Control action of  $ac ext{-voltage}$  restoration controller.  $\varrho_{i}$  Time constant of  $ac ext{-voltage}$  restoration controller.

 $\delta V_{\rm D}$  Control action of dc-DG's active power droop controller.

Positive gains of ac-voltage restoration controller.

 $\psi_{\rm i}$  Control action of dc-voltage restoration controller.  $\psi_{\rm dc-i}$  Control action of power consensus with dc-DGs.  $\psi_{\rm ac-i}$  Control action of power consensus with ac-DGs.  $\rho_{\rm i}$  Time constant of dc-voltage restoration controller.  $\gamma_{\rm i}, c_{\rm i}$  Positive gains of dc-voltage restoration controller.

 $P_{\text{IC}-k}^*$  Active power reference for the  $k^{th}$  IC.

 $\tau_k$  Time constant of IC controller.  $\mu_k, \vartheta$  Positive gain of IC controller.

 $\beta_{\rm i}, b_{\rm i}$ 

 $\mathbb{N}_{ac}$  Set of ac-DGs in the ac-microgrid.  $\mathbb{N}_{dc}$  Set of dc-DGs in the dc-microgrid.

 $\mathbb{N}_{IC}$  Set of ICs in the hybrid ac/dc-microgrid.

 $P_{\rm ac}^{\rm D}, P_{\rm dc}^{\rm D}$  Lumped loads modelled for the optimization problem.

 $P_{\text{ac-i}}^{\text{G}}$  Power generated by the i<sup>th</sup> ac-DG.  $P_{\text{dc-}j}^{\text{G}}$  Power generated by the j<sup>th</sup> dc-DG.  $P_{\text{IC-}k}^{\text{ac}}$  Power transferred by the k<sup>th</sup> IC.

 $P_{\text{IC}}^{\text{ac}}$  Total power transferred at the *ac*-side of the *ICs*.  $P_{\text{IC}}^{\text{dc}}$  Total power transferred at the *dc*-side of the *ICs*.

 $P_{\rm IC}^{
m LOSS}$  Total power lost in the ICs.

 $k^{\text{LOSS}}$  Percentage of the power lost in the IC.  $k_{\text{IC}}, k_{\text{IC}}^{\text{LOSS}}$  Parameters for power through the IC.  $\begin{array}{c} P_{ac}^{G} \\ P_{dc}^{G} \end{array}$ Set of power generated by the ac-DGs. Set of power generated by the dc-DGs.  ${
m P^{ac}_{IC}}$ Set of power through the *ICs*. Cost function of the  $i^{th}$  ac-DG.  $C_{\text{ac-i}}$ Cost function of the  $j^{th}$  dc-DG.  $C_{\mathrm{dc}-i}$ Parameters for the cost function of the  $i^{th}$  ac-DG.  $\{a,b,c\}_{ac-i}$ Parameters for the cost function of the  $j^{th}$  dc-DG.  $\{a,b,c\}_{dc-i}$  $P_{\mathrm{ac-i}}^{\mathrm{G-}}, P_{\mathrm{ac-i}}^{\mathrm{G+}}$ Limits for the power generated by the i<sup>th</sup> ac-DG.  $P_{\mathrm{dc}-j}^{\mathrm{G}-}, P_{\mathrm{dc}-j}^{\mathrm{G}+}$ Limits for the power generated by the  $j^{th}$  ac-DG.  $P_{\mathrm{IC}}^{\mathrm{ac-}}, P_{\mathrm{IC}}^{\mathrm{ac+}}$ Limits for the power through the  $k^{th}IC$ . Lagrange multiplier associated with the power balance constraint.  $\alpha_{ac-i}^+, \alpha_{ac-i}^-$ Lagrange multiplier associated with the power limits of ac-DGs.  $\alpha_{\mathrm{dc}-i}^+, \alpha_{\mathrm{dc}-i}^-$ Lagrange multiplier associated with the power limits of dc-DGs.  $\alpha_{\mathrm{IC}-k}^+, \alpha_{\mathrm{IC}-k}^-$ Lagrange multiplier associated with the power limits of ICs.  $\lambda_{i}^{\mathrm{ac}}$ Lagrange multiplier for the  $i^{th}$  ac-DG. Lagrange multiplier for the  $j^{th}$  dc-DG.  $\lambda_i^{\mathrm{dc}}$ Secondary control action for the  $i^{th}$  ac-DG.  $\Omega_{i}^{ac}$  $k_{\text{ac-i}}^{a,b,c,d,e}, \mu_{\text{ac-i}}^{a,b}$ Positive gains of the ac-DG's controller. Secondary control action for the  $j^{th}$  dc-DG.  $k_{\mathrm{dc}-j}^{a,b,c,\mathrm{d,e}},\mu_{\mathrm{dc}-j}^{a,b}$ Positive gains of the dc-DG's controller.  $\tau_{\rm IC}^{a,b}, \mu_{\rm IC}^{a,b}$ Positive gains of the *IC*'s controller.  $\lambda^{
m G}$ Lagrange multiplier associated with the power balance constraint.  $\lambda^{\mathrm{IC}}$ Lagrange multiplier associated with the power transfer constraint.  $\lambda_{ac-i}^G$ Lagrange multiplier for the  $i^{th}$  ac-DG. Lagrange multiplier for the  $j^{th}$  dc-DG.  $\lambda_{\mathrm{dc}-i}^{\mathrm{G}}$  $\lambda_k^{\rm IC}$ Lagrange multiplier for the  $k^{th}$  IC.  $\hat{P}_{\text{IC}-g}^{\text{ac}}$ Total power estimated at the ac-side of the ICs.  $\hat{P}_{\text{IC}-g}^{\,\text{dc}}$ Total power estimated at the dc-side of the ICs.  $\overline{k_{\rm IC}}$ Average value of the variables  $k_{\rm IC}$  received by the DGs. Average value of the variables  $\lambda^{\rm IC}$  received by the DGs.  $\overline{\lambda^{\rm IC}}$ Average value of the variables  $\lambda^{G}$  received by the ICs.  $\overline{\lambda^{\mathrm{G}}}$ Constant delay for experimental test #4.  $\tau_{
m d}$ Normalized frequency at the ac-side of the IC.  $f_{pu}$  $V_{pu}$ Normalized voltage at the dc-side of the IC.



## Chapter 1

## Introduction

The continuous improvement in power electronic technologies, the strengthening of microprocessors, and the capability to implement more complex control strategies have allowed the researchers to address environmental concerns and focus on a significant effort in the research of microgrids. In the literature, a microgrid is defined as an electrical system capable of integrating different distributed generators (DGs) and loads, which can use both alternating or direct current (i.e., ac or dc). Moreover, a microgrid can also incorporate variable energy sources such as renewable energies operating with energy storage systems (ESS) [1]. Electric vehicles (EVs) correspond to a particular type of load since they can be used to manage the loading in the microgrid, and it is possible to implement demand-side management strategies by controlling their charge profiles [2]. The DGs supply electricity to the local loads, reducing the transmission stage's power loss [3]. Although it is possible to operate a microgrid with medium-voltages, its application at a low-voltage level is preferred [4,5]. Therefore, a massive inclusion of microgrids at the distribution level is highly probable in the short term. The operation of these systems needs to be studied to guarantee the electrical system's efficiency, safety, and reliability [6].

The general topology for an ac-microgrid is presented in Fig. 1.1a [7]. Traditionally, an ac-microgrid is composed of ac-energy sources, which can be renewable (such as photo-voltaic panels (PVs) or wind turbines (WTs) interfaced through power converters) or non-renewable (such as diesel generators (DiGens)) energy-based DGs, and ac-loads connected through a 3-wire (or 4-wire) ac-bus. On the other hand, since the ac-microgrid can supply (at least a part of) the power demanded by the loads, it can work autonomously without a connection to the main grid [8]. Therefore, the microgrid can operate in "islanded" or "grid-connected" mode, based on the status of the switch (see Fig. 1.1a) [9]. The focus of this thesis is on the "islanded" mode of operation.

Since dc-power generation and dc-loads have had significant growth in recent years, dc-microgrids and dc-distribution networks have caught the attention of researchers [10–14]. The general topology for a dc-microgrid is presented in Fig. 1.1b. As described before for the ac-microgrid, a dc-microgrid is comprised of renewable energy-based DGs (e.g., PVs and WTs), ESSs and dc-loads (ac-loads may also be connected through ac-to-dc converters) connected through a dc-bus. On the other hand, the dc-microgrid can also operate in "islanded" or

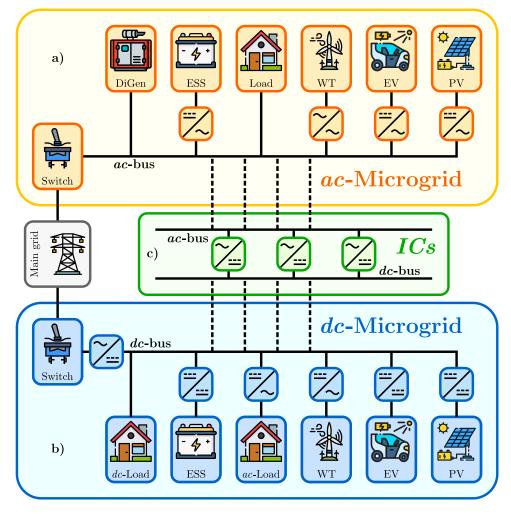


Figure 1.1: General topology for a hybrid ac/dc-microgrid. a) ac-microgrid. b) Interlinking converters. c) dc-microgrid.

"grid-connected" mode (through a dc-to-ac converter), based on the status of the switch. As mentioned before, the focus of this thesis is on the "islanded" mode operation.

Around the world, the distribution systems are mainly ac-distribution systems, and it is not economically (or even technically) possible to replace the existing ac-distribution systems with dc-microgrids, despite the enormous potential of the dc-microgrids. Therefore, it is more plausible to consider an electrical system that merges the two systems described before in the short or mid-term taking advantage of the benefits that each of them has [15]. In other words, it is possible to view the ac-microgrids and the dc-microgrids operating together, i.e., as a single entity, interfaced through one or more Interlinking Converters (ICs) as presented in Fig. 1.1c [16]. These systems are known as "hybrid ac/dc-microgrids", and their operation and control have become an essential topic for research [17–19]. This is the main topic for the research developed in this thesis.

The microgrid (ac, dc or hybrid ac/dc) is seen by the main grid as a single entity when operated in grid-connected mode due to having a single point of common coupling (PCC). The DGs do not have to regulate both the nominal amplitude and frequency of the voltages

in the microgrid since the main grid (stiff grid) sets those parameters. The DGs can control the exchange of active power (or reactive power in the ac-microgrid) between the main grid and the microgrid (i.e., the DGs act as "grid following" units) [20]. On the other hand, in an isolated microgrid (i.e., islanded mode operation), the DGs need to maintain the amplitude and frequency of the voltages within limits all the time (i.e., the DGs act as "grid forming" or "grid supporting" units). However, in the islanded mode, it is also possible to have "grid following" units.

Controlling the DGs in a hybrid ac/dc-microgrid (i.e., ac-DGs and dc-DGs working collaboratively) is more complex than when the microgrids are operating separately. In addition to controlling the internal variables and the interaction of the DGs within each microgrid, it is necessary to control the interaction of the DGs on both sides, that is, between ac-DGs and dc-DGs. Thus, the control of the power transfer through the IC is crucial for achieving the required operation of the hybrid ac/dc-microgrid [21]. Different objectives can be defined for the operation of the DGs. For example, one can mention active power-sharing among DGs on both sides based on power ratings of units [22,23], active power-sharing within microgrids and transferring power through the IC in case one microgrid is overloaded [24], to name a few. However, in all cases the hybrid ac/dc-microgrid is considered as 3 independent systems interacting with each other and not as a single entity.

Although the hybrid ac/dc-microgrid may have only one IC, the most general topology employs multiple ICs. In the former case, the most typical control strategy for the IC is to equalize the normalized frequency (at the ac-side) and voltage (at the dc-side) [15], However, this strategy is not as effective when the microgrid has many buses. On the other hand, in the latter case, additional controllers should be used since the voltage at the dc-side of the ICs is not necessarily equal at all the buses [22]. However, in most cases reported in the literature the secondary control is not considered.

Based on the hierarchical control approach [5], the secondary control loop can be implemented as a centralized controller, a decentralized controller or a distributed controller (see Fig. 1.2) [25, 26]. The most typical approach considers a central controller (centralized approach), which requires a communication link with all the agents in the microgrid, as shown in Fig. 1.2a. Therefore, this strategy is prone to single-point failures and its implementation is infeasible with a large microgrid since the computational burden is too high. The decentralized approach lacks a central controller and all the agents operate with local information (see Fig. 1.2b). Thus, it is hard to achieve collaborative objectives among the agents with this approach. Finally, the distributed approach does not require a central station for control, and agents work autonomously in a cooperative fashion to reach a global objective (see Fig. 1.2c). This approach is more robust since each agent needs to communicate only with its neighbouring agents [27].

Motivated by the aforementioned considerations, the main focus of this thesis is on developing a distributed secondary control strategy that considers the hybrid ac/dc-microgrid as a single entity and not as three independent systems interacting with each other. In addition to restoring the variables modified by the primary control, the proposed control strategy must be capable of assigning a secondary control objective to the DGs in the hybrid ac/dc-microgrid, such as sharing power between the DGs (and between the ICs) or minimizing

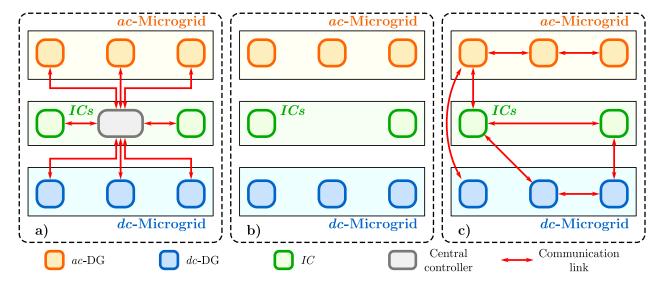


Figure 1.2: Secondary control types for microgrid control. a) Centralized. b) Decentralized. c) Distributed.

the total operating costs of the microgrid. In addition, the thesis will provide guidelines to obtain an analytical closed-loop model for the hybrid ac/dc-microgrid with the proposed control strategies, deriving the stable limits of the controllers and validating their tuning. Furthermore, several experimental and simulation tests will validate the performance of the control schemes.

The Ph.D. program of this candidate is developed under a Cotutelle Agreement (double-degree agreement) between the University of Chile, Chile, and the University of Waterloo, Canada.

### 1.1 Project Hypotheses

The main hypotheses that support this Ph.D. project are described below. Particularly, this thesis explores the implementation of distributed secondary control strategies applied to hybrid ac/dc-microgrids to regulate frequency, ac-voltage amplitude and dc-voltage magnitude, while augmenting the objective of the controller with either a power-consensus improvement or an operation cost minimization. The main feature of this strategy is to utilize a distributed communication network that considers the interlinking converters and to include them in the control scheme.

- A consensus-based distributed secondary control strategy could be designed and implemented in a hybrid ac/dc-microgrid in order to simultaneously regulate ac-voltage magnitude and frequency, as well as the dc-voltage magnitude, by including the interlinking converters in the control strategy.
- The controller could be augmented with either a power-sharing improvement or an operation cost minimization. This second control task could be performed by considering the information of the nearest neighbours from both sides of the microgrid, i.e., achieving a common consensus among ac-DGs and dc-DGs.

- The inclusion of an *IC* for the power/energy exchange between the *ac*-side and the *dc*-side of a hybrid *ac/dc*-microgrid in the distributed communication network could help the secondary control task and reduce the possibility of instability due to a failure on this device. To achieve the former, the *IC* should communicate with, at least, one DG from each sub-microgrid.
- Additionally, if the *IC* is considered in the communication network, it could be possible to automatically divide the secondary control strategy for the hybrid ac/dc-microgrid into two separate strategies. Separately, these strategies should be able to regulate the secondary variables within each microgrid and to perform the second objective: (i) independently equalize the power being generated by the ac-DGs and dc-DGs, normalized to the units' ratings, or (ii) re-dispatch the DGs to minimize the operation cost within each microgrid.
- If multiple ICs are considered in the distributed communication network, it could be possible to maintain the performance of the distributed secondary controller under contingency of one IC. Even though the control of the hybrid ac/dc-microgrid is more complex, the inclusion of multiple ICs helps to improve reliability and security of the electrical system.
- By considering multiple *ICs* in the distributed secondary control strategy and, hence, in the distributed communication network, it could be possible to control and to reduce the unwanted circulating currents in the *dc*-side of the *ICs*.
- The correct tuning of the distributed controllers proposed in this Thesis can be verified by the analysis of a closed-loop model of the hybrid ac/dc-microgrid. Moreover, the stable limits of the gains can be derived from this model.

### 1.2 Project Objectives

#### General Objective

This Ph.D. research project is aimed to study and investigate the design and implementation of consensus-based distributed secondary control strategies for hybrid ac/dc-microgrids, treating the microgrid as one electric entity and not as three independent systems (i.e., ac, dc and IC).

#### Specific Objectives

The research described in this thesis pursues to accomplish the following objectives:

- To design and implement a distributed secondary control strategy for achieving active power consensus among all the DGs in a hybrid ac/dc-microgrid, i.e., ac-DGs and dc-DGs.
- To validate (experimentally and with simulations) the effectiveness of the secondary control strategy for active power consensus.

- To design and implement a distributed secondary control strategy for minimizing (in real-time) the operation cost in a hybrid ac/dc-microgrid, including the losses in the ICs in the formulation of the optimization problem.
- To validate (with simulations) the effectiveness of the secondary control strategy for operation cost minimization.
- To obtain a small-signal closed-loop model for verifying the tuning and deriving the stable limits of the two proposed controllers. In addition, the stability of the controllers can be studied with the closed-loop model.
- To design and implement a scaled down prototype of a hybrid ac/dc-microgrid to validate the proposed control strategies in an experimental rig.

### 1.3 Contributions

The contributions of this work can be summarized as follows:

- Two coordinated distributed secondary control strategies for hybrid ac/dc-microgrids are proposed, which treat the hybrid microgrid as one electrical entity and not as three independent systems (i.e., ac, dc, and IC). The proposed strategies achieve seamless restoration of the variables modified by the primary control at both sides of the hybrid ac/dc-microgrid, while achieving either active power consensus (first strategy) or operation cost minimization (second strategy).
- The active powers transferred through the *ICs* are considered in the consensus functions and algorithms proposed in this work, which avoids circulating currents and achieves either an accurate power-sharing between the *ICs* (first strategy) or a loss minimization in the *ICs* (second strategy).
- The strategy achieves plug-and-play capability and robustness of the hybrid ac/dc-microgrid; power-sharing (first strategy) or cost minimization (second strategy) are achieved even if ICs (or DGs) are connected or disconnected.
- The viability and effectiveness of the proposed control strategies have been validated using a 24kW hybrid ac/dc-microgrid prototype. A simulation study is also considered for including several scenarios which cannot be validated using the lab prototype, including scenarios with multiple ICs.
- Two analytical models of the closed-loop systems for the hybrid ac/dc-microgrid with the proposed consensus-based secondary control strategies are developed, which permit small-signal stability analysis and parameter tuning for the two proposed strategies.
- The work developed during this project has resulted in the publication of 3 international conference papers [28–30] and 4 journal papers [26,31–33] submitted to top-tier indexed journals. In addition, 11 manuscripts have been published, with the candidate as a co-author [34–44]. The details of these publications and other contributions to papers related to microgrids are listed in Chapter 9.3.

### 1.4 Thesis Structure

The rest of this work is divided into nine Chapters, as follows:

Chapter 2 extensively reviews the secondary control strategies for hybrid ac/dc-microgrids proposed in the literature. Particular emphasis is placed on consensus-based distributed schemes. Furthermore, optimization techniques applied to hybrid ac/dc-microgrids are also addressed.

Chapter 3 proposes a secondary control strategy that simultaneously regulates ac-voltage magnitude and frequency, as well as the dc-voltage magnitude, by including the interlinking converters in the control strategy via a distributed consensus approach. This improves the power-sharing accuracy and secondary control restoration of the variables in both ac-and dc-sides of the microgrid.

Chapter 4 introduces a new secondary control strategy that, as the one described in Chapter 3, simultaneously regulates ac-voltage magnitude and frequency, as well as the dc-voltage magnitude, while the DGs and the ICs (if multiple ICs are available) are dispatched to minimize the operation cost of the hybrid ac/dc-microgrid.

Chapter 5 describes the design and construction of an experimental prototype of a hybrid ac/dc-microgrid composed of six dc-DGs, three ac-DGs and a single IC. The capabilities and characteristics of the control platform, power stage and power sources used to emulated the experimental system are detailed.

Chapter 6 presents two analytical closed-loop models of the proposed secondary control strategies proposed in this thesis, which are used to derive the stable limits for the controller's gains and verify their tuning. Moreover, a small-signal analysis is performed with the closed-loop models obtained in this Chapter, identifying the eigenvalues of the characteristic matrix and their participation factors.

Chapter 7 shows the experimental and simulation validation for the control strategy proposed in Chapter 3. A set of experimental tests for step-changes in the load, plug-and-play of DGs and communication issues, such as communication delays and communication failures, are included. A set of simulation tests for a microgrid with multiple *ICs* and performance comparison against other control schemes reported in the literature are included.

Chapter 8 displays the simulation validation for the control strategy proposed in Chapter 4. A set of simulation tests for enabling the control loops and step-changes in the load at both sides are included, for hybrid ac/dc-microgrids considering one or more ICs. Finally, a comparison test for the two secondary control strategies proposed in this thesis is depicted.

Chapter 9 summarizes the contents presented in this thesis, suggests possible directions for the future research on the topics addressed in this work, and presents a summary of the works published during this Ph.D. project, including manuscripts derived from the work in this thesis and other papers where the candidate has contributed.

## Chapter 2

## State of the Art

### 2.1 Introduction

In this Chapter, the state of the art regarding topologies and control strategies applied to microgrids is presented. The main control strategies and topologies applied to both ac- and dc-microgrids are presented. However, special attention is focused on hybrid ac/dc-microgrids considering Interlinking Converters (ICs) and Distributed Secondary Control (DSC) systems for hybrid ac/dc-microgrids.

The Chapter is organized as follows: a comprehensive background about hierarchical control strategies for ac-microgrids (Section 2.2), dc-microgrids (Section 2.3), and hybrid ac/dc-microgrids (Section 2.4) is presented. Section 2.5 shows the fundamentals of cooperative control of multiagent systems, which is the basis of the distributed secondary control strategies proposed in this work. Finally, Section 2.6 reviews the state of the art regarding DSC applied to ac-, dc-, and hybrid ac/dc-microgrids.

### 2.2 Control of *ac*-Microgrids

As mentioned in Chapter 1, the focus for this thesis is on the "islanded mode" operation of hybrid ac/dc-microgrids. Therefore, the DGs need to operate in coordination to supply the power demanded by the loads and ensure a good quality of the energy supplied. The most typical objective for the microgrid's control is to proportionally share the load power based on the nominal capacities of the DGs [45–48]. However, different functionalities have been proposed to coordinate the DGs, such as secondary variables restoration or power-sharing improvement [18]. Although these functionalities can be performed with a centralized control strategy, a fast communication network is required if the system's reliability has to be maintained. However, a centralized controller gets expensive and unfeasible as the number of DGs increases in the microgrid.

Droop control is the most commonly used control scheme to share the generated power among the DGs in a microgrid, which follows a decentralized approach relying solely on local measurements. Although this control strategy has a stable, reliable, and fast operation, it causes deviations of the microgrid variables (frequency and amplitude of the voltages in the ac-microgrids, and magnitude of the voltages in the dc-microgrids) from their nominal values, which is a significant drawback of this decentralized controller. Moreover, it is not possible to implement a higher level of coordination between the DGs since this control is based on local measurements only. Thus, in order to take advantage of the benefits of the decentralized and centralized (or distributed) schemes — i.e., low cost and high reliability of the former, and coordination flexibility of the latter — a hierarchical control structure should be considered.

In the hierarchical control structure, the control is separated in three main layers: the primary, the secondary and the tertiary control loops, as can be seen in Fig. 2.1 for a fully centralized approach (see Fig. 2.1a) and a distributed approach (see Fig. 2.1b). Firstly, the primary control loop corresponds to the decentralized droop control. Secondly, the secondary control loop which restores the secondary variables to their nominal values using a slow-communication-based scheme. Finally, the slower higher level tertiary control loop manages the energy exchange of the microgrid (mostly when it is operating in "grid-tied" mode); however, since the focus of this thesis is on "islanded" microgrids, this control level is not considered in this work. Additionally, an internal (or zero) control loop is needed to control the internal variables (current/voltage) of the DGs [49–51]. It is worth mentioning that the higher the control level is, the smaller the bandwidth becomes [5]. The hierarchical control loops for ac-microgrids are described in the following.

### 2.2.1 Primary Control of ac-Microgrids

The primary control loop for ac-microgrids is primarily responsible for setting the current and voltage at the output of the ac-DGs. Furthermore, it regulates the power-sharing among different ac-DGs in the ac-microgrid. The typical configuration for a power-electronic-interfaced ac-DG is presented in Fig. 2.2, which consists of a three-phase voltage source converter (VSC) and an LC-LCL filter connected to the ac-microgrid though an output impedance [52]. The primary controller comprises the inner control loop (current/voltage control loops) and the droop controller. Although a single voltage control loop is sufficient to control the output voltage in an ac-DG (e.g., a proportional-resonant controller is capable of controlling the

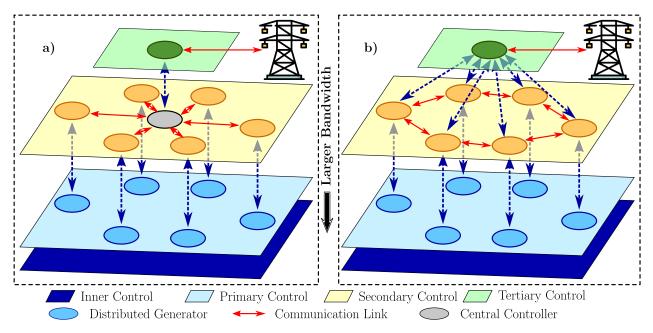


Figure 2.1: Hierarchical control structure for microgrids. a) Centralized. b) Distributed.

output voltage), it is recommended to include a nested control scheme with an inner (current) control loop and an outer (voltage) control loop. These controllers are described in the following.

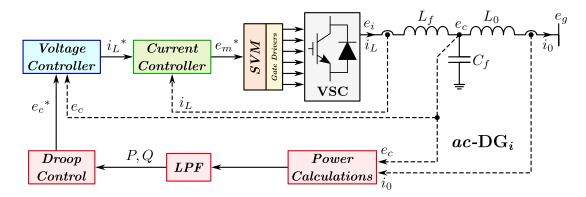


Figure 2.2: Primary control loop for the ac-DGs.

#### **Current Control**

A nested controller is preferred since the (inner) current control loop is able to limit the output current for protection purposes while controlling the current injected by the VSC. The current control loop is the fastest in the system, i.e., it has the largest bandwidth. It can be implemented using different types of controllers; however, the most commonly used in the literature are the Proportional-Integral (PI) controller and the Proportional-Resonant (PR) controller. Due to the oscillatory nature of the ac-voltage, the currents must be converted into the dq synchronous reference frame for the implementation of the PI-controller. On the other hand, the PR-controller can be implemented in either the abc natural reference frame or the  $\alpha\beta$  stationary reference frame.

Regarding the PI-controller implemented in dq reference frame, it is necessary to implement an additional negative-sequence controller to operate in parallel with the positive sequence controller. Furthermore, additional controllers (one for each harmonic frequency to be compensated) must be implemented for harmonic compensation when nonlinear loads are present. On the other hand, PR-controllers in  $\alpha\beta$  reference frame show some advantages over PI-controllers in dq reference frame. For example, both positive and negative sequence currents can be controlled at the same time with the PR-controller. Therefore, PR-controllers are used in this work. Fig. 2.3 shows the block diagram (see green blocks) of the current PR-controller in  $\alpha\beta$  frame. The general transfer function for a controller with a resonant frequency  $\omega_r$  is given by (2.1).

$$PR(s) = k_p + \frac{k_r s}{s^2 + \omega_r^2}$$
 (2.1)

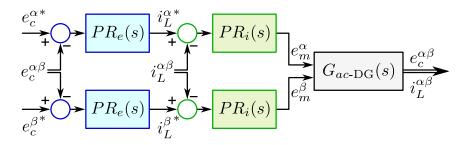


Figure 2.3: Inner control loop (voltage/current) for the ac-DGs.

#### Voltage Control

The voltage control loop is the outer loop in the nested controller, and its bandwidth must be at least seven times smaller than that of the current control loop to avoid coupling. The voltage controller is responsible for the VSC to follow the voltage reference by setting the reference currents for the current control loop.

As described for the current control loop, voltage controllers can be implemented either in dq or in  $\alpha\beta$  reference frames. In this case, the PR-controller is considered. Fig. 2.3 shows the block diagram (see blue blocks) of the PR voltage controller implemented in  $\alpha\beta$  reference frame, and the transfer function is shown in (2.1).

#### **Droop Control**

The droop control is used in the primary control level to ensure the power-sharing among the generation units [5, 45, 46, 48]. This stage has the smallest bandwidth within the local controller of the *ac-DG*. The power droop controller sets the voltage reference by defining both the voltage amplitude and frequency to the inner voltage controller based on the converter output active and reactive powers.

Traditionally, and considering that the ac-microgrid is composed of purely inductive lines, the droop control used in the  $i^{th}$  inverter to obtain an accurate active power-sharing  $(P_i)$  among the ac-DGs needs to modify the frequency of the voltages  $(\omega_i)$  emulating the mechanical/electrical behaviour of classical synchronous machines [53], as shown in Fig. 2.4a

for a generation unit (i.e., unidirectional active power). On the other hand, the conventional droop control of the i<sup>th</sup> inverter modifies the amplitude of the voltages  $(E_i)$  generated to share the reactive power  $(Q_i)$  among the generation units [48,54], as shown in Fig. 2.4b. The droop-slopes of the active power  $(M_{ac})$  and reactive power  $(N_{ac})$  droop-curves are given by (2.2).

$$M_{\rm ac} = \frac{\omega_{MIN} - \omega^*}{P_{MAX}}, \qquad N_{\rm ac} = \frac{E_{MIN} - E_{MAX}}{Q_{MAX} - Q_{MIN}}$$
(2.2)

where  $\omega^*$ ,  $E^*$  are the nominal frequency and voltage amplitude, respectively,  $\omega_{MIN}$ ,  $E_{MIN}$ ,  $E_{MAX}$  are the frequency and voltage amplitude limits, and  $P_{MAX}$ ,  $Q_{MIN}$ ,  $Q_{MAX}$  are the active and reactive power limits of the i<sup>th</sup> ac-DG. Thus, the conventional droop controllers for the i<sup>th</sup> ac-DG are described mathematically by:

$$\omega_{\rm i} = \omega^* + M_{\rm ac-i} P_{\rm i} \tag{2.3a}$$

$$|E_{\rm i}| = E^* + N_{\rm ac-i}Q_{\rm i}$$
 (2.3b)

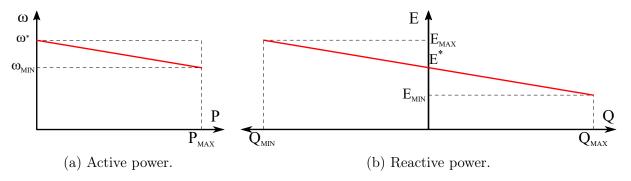


Figure 2.4: Droop curves for control of the ac-microgrid.

### 2.2.2 Secondary Control of ac-Microgrids

As mentioned before, the primary control loop (droop controller) is an efficient decentralized scheme to achieve proportional power-sharing among the ac-DGs since it is fast, reliable and only relies on local measurements [55]. Nonetheless, it produces deviations in the frequency and amplitude of the ac-voltages and an inaccurate reactive power-sharing among ac-DGs due to the differences in the output voltages and line impedances. Furthermore, it is impossible to implement higher-level coordination since the control system lacks a communication network. Therefore, the main objective for the secondary control level is to resolve the drawbacks mentioned above from the primary control level.

Since secondary control level is a higher-level control, its bandwidth is smaller than that of the primary control level; thus, lowering the communication network bandwidth requirements. Centralized, decentralized and distributed secondary controllers have been addressed in the literature [56–62]. However, since the focus of this work is on distributed control strategies, this structure will be thoroughly discussed in Section 2.5, while the others are briefly discussed in the following.

#### Frequency Restoration

As mentioned before, the main objective for the secondary control level is to restore the variables modified by the primary level control (droop controller). From the active power droop controller (2.3a), the frequency of the voltage of the  $i^{th}$  ac-DG will be adjusted when the active power demand changes to share the active power among the ac-DGs. This deviation in the frequency may cause issues in the operation of the ac-microgrid [63, 64]; thus, it is necessary to regulate the frequency and ensure the system is operating at the nominal frequency. Since the frequency of the ac-microgrid (in steady-state) is a global variable, the frequency restoration controller may use the measurement of the frequency in the microgrid at any arbitrary node.

The most typical secondary frequency controller is presented in Fig. 2.5 [65, 66]. The frequency is restored by vertically shifting the droop curve, as shown in Fig. 2.5b. Usually, a PI controller is used to determine the amount of the displacement of the curve, as shown in Fig. 2.5a.

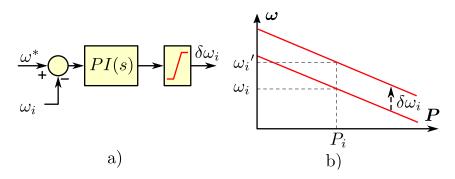


Figure 2.5: Secondary control loop for frequency restoration in the ac-DGs. a) Frequency restoration controller. b) Controller action.

#### Voltage Amplitude Restoration

From the reactive power droop controller (2.3b), the amplitude of the voltage in the i<sup>th</sup> ac-DG will be adjusted when the reactive power demand changes to share the reactive power among the ac-DGs. To avoid the undesired operation of the ac-microgrid, it is necessary to regulate the amplitude of the voltages [2,5].

A decentralized approach uses the local voltage of the ac-DG, i.e., the measured voltage at the output of the VSC. This controller is presented in Fig. 2.6 and it works shifting the droop-curve as in frequency restorer control [54, 63, 66]. However, since the local voltages are not equal at different points in the ac-microgrid, the voltage restoration is applied to the average voltage of the ac-microgrid in the centralized approach. On the other hand, the voltage restoration produces inaccurate reactive power-sharing among the ac-DGs, which can be controlled as follows.

In the literature, virtual impedances have been considered to improve the reactive power-sharing in *ac*-microgrids. In [63], the authors consider the implementation of a virtual impedance to share the active and reactive powers. However, it is not possible to do it accurately when the load is unbalanced or non-linear, although it is possible to share it with

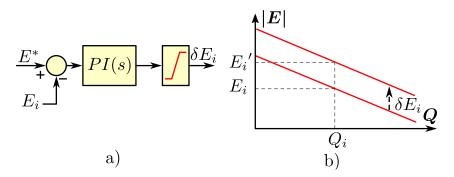


Figure 2.6: Secondary control loop for voltage restoration in the ac-DGs. a) Voltage restoration controller. b) Controller action.

a mismatch of the feeder impedance. The unbalanced load condition is addressed in [67] by an enhanced virtual impedance strategy, and the non-linear load condition is studied in [68]. The reactive power-sharing under unbalanced or non-linear load is achieved in [69]. However, any change in the microgrids topology (e.g., connection/disconnection of any load or feeder) may produce instability. A modification from the Q-E droop-curve to a Q-f droop curve was proposed in [70], which can be applied in case the lines in the ac-microgrid are highly resistive.

# 2.3 Control of dc-Microgrids

The structure for the control system of dc-microgrids is the same that the one described for ac-microgrids (see Section 2.2), i.e., the hierarchical control structure. Decentralized power-sharing is implemented in the primary control level by droop controllers, whereas the voltage regulation, power-sharing corrections, and/or optimization strategies are performed in the secondary control level. Tertiary control level is responsible for the energy exchange with external grids; however, since the focus for this thesis is on "islanded" microgrids, this control level is not considered in this work. The primary and secondary control loops for dc-microgrids are described in the following.

#### 2.3.1 Primary Control of dc-Microgrids

The primary control loop for dc-microgrids is primarily responsible for setting the current and voltage at the output of the dc-DGs. Furthermore, it regulates the power-sharing among different dc-DGs in the dc-microgrid. The typical configuration for a power-electronic-interfaced dc-DG is presented in Fig. 2.7, which consists of a dc-to-dc converter and an LC or LCL filter connected to the dc-microgrid through an output impedance [52]. As for the ac-microgrid, the primary controller comprises the inner control loop (current/voltage control loops) and the droop controller. These controllers are described in the following.

#### **Current Control**

As for the ac-microgrid, a nested controller is preferred since the (inner) current control loop is able to limit the output current for protection purposes while controlling the current injected by the dc-to-dc converter. The current control loop is the fastest in the system, i.e.,

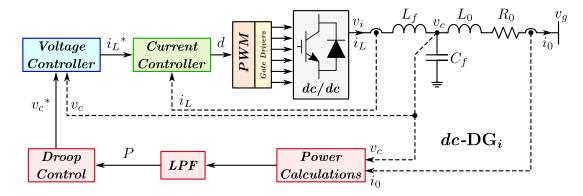


Figure 2.7: Primary control loop for a dc-DG.

it has the largest bandwidth. It can be implemented with several controllers; however, the most commonly used in the literature is the PI-controller. Fig. 2.8 shows the block diagram (see green block) of the current PI-controller, and its transfer function is given by (2.4). The current controller gains are set to allow stable operation with the largest possible bandwidth.

$$PI(s) = k_p + \frac{k_i}{s} \tag{2.4}$$

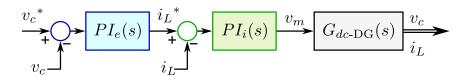


Figure 2.8: Inner control loop (voltage/current) for the dc-DGs.

#### Voltage Control

The voltage control loop is the outer loop in the nested controller, and its bandwidth must be at least ten times smaller than that of the current control loop to avoid coupling. The voltage controller is responsible for making the dc-to-dc converter follow the voltage reference by setting the reference current for the current control loop.

As described for the current control loop, for the voltage control loop a PI-controller is implemented. Fig. 2.8 shows the block diagram (see blue block) of the PI voltage controller implemented, and the transfer function is shown in (2.4).

#### **Droop Control**

As mentioned before, droop controllers are used in the primary control level to ease the power-sharing among the DGs. This stage has the smallest bandwidth within the local controller of the dc-DG. The power droop controller sets the voltage reference by defining its magnitude to the inner voltage controller based on the converter output active power.

Either current or power droop curves can be used in dc-microgrids to facilitate current or power-sharing between parallel dc-DGs, respectively. However, power droop control (P-V) is considered in this work, since the main focus is on hybrid ac/dc-microgrids which require

power-sharing or power dispatch between the two sub-microgrids. As for ac-microgrids, the droop controller gain is defined based on the operation limits of the dc-DG and the accepted voltage deviation. Fig. 2.9 shows the droop characteristics of generators in dc-microgrids with unidirectional power (ESS are out of the scope of this work), and the droop-slope ( $M_{dc}$ ) of the active power droop-curve is given by:

$$M_{\rm dc} = \frac{V_{MIN} - V^*}{P_{MAX}} \tag{2.5}$$

where  $V^*$  is the nominal voltage magnitude,  $V_{MIN}$  is the minimum voltage limit, and  $P_{MAX}$  is the active power limit of the i<sup>th</sup> dc-DG. Thus, the conventional droop controller for the i<sup>th</sup> dc-DG is described mathematically by:

$$V_{\rm i} = V^* + M_{\rm dc-i}P_{\rm i}$$
 (2.6)

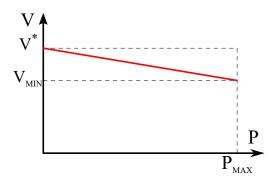


Figure 2.9: Droop curves for control of the dc-microgrid.

#### 2.3.2 Secondary Control of dc-Microgrids

As mentioned before, the primary control loop (droop controller) in dc-microgrids produces deviations in the magnitude of the dc-voltages and an inaccurate power-sharing among dc-DGs due to the differences in the output voltages and line resistances. Furthermore, same as for the ac-microgrid, it is impossible to implement higher-level coordination since the control system lacks a communication network. Therefore, the main objective for the secondary control level is to resolve the drawbacks mentioned above for the primary control level.

Since secondary control level is a higher-level control, its bandwidth is smaller than that of the primary control level; thus, the bandwidth of the communication network may also be small. Centralized, decentralized and distributed secondary controllers have been studied in the literature [11, 12, 71]. However, since the focus of this work is on distributed control strategies, this structure will be thoroughly discussed in Section 2.5, while the others are briefly discussed in the following.

#### Voltage Magnitude Restoration

From the power droop controller (2.6), the voltage in the  $i^{th}$  dc-DG will be adjusted when the power load changes to share the power among the dc-DGs. To avoid the undesired operation of the dc-microgrid, it is necessary to regulate the voltages [72,73].

A decentralized approach uses the local voltage of the dc-DG, i.e., the measured voltage at the output of the VSC. This controller is presented in Fig. 2.10 and it works by vertically shifting the droop-curve [74,75]. However, since the local voltages are not equal for different points in the dc-microgrid, the voltage restoration is applied to the average voltage of the dc-microgrid in the centralized approach. On the other hand, the voltage restoration produces inaccurate power-sharing among the dc-DGs, which can be controlled as follows.

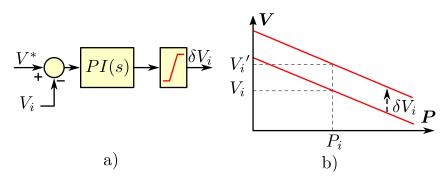


Figure 2.10: Secondary control loop for voltage restoration in the dc-DGs. a) Voltage restoration controller. b) Controller action.

The authors in [76] presented a low-bandwidth communication-based improved droop method for improving the power-sharing among dc-DGs in dc-microgrids with different line resistances. On the other hand, virtual resistances have been also proposed to improve the power-sharing in dc-microgrids [77–79].

# 2.4 Control of Hybrid ac/dc-Microgrids

As described before, a hybrid ac/dc-microgrid generally comprises an ac-microgrid and a dc-microgrid, which are interconnected through one or more bidirectional Interlinking Converters (IC) [15–17]. This topology facilitates the interconnection of various ac- and dc-based DGs and reduces several conversions due to the existence of independent ac- and dc-grids [18]. In the following, the basics of hybrid ac/dc-microgrid control and some relevant aspects of the ICs are detailed.

Hybrid ac/dc-microgrids is a research topic that has been mildly addressed in the literature. In this case, the power transferred through the ICs depends on the deviations of the primary variables (i.e., frequency on the ac-side and voltage on the dc-side) discussed before [6,15,21–24,76]. The most commonly used power controller for the IC is presented in Fig. 2.11. The power controller measures the voltage at the dc-side ( $V_{dc}$ ) and the frequency on the ac-side ( $\omega_{ac}$ ), which is obtained using a phase locked-loop (PLL) from the ac-voltage ( $E_{ac}$ ). Later, the secondary variables are normalized using (2.7) and compared. The error is processed with a PI controller and the output of the controller is the power reference for the IC ( $P_{IC}^*$ ).

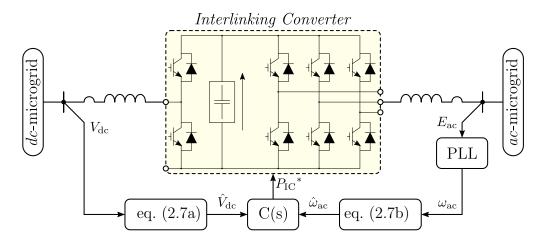


Figure 2.11: Closed-loop control system of the IC in hybrid ac/dc-microgrid.

$$\hat{V}_{dc} = \frac{V_{dc} - (V_{dc,max} + V_{dc,min})/2}{(V_{dc,max} - V_{dc,min})/2}$$
(2.7a)

$$\hat{\omega}_{ac} = \frac{\omega_{ac} - (\omega_{ac,max} + \omega_{ac,min})/2}{(\omega_{ac,max} - \omega_{ac,min})/2}$$
(2.7b)

In the literature, the main effort has been focused on the power management of the generation units at both sides (ac and dc) of the microgrid. In [16], the authors studied the operation of a hybrid ac/dc-microgrid with different energy sources, considering uncertainties and variability of the energy sources (e.g., wind and solar). Additionally, the voltage stability and the control of frequency and amplitude of the voltages was addressed. However, neither the secondary control loop nor the communication network was considered.

There are two main approaches proposed for active power management in hybrid ac/dc-microgrids. First, the power controller seeks to share the active power among all the generation units from both sides [21–23]. The work presented in [21] proposes the use of normalized droop curves on both sides of the IC. Thus, the active power reference for the IC control is generated to share the active power on both sides of the hybrid ac/dc-microgrid.

Second, in [24] a strategy to control the active power flowing through the IC from an underloaded microgrid to an overloaded microgrid is proposed. In that way, it is not possible to share the active power among generation units from different sides (i.e., the generation units only share the active power with units within the same microgrid). Thus, the IC can be "turned off" if both microgrids are underloaded or overloaded simultaneously. In these works, the secondary control loop is not considered, and the restoration of the voltage of the dc-side and/or the frequency of the ac-side produced by a secondary control loop would interfere with the accuracy of the power-sharing among both sides.

Depending on the size and topology of the hybrid ac/dc-microgrid, it may be necessary to consider the use of multiple ICs. However, due to the effect of line resistances in the dc-microgrid, the conventional dc-voltage based droop approaches used to manage the power

flow among ac- and dc-microgrids may produce a circulating current as well as stressing the ICs. To avoid that unwanted scenario, in [80] the authors propose to implement special droop characteristics to obtain the frequency and voltage reference values for the ac-side of ICs. In [81], the frequency of the ac-side and the voltage of the dc-side are considered to share the active power among the ICs. The introduction of a superimposed frequency in the dc-microgrid is considered in [82]. The secondary control loop is not considered in any of the aforementioned papers.

# 2.5 Cooperative Control of Multi-Agent Systems

In this work, techniques from distributed cooperative control of multi-agent systems (MAS) are applied to power-sharing problems in microgrids. This Section outlines the fundamental ideas used in the rest of the Thesis for developing design methods and analysis for cooperative control of MAS on graphs. Moreover, state of the art regarding distributed secondary control strategies applied to ac-, dc- and hybrid ac/dc-microgrids is addressed.

In MAS, each agent in a group or system can make decisions based on the information locally available from its closest neighbours, resulting in a synchronized behaviour of the overall group [83, 84]. The control protocols where each agent seeks to achieve consensus with its neighbours are considered distributed since they depend only on the local neighbour information as allowed by the communication graph topology. Thus, the agents in a microgrid (i.e., the DGs and ICs) need to communicate with their closest neighbours. The idea of a communication graph that models the information flows in a multi-agent group is now introduced.

#### 2.5.1 Adjacency Matrix

The communication network interconnecting a dynamical system (e.g., the hybrid ac/dc-microgrid) can be modeled as a graph with directed edges corresponding to the allowed flow of information between the agents. According to [85], it is possible to describe the distributed communication network by a weighted graph  $G(\mathcal{V},\mathcal{E},A)$  where  $\mathcal{V}=\{1,\ldots,n\}$  is a labeling of the DGs,  $\mathcal{E}\subseteq\mathcal{V}\times\mathcal{V}$  is the set of communication links, and  $\mathbf{A}$  is the  $n\times n$  adjacency matrix (or connectivity matrix) of the graph, with elements  $a_{ij}=a_{ji}\geq 0$  (in this case the graph is said to be undirected since it is bidirectional). Particularly, it is considered that  $(i,j)\in\mathcal{E}$  if node i sends information directly to node j, and in this case,  $a_{ij}>0$ . Thus, the sparsity pattern of the adjacency matrix  $\mathbf{A}$  encodes the topology of the communication layer (see example in Fig. 2.12).

As mentioned before, a graph can be represented by an adjacency matrix  $\mathbf{A} = [a_{ij}]$  with weights  $a_{ij} = 1$  meaning that units i and j can communicate with one another and  $a_{ij} = 0$  otherwise. It is possible to define the weighted in-degree of node i (d<sub>i</sub>) as the sum of i<sup>th</sup> row elements of  $\mathbf{A}$  as shown in (2.8) [84]. Moreover, the diagonal in-degree matrix  $D = \text{diag}\{d_i\}$  and the graph Laplacian matrix L = D - A can be defined [86]. These concepts will be used for obtaining the closed-loop model of the proposed controllers in Chapter 6.

$$d_{i} = \sum_{j=1}^{n} a_{ij} \tag{2.8}$$

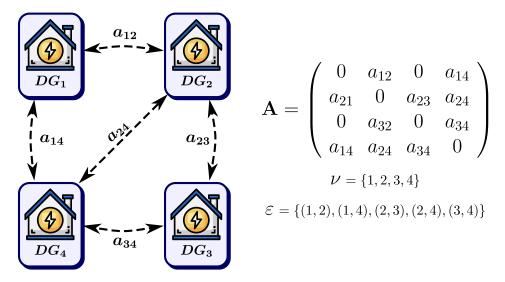


Figure 2.12: Example of adjacency matrix construction for four DGs.

The time response of the proposed distributed secondary control strategy is strongly related to the density of matrix **A**. The time response is slow when **A** is sparse, i.e., if most of the elements of **A** are zero, while the time response is fast if the density of **A** is high (most of the elements of **A** are equal to 1). Therefore, unlike the case of centralized or decentralized secondary control strategies, in this case the time response also depends on the size and topology of the communication network. Finally, a necessary condition for stability is that the **A** matrix must have a spanning tree, i.e., a path from any single node to any other one in the communication graph.

Now, two approaches for the consensus or synchronization problem are reviewed. First, the cooperative regulator (or leaderless consensus) problem, where all agents have the same role, is explained, which is the base for the secondary control strategies proposed for power-sharing in Chapter 3 and for cost minimization in Chapter 4. Then, the cooperative tracker (or controlled consensus) problem, where the agents synchronize to the dynamics of a leader or root node which generates a command target trajectory, which is used in the secondary controller presented in Chapter 4 for hybrid ac/dc-microgrids with multiple ICs.

## 2.5.2 Cooperative Regulator

Given dynamical systems at each node i with state  $x_i(t)$ , the objective for this approach is to find a distributed control protocol for each agent i that drives all states to the same constant steady-state value, which is known as a consensus value. The control scheme must be distributed in that the control for agent i is only allowed to depend on the state of agent i and its neighbours  $j \in N_i$  in the graph topology. Thus, the local control protocols for each agent i are:

$$u_{i} = \dot{x}_{i} = \sum_{j \in N_{i}} a_{ij} (x_{j} - x_{i})$$
 (2.9)

where  $a_{ij}$  is from the adjacency matrix **A** and describes the communication network in the hybrid ac/dc-microgrid.

In [84], it is demonstrated that the cooperative regulator protocol (2.9) guarantees consensus and the node states come to the same steady-state values  $x_i = x_j = c, \forall i, j$ , namely consensus value, given by:

$$c = \sum_{i=1}^{N} p_i x_i(0) \tag{2.10}$$

where  $w_1 = [p_1, \dots, p_N]^T$  is the normalized left eigenvector of the Laplacian L for  $\lambda_1 = 0$ . Finally, consensus is reached with a time constant given by (2.11), with  $\lambda_2$  the second eigenvalue of L, known as the Fiedler eigenvalue.

$$\tau = \frac{1}{\text{Re}\{\lambda_2\}}\tag{2.11}$$

#### 2.5.3 Cooperative Tracking

For this approach, we consider the situation shown in Fig. 2.13, which depicts a leader or target node with state  $x_0(t)$ . The objective here is to find a distributed control protocol for each agent i that drives all states to the state of the leader node. The following distributed cooperative tracker protocols are considered to solve this problem:

$$u_{i} = \dot{x}_{i} = \sum_{j \in N_{i}} a_{ij}(x_{j} - x_{i}) + g_{i}(x_{0} - x_{i})$$
 (2.12)

where the gains  $g_i \ge 0$  are known as pinning gains,  $g_i > 0$  if agent i has a direct communication link with the leader node and  $g_i = 0$  otherwise.

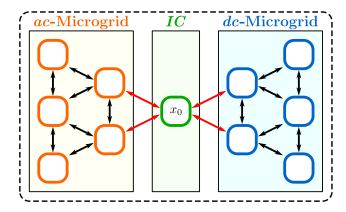


Figure 2.13: Communication graph with leader node  $x_0$ .

## 2.6 Distributed Secondary Control in the Literature

In the following, the main works reported in the literature regarding DSC strategies applied to ac-, dc- and hybrid ac/dc-microgrids are discussed. The distributed control systems reported in this section have been classified considering the type of microgrid studied, i.e., ac-strategies, dc-strategies and hybrid ac/dc-strategies.

#### 2.6.1 DSC for ac-Microgrids

As discussed before, the secondary control loop for ac-microgrids is in charge of restoring amplitude and frequency of the voltages to their nominal values. It must be noted that the frequency is a global variable of the microgrid. On the contrary, the voltage is a local variable (i.e., different values of voltages could be obtained, in steady-state, at different points of the microgrid). Therefore, different criteria can be considered for achieving voltage regulation in the microgrid, for example, by (i) regulating the converters' output voltage close to the nominal value, (ii) regulating the average voltage of the microgrid, or (iii) regulating the voltage in some specific points of the microgrid.

Different DSC strategies for ac-microgrids have been proposed in the literature. The authors in [87, 88] propose to use the average values of voltage magnitude and frequency to improve the primary control droop characteristics. Other works, based on techniques from the cooperative control of MAS, have proposed secondary control schemes that ensure asymptotic convergence of the controlled variables [85, 89–92]. A secondary control system based on an input-output feedback linearization approach was proposed in [89, 91]. The proposed methodology allows a non-linear formulation to be solved by conventional asymptotic consensus protocols. The non-linear relationship is developed for the voltage loop based on [49], and the feedback linearization relies on Lie's algebra. The methods presented in [91] inspire further developments in secondary control [85, 90].

In [85], the authors proposed a simplified methodology for the application of distributed cooperative secondary control over traditional droop based converter-based microgrids. The strategy considers a distributed averaging algorithm to restore the voltage amplitude and frequency while maintaining the active and reactive power-sharing. This controller acts over primary droop control schemes by applying an integral control with a consensus protocol referred to as Distributed-Averaging Proportional-Integral (DAPI) controller. The DAPI system is the basis of the control strategies proposed in this work, and it is further explained as follows:

Firstly, the DAPI frequency controller is proposed for restoring the frequency in the acmicrogrid and to ensure an accurate active power-sharing among units:

$$\omega_{\rm i} = \omega^* - m_{\rm i} P_{\rm i} + \Omega_{\rm i} \tag{2.13}$$

$$k_{i} \frac{\mathrm{d}\Omega_{i}}{\mathrm{d}t} = -\left(\omega_{i} - \omega^{*}\right) - \sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{ij} \left(\Omega_{i} - \Omega_{j}\right)$$
(2.14)

where  $\Omega_i$  is the secondary control variable,  $k_i$  is a positive gain for adjusting the transient response of the controller,  $\omega_i$  is the frequency and  $P_i$  is the active power of the i<sup>th</sup> ac-DG, and  $a_{ij} = a_{ji}$  is the element of the matrix **A** described in Section 2.5.1. The standard droop controller with the additional secondary control input  $\Omega_i$  is shown in (2.13), while (2.14) ensures the frequency restoration since, in steady-state, the derivative on the left side of the equation is zero and, then,  $\omega_i = \omega^*$ . Additionally, the condition  $\Omega_i = \Omega_j$  has to be fulfilled for all the ac-DGs i,  $j \in \mathbb{N}_{ac}$  to guarantee that all droop curves are equally shifted. The latter condition ensures that the active power-sharing is maintained.

Secondly, the DAPI voltage controller is proposed to restore the voltage amplitude in the

microgrid and to improve the reactive power-sharing among units:

$$E_{\rm i} = E^* - n_{\rm i}Q_{\rm i} + e_{\rm i} \tag{2.15}$$

$$\kappa_{i} \frac{\mathrm{de}_{i}}{\mathrm{d}t} = -\beta_{i} \left( E_{i} - E^{*} \right) - \sum_{j \in \mathbb{N}_{ac}} b_{ij} \left( \frac{Q_{i}}{Q_{i}^{*}} - \frac{Q_{j}}{Q_{j}^{*}} \right)$$

$$(2.16)$$

where  $e_i$  is the secondary control variable,  $\beta_i$  and  $\kappa_i$  are positive gains for adjusting the transient response of the controller,  $E_i$  is the i<sup>th</sup> voltage and  $Q_i^*$  is the i<sup>th</sup> reactive power rating, and  $b_{ij} = b_{ji}$  is the element of the adjacency matrix of a communication network **B** described in [85]. The equation (2.15) corresponds to the standard voltage droop controller with the additional secondary control input  $e_i$ , while (2.16) allows to choose between controlling the local voltage  $E_i$  (**B** = 0), the reactive power-sharing ( $\beta_i$  = 0) or a trade-off between both controls (**B**  $\neq$  0,  $\beta_i$   $\neq$  0). This trade-off between control of the voltage and reactive power-sharing is well-known and previously studied in the literature [93].

#### Approaches for Power-sharing and Variables Restoration

Other approaches have been proposed for improving the power-sharing while the secondary variables are restored [94,95]. In [94], a fully distributed control methodology for secondary control of ac-microgrids is proposed. The authors claim that the proposed scheme maintains the average voltage of the microgrid at the rated value while the Q-V droop coefficients are fine-tuned to mitigate any reactive power mismatch. Furthermore, the authors state that, unlike most conventional methods, the proposed controller does not utilize any explicit frequency measurement to regulate all inverter frequencies at the nominal value while sharing the active power demand among them. The proposed scheme estimates the average normalized active power using a dynamic consensus protocol and, accordingly, regulates the microgrid's frequency and shares the active power demand proportional to inverters' rating. Plug-and-play capability, and resiliency to different communication topologies and constraints such as limited bandwidth, delay, and packet loss, are verified through experiments. In [95], dynamic weights are reassigned to reach different targets. It is claimed that the strategy discussed in [95] could enhance the stability of the system, achieving a better dynamic response. The authors in [96] propose a distributed strategy to improve only the reactive power-sharing.

Several variations and modifications to the distributed secondary control algorithms have been proposed and studied. For instance, in [97–99], techniques of predictive control are utilized to restore the frequency and voltage amplitudes to nominal values. Another type of modification in the control algorithms is developed in [100–103], where finite-time consensus control is employed to restore both frequency and voltage in the microgrid. The proposed algorithms are designed to achieve the restoration of secondary variables (frequency and voltage) in a finite time. It is also claimed that these control strategies increase the convergence speed and the robustness against noise and uncertainties. However, these topics are outside the scope of this work.

#### Approaches for Economic Dispatch

The conventional centralized dispatch problem can be solved in a distributed manner. In this sense, it should be highlighted that in contrast to the centralized approach, distributed algorithm achieve the minimum cost by considering the communication between DGs. The most typical approach for minimizing the cost of the microgrid is considering the incremental cost consensus (ICC) [104–108].

In [104], a multiagent based distributed method is proposed to minimize operation cost of the ac-microgrid. Each ac-DG acts as an agent which regulates the power individually using proposed frequency scheduling method. Optimal power command is obtained through a consensus algorithm with only light communication between neighbouring agents. The authors in [105] propose a method similar to that proposed in [104], however, the finite-time convergence properties for distributed secondary frequency and voltage controllers are demonstrated. The limits for the operation of the ac-DGs are not considered in the aforementioned papers.

Delay effects and communication issues are thoroughly studied in [106,107]. The impact of time delays on the stability of the distributed economic dispatch algorithm is carefully studied in [106], and the generalized Nyquist criterion is considered to derive the maximum allowable delay bounds for the strategy presented in [106]. The effectiveness of the proposed algorithm and a verification of the correctness of the theoretical results are validated through simulation tests. In [107], the authors claim that whether the communication delays exist or not, the control protocols always guarantee that the output voltage amplitude and frequency of each distributed generator track to the reference values and maintain the optimal active power-sharing and the accurate reactive power-sharing properties. The plug-and-play capability is also validated in [107].

The power limits of the ac-DGs are considered in the strategies proposed in [108, 109]. Here, the authors propose a distributed control strategy for frequency control, congestion management, and optimal dispatch in isolated microgrids. The control scheme avoids lines overloading in the microgrid with a distributed approach, while restoring the secondary variables and optimizing the operation of the microgrid. The authors in [108] also derive a closed-loop model of the microgrid with the proposed scheme, in order to perform an eigenvalue analysis and small-signal stability analysis. Furthermore, the closed-loop model of the system is used to derive the stable limits for the parameters of the controller.

#### Approaches for Power Quality Issues

The loads in a low-voltage ac-microgrid are typically unbalanced and non-linear with several connections and disconnections of single-phase loads, which can produce power quality issues in the ac-microgrid. Thus, the DSC has been extended for improving the power quality in ac-microgrids by considering imbalance and harmonics issues. Two approaches have been addressed in the literature. In the first approach, the strategy aims to improve the unbalanced and distorted currents-sharing among the ac-DGs [110,111]. Conversely, the second approach is focused on compensating the voltages at some buses in the microgrid (buses where critical loads or more sensitive systems are connected) [112].

In [93], a distributed dynamic consensus algorithm to improve the negative sequence components current-sharing while enhancing the voltage quality at the PCC is proposed. This strategy is based on the symmetrical components theory. Therefore, to share the negative sequence component of the current among the ac-DGs, a negative sequence component of the voltage is included in the reference voltage to be synthesized by the i<sup>th</sup> ac-DG. This proposal

ensures an accurate imbalance sharing. The ac-microgrid considered in [93] corresponds to two 3-leg converters in parallel configuration feeding an unbalanced load. Experimental results are provided that validate the proposal. However, it is not discussed how to extend the proposal for a more complex microgrid configuration. Moreover, it is challenging to implement the reported methodology in a microgrid with more than two ac-DGs.

#### Approaches for Energy Storage Systems

Distributed secondary control strategies have been proposed for equalizing the state of charge (SoC) of battery energy storage systems (BESS) in ac-microgrids [113, 114]. In [113], the authors use frequency scheduling instead of adaptive droop gain to regulate the active power of the distributed energy storage (DES). Each DES unit is taken as an agent and it schedules its own frequency reference given by the real power droop controller according to the SoC values of all other DES units. Each agent uses a dynamic average consensus algorithm to obtain the average SoC value of DES. On the other hand, the scheme proposed in [114], achieves power regulation by adjusting the virtual resistances of voltage-controlled inverters with an autonomous current-sharing controller. However, BESSs are not considered in this work.

Although several alternatives for distributed secondary control in ac-microgrids have been analyzed, none of the previous works discussed in this section considers the existence of either the dc-side or the IC in a hybrid ac/dc-microgrid. Next, the works proposed for dc-microgrids are reviewed.

#### 2.6.2 DSC for dc-Microgrids

As discussed before, the secondary control loop for dc-microgrids is responsible for restoring magnitude of the voltages to their nominal values. As mentioned before, since the voltage is a local variable, the same criteria discussed for ac-microgrids can be defined for dc-microgrids, i.e., by regulating (i) the converters' output voltage, (ii) the average voltage of the microgrid, or (iii) the voltage at some specific points of the microgrid. The secondary control loop in dc-microgrids can include an additional control objective. The most common goal for the secondary control loop is to improve the power-sharing among dc-DGs while restoring the average voltage to its nominal value. Different approaches are reviewed in the following.

#### Approaches for Power-sharing and Variables Restoration

As mentioned before, the main challenge in this strategy is to restore the average voltage of the dc-DGs without deteriorating the power/current sharing. However, it is not possible to simultaneously achieve a good voltage regulation and an accurate power-sharing between the dc-DGs (i.e., the problem is similar to the Q-V problem in ac-microgrids). An optimal control strategy is proposed in [115] to solve the problem of optimal voltage and power regulation for dc-DGs with a distributed communication network. Here, the authors show the good performance of the proposed strategy with variable and distributed generation. However, this strategy requires the full information of the microgrid; otherwise, the optimal control is reduced to several controls, including conventional droop control.

A distributed approach is presented in [116–118]. In these works a distributed-adaptive

droop mechanism is proposed for secondary/primary control of dc-microgrids. The controller on each agent exchanges data with only its neighbouring agents on a sparse communication graph spanned across the microgrid. To achieve a good voltage regulation and an accurate current/power sharing, this control strategy can be divided in two main stages or modules: (i) a voltage regulator based on an observer for estimating the average voltage in the microgrid, and (ii) a current regulator to fine-tune the droop coefficients for different load conditions. In [28], a simplified version of the controller proposed in [116] was presented. This strategy utilizes power-sharing consensus instead of using a current-sharing consensus, and the voltage regulation is based on measured voltages instead of estimated voltages, which simplifies the controller.

A DSC applied for voltage regulation and droop slope correction is discussed in [119,120]. The controller is proposed to modify the droop slope in order to change the output impedance in each converter, to achieve load current sharing. In [121], a low voltage dc-microgrid with merged ac and dc characteristics has been considered to propose an alternative droop scheme for low-voltage dc-microgrids with both primary power-sharing and secondary voltage regulation merged. Two droop expressions are proposed in this work. The first expression is for regulating the ac-frequency and active power generated by the dc-DGs, while the second one is for relating the dc-voltage to the power term. It is claimed in [121] that better active power-sharing and proper average voltage regulation in the dc-microgrid are achieved.

#### Approaches for Economic Dispatch

The ICC approach for ac-microgrids discussed in the previous section can be extended to dc-microgrids, where a consensus algorithm is considered to achieve equal incremental cost in all the generating units. The distributed economic dispatch (ED) of dc-microgrids, unlike the distributed ED of ac-microgrids, modifies the voltage droop control scheme. In this context, the ED is solved at the same time that the global average voltage is restored [122, 123].

In [122], the ED is achieved by modifying the voltage reference from the droop controllers for the dc-microgrid through a PI controller, which modifies the output power of the  $i^{th}$  dc-DG to be equal to the optimal output power. The optimal power is obtained using an ICC algorithm similar to that discussed for the ac-microgrid. However, this work has some limitations: the power limits for dc-DGs are not considered. Moreover, this strategy only regulates the local output voltage of each dc-DG instead of the global voltage of the microgrid, not being able to guarantee the optimal operation. In [123], the global voltage regulation issue is addressed, and the distributed consensus technique is used for ED and voltage control of the microgrid. The voltage reference for the local controller is modified by adding voltage deviations to the reference voltage. Unlike [122], the works reported in [123,124] include the operation limits for the active power generated by the dc-DGs.

In [125, 126], a distributed adaptive droop control algorithm is proposed for optimal dispatch and current regulation by applying a consensus algorithm. In these works, an ED problem is considered to obtain the incremental cost considering power losses, where a penalty term is added to the cost function. The transmission losses are approximated by the square of the output power of each generating unit. Although this penalty term is added for considering the transmission losses in the cost function, the power-losses are not modelled in [125]. On the other hand, the losses are assumed constant in [126].

Several works include time delay analysis in their proposed consensus algorithms to evaluate their performance in this scenario. As reported in [127], time delays affect the convergence and performance of consensus algorithms. Thus, in [106, 122, 128], the effects of a constant communication delay on the ED problem are studied using simulation work, while in [127], time-varying delays are analyzed. Finally, uncertainties have also been addressed in the literature: for instance, in [129] the authors proposed a distributed controller for economic dispatch with random wind energy power.

#### Approaches for Energy Storage Systems

DSC strategies to achieve SoC equalization among the energy storage units in dc-microgrids have been reported in the literature [130, 131]. Besides the typical tasks of the secondary control system (i.e., voltage restoration and power-sharing), in [130], the charge and discharge of the batteries is monitored, and the SoCs are equalized, simultaneously, by using a distributed controller which regulates the BESS droop coefficients. Alternatively, adaptive virtual impedances adjusted using distributed control algorithms can be utilized to achieve SoC equalization among the BESS located in a microgrid (see [114]). It is claimed that virtual impedance-based methods are intrinsically more stable because droop variations can affect the stability of ac- and dc-microgrids [114]. In [131], ultracapacitors are included in the BESS, which posses two time-scales, one for the batteries (slower) and the other for the ultracapacitors (faster). In [132], a feedback linearization technique is used to obtain a second-order consensus strategy of the voltage applied to equalize the SoC of a BESS. The strategy proposed in [133] balance the SoC of the BESS while optimizing the operation of the microgrid.

Although several alternatives for distributed secondary control in dc-microgrids have been analyzed, none of the previous works discussed in this section considers the existence of either the ac-side or the IC in a hybrid ac/dc-microgrid. Next, the works proposed for hybrid ac/dc-microgrids are reviewed.

## 2.6.3 DSC for Hybrid ac/dc-Microgrids

To the best of the candidate's knowledge, the study of distributed secondary control strategies for integrating the secondary control loop of both sides in the hybrid ac/dc-microgrid has been slightly addressed in the literature. Conversely, the secondary control loop on each side is applied independently of the other side and the operation of the IC depends on the secondary control variables, i.e., the power-sharing among both microgrids might be inaccurate if the secondary control variables are restored to their nominal values. Moreover, the optimal operation of hybrid ac/dc-microgrids as a single entity has not been addressed.

A distributed control scheme for reliable autonomous operation of a hybrid three-port ac/dc/DES microgrid is presented in [23]. First, the authors consider a fully decentralized control, which is achieved by local power sharing (LPS) in individual ac- or dc-microgrid, global power sharing (GPS) throughout ac- and dc-microgrids, and storage power sharing (SPS) among distributed storage units. Upon fully decentralized control, each power module can operate independently without communication links. This benefits riding through communication malfunction in multilayer supervision control system. Secondly, a multilevel

power exchange control for scheduling LPS, GPS, and SPS is considered to reduce unnecessary power exchange between ac- and dc-microgrids and operations of DES units with the benefit of reducing power exchange losses and prolonging storage lifetime.

In [134, 135], the authors proposed a distributed coordination control strategy for the hybrid ac/dc-microgrid. In [134], it is claimed that this control strategy regulates not only dc current and reactive power-sharing among DGs in ac- and dc-microgrids but also maintains power-sharing among two microgrids and restores the ac-frequency and dc-voltage to their nominal values. The proposed control strategy is based on a distributed consensus algorithm. In [135], the goal is to implement power-sharing among all DGs in the hybrid ac/dc-microgrid and among the ICs in a distributed manner, without the need for additional proportional-integral controllers. However, in [134,135], the control of the ICs is based on the normalized droop curves of the ac- and dc-microgrids [21]. This behaviour is jeopardized when restoration is applied to the frequency and dc-voltage.

The integration of a global DSC strategy in DGs at both sides of the microgrid has been addressed in [28]. The authors propose a distributed control that ensures the regulation of the frequency (at the ac-side) and the amplitude (at both sides) of the voltages. Additionally, with the proposed strategy, all the DGs achieve an accurate power-sharing. The results obtained using simulation are presented and discussed in [28] to validate the capability of the proposed scheme to transfer power from the dc-side to the ac-side (and vice-versa), and its plug-and-play capability. However, in this work, the IC is not considered in the secondary control strategy.

In conclusion, there is still room for research on this topic since all proposed control strategies have drawbacks and they do not consider a secondary distributed control approach to integrate the secondary variables restoration of both sides in a hybrid ac/dc-microgrid as a single entity.

## 2.7 Summary

In this Chapter, the hierarchical control structures of ac-, dc-, and hybrid ac/dc-microgrids were presented. Deviations in secondary variables (frequency and voltage magnitude deviations in ac-microgrids, as well as voltage magnitude deviations in dc-microgrids) produced by the primary control level (decentralized droop controllers) are regulated by considering a higher level control, namely secondary control level. Moreover, dc-microgrids implementing P-V droops suffer from inaccurate power-sharing due to different line resistances and terminal voltages of dc-DGs. Similarly, inaccurate reactive power-sharing among droop-controlled ac-DGs in an ac-microgrid is obtained.

Secondary controllers use slow communication links to restore the secondary variables, while attaining additional objectives such as improving power-sharing accuracy and power quality (in ac-microgrids), equalization of SoC in BESS, or optimal operation of the DGs in the microgrids. Distributed controllers are more suitable for microgrids with a high number of DGs than centralized and decentralized schemes since the reliability and security of the microgrid improves when considering these types of architecture. However, in the literature, hybrid ac/dc-microgrids are typically considered as three independent systems interacting

with each other when implementing the secondary control loop.

In hybrid ac/dc-microgrids, a crucial device is the IC, which is a power electronics device capable of interconnecting the ac-microgrid with the dc-microgrids. The ICs must handle bidirectional power flow in order to transfer power from one side to the other one, in order to fulfill a goal such as avoiding the overloading of a microgrid or reducing the total operation cost. When the hybrid ac/dc-microgrid has several ICs it is necessary to regulate the power through them.

# Chapter 3

# Control Strategy Proposed for Power-Sharing

#### 3.1 Introduction

In this Chapter, the consensus-based distributed secondary control (DSC) strategy proposed for power-sharing among all agents in the ac/dc-microgrid is presented. In the DSC scheme proposed in this Chapter, each DG will achieve real power-sharing with all other DGs of the hybrid microgrid, i.e., ac-DGs and dc-DGs, and each ac-DG will approximately achieve reactive power-sharing with all other ac-DGs.

The main challenge for this Chapter is to design a control strategy applied to hybrid ac/dc-microgrids (ac/dc-DSC) that restores the secondary variables to their nominal values, and improves (or, at least, not degrade) power-sharing among DGs. Moreover, the control system has to identify when the IC is not available because, in that case, it is not possible to transfer power between the two sides of the hybrid ac/dc-microgrid (i.e., there is no electrical path for transferring power).

This Chapter is organized as follows: Section 3.2 presents a distributed secondary control strategy for power-sharing in a hybrid ac/dc-microgrid. The inner, primary and secondary control loops for the ac-DGs and dc-DGs are presented in Section 3.3 and Section 3.4, respectively. The controllers for the ICs are depicted in Section 3.5 for the case where a single IC is connected to the microgrid, and in Section 3.6 when multiple ICs are transferring power between the two sides of the microgrid. Finally, Section 3.7 summarizes the control strategy proposed for power-sharing among all the agents in the ac/dc-microgrid.

## 3.2 Control Strategy for Power-Sharing

In this strategy, the standard power-voltage (P/V) droop controller [72] is considered as the primary control system in the dc-microgrid, and the standard (P/f) and (Q/V) droop controllers [60] are used as the primary control systems in the ac-microgrid. As the frequency is a global variable in the ac-microgrid, accurate active power-sharing is achieved in the acmicrogrid. On the other hand, the amplitude of the voltages is a local variable. Thus, it is not possible to achieve both accurate power-sharing and voltage restoration simultaneously for each DG on either side of the microgrid [85].

A hybrid ac/dc-microgrid is considered in this section, which consists of ac-DGs, dc-DGs, and ICs, and the sets of these devices are labelled as  $\mathbb{N}_{ac} = \{1, \dots, n\}$ ,  $\mathbb{N}_{dc} = \{n + 1, \dots, n + m\}$ , and  $\mathbb{N}_{IC} = \{n + m + 1, \dots, n + m + g\}$ , respectively. The general topology for the hybrid ac/dc-microgrid is shown in Fig. 3.1. A global secondary control strategy for hybrid ac/dc-microgrids should restore the secondary variables on both sides of the microgrid; moreover, should ensure power-sharing between all ac-DGs and dc-DGs. To achieve the latter objective, the power flowing through the IC must be adjusted. In the following, several distributed control mechanisms for achieving these goals are introduced. These control laws will use peer-to-peer communication among dc-DGs, ac-DGs, and ICs, as described in Section 3.1. Even though the control strategy considers communication links between the units, a generalization of a communication channel is considered in this work, i.e., the thesis does not consider specific communication links (such as wireless systems, fibre optic communication links, among others).

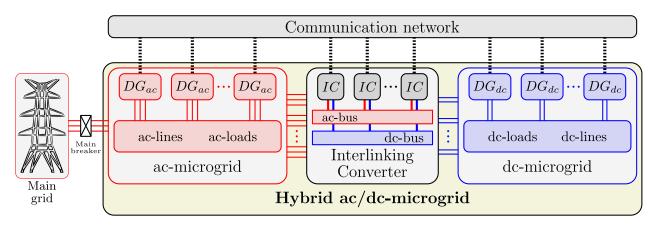


Figure 3.1: General topology of a hybrid ac/dc-microgrid. If the main breaker is closed, it is a "grid-tied" microgrid; otherwise, it is an "islanded" microgrid.

# 3.3 Control Strategy Proposed for ac-Microgrid

## 3.3.1 Inner and Primary Control Loops

In ac-microgrids, three-phase voltage source converters (VSCs) are the most commonly used power electronic devices used to interface the ac-DGs with the network. In this case, the voltage-controlled VSCs (VC-VSCs) are considered due to their capability to operate in droop controlled mode, i.e., to synthesize voltages with variable frequency and amplitude. The structure and control loops for a droop controlled VC-VSC are depicted in Fig. 3.2. A low-pass LC filter is considered to interface the ac-DGs with the ac-microgrid.

A nested structure is considered for the controllers in the inner control loop of the ac-DGs, which consists of an outer voltage controller (slower) and an inner current controller (faster). Event though any kind of controller is able to synthesize the ac-DG's output ac-voltage, a proportional-resonant (PR) controller is considered in both stages of the nested controller.

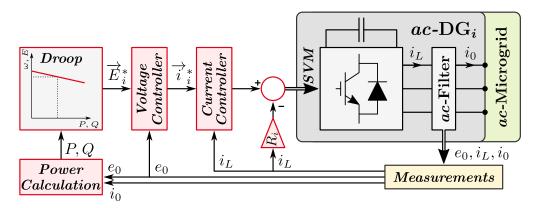


Figure 3.2: Inner and primary control loops for the ac-DGs.

The typical structure for a PR controller  $(C_{PR}(s))$  is given by:

$$C_{PR}(s) = k_P + \frac{2k_i s}{s^2 + \omega_0^2}$$
 (3.1)

where  $k_P$  and  $k_i$  are the proportional and integral gains of the controller, respectively, and  $\omega_0$  is the resonant frequency. An important feature of the PR controller is that it is possible to program it with a variable resonant frequency; thus, it is possible to adjust the response of the controller according to the changes in the frequency due to the droop controller. As depicted in Fig. 3.2, the reference currents  $(\vec{i}_i^*)$  are set by the outer voltage loop, while the reference voltages (i.e., the output voltage magnitude and frequency) are set by the outer droop controllers and the secondary control loops, as will be described in the next Section. The typical structure for the droop controllers is given by:

$$\delta\omega_{\rm D} = M_{\rm ac} P_{\rm ac} \tag{3.2a}$$

$$\delta E_{\rm D} = N_{\rm ac} Q_{\rm ac} \tag{3.2b}$$

where  $\delta\omega_{\rm D}$  and  $\delta E_{\rm D}$  are the control actions of the droop control,  $M_{\rm ac} < 0$  and  $N_{\rm ac} < 0$  are the droop slopes,  $P_{\rm ac}$  and  $Q_{\rm ac}$  are the active and reactive power generated by the ac-DG, respectively.

#### 3.3.2 Secondary Control Loops

The secondary control loop for ac-DGs aims to restore the frequency  $\omega_i$  and the amplitude  $E_i$  of the ac-voltage to their nominal values  $\omega^*$  and  $E^*$ , while maintaining satisfactory power-sharing. In the proposed DSC for the ac-DGs in the ac-microgrid (ac-DSC), the communicated variables in the ac-microgrid are the active and reactive powers  $P_i$  and  $Q_i$  in p.u., which are given by

$$P_{i} := P_{ac-i}/S_{max-i}, \qquad i \in \mathbb{N}_{ac},$$

$$Q_{i} := Q_{ac-i}/S_{max-i}, \qquad i \in \mathbb{N}_{ac},$$

$$(3.3)$$

where  $P_{\text{ac-i}}$  ( $Q_{\text{ac-i}}$ ) is the instantaneous real (reactive) power generated by the i<sup>th</sup> ac-DG, and  $S_{\text{max-i}}$  is the rated apparent power of the i<sup>th</sup> ac-DG. For the ac-microgrid, the secondary variables are the frequency ( $\omega_i$ ) and the amplitude ( $E_i$ ) of the voltages, while the consensus variables are the active power in p.u. ( $P_i$ ) and the reactive power in p.u. ( $Q_i$ ). Following the

traditional approach, two ac-DSCs are proposed: the first one for active power-sharing and frequency restoration, and the second one for reactive power-sharing and voltage restoration.

The ac-DSC for active power-sharing and frequency restoration is given by:

$$\omega_{i} = \omega^* + M_{ac-i}P_{ac-i} + \psi_{i} \tag{3.4a}$$

$$\sigma_{i}\dot{\psi}_{i} = -(\omega_{i} - \omega^{*}) + \psi_{ac-i} + \psi_{dc-i}$$
(3.4b)

$$\psi_{\mathrm{ac-i}} = -\sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{ij} \left( P_{\mathrm{ac-i}} - P_{\mathrm{ac-j}} \right) \tag{3.4c}$$

$$\psi_{\mathrm{dc-i}} = -\sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ij} \left( P_{\mathrm{ac-i}} - P_{\mathrm{dc-}j} \right) \tag{3.4d}$$

for  $i \in \mathbb{N}_{ac}$ , where  $P_{ac-j}$  is the instantaneous power in p.u. generated by the  $j^{th}$   $ac\text{-DG} \in \mathbb{N}_{ac}$ , and  $P_{dc-j}$  is the instantaneous power in p.u. generated by the  $j^{th}$   $dc\text{-DG} \in \mathbb{N}_{dc}$ .

The ac-DSC for reactive power-sharing and voltage restoration is given by:

$$E_{\mathbf{i}} = E^* + N_{\mathbf{ac-i}} \cdot Q_{\mathbf{ac-i}} + \chi_{\mathbf{i}} \tag{3.5a}$$

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}} \left( E_{\mathbf{i}} - E^* \right) - b_{\mathbf{i}} \sum_{j \in \mathbb{N}_{ac}} a_{\mathbf{i}j} \left( Q_{\mathbf{i}} - Q_j \right)$$
(3.5b)

for  $i \in \mathbb{N}_{ac}$ . The gains  $M_{ac-i}$ ,  $N_{ac-i} < 0$  are the primary droop gains, and  $\sigma_i$ ,  $\varrho_i > 0$  are time constants. The gains  $\beta_i$ ,  $b_i > 0$  can be tuned to produce a compromise between voltage regulation accuracy and reactive power-sharing accuracy. A block diagram of the proposed distributed secondary control strategy for the ac-DGs is shown in Fig. 3.3.

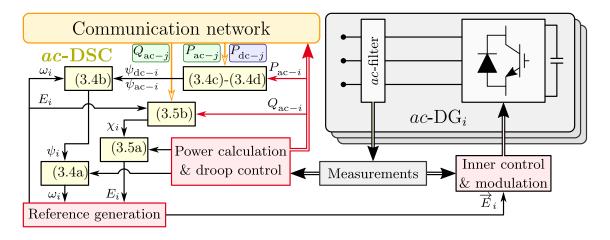


Figure 3.3: Proposed distributed secondary control for ac-microgrid (ac-DSC).

Although relatively similar control strategies have been studied before (see [99]), the key difference in this work is that the active power consensus of ac-DGs is extended to dc-DGs (and later, to ICs). This is reflected in the term  $\psi_{dc-i}$  [see (3.4d)], which illustrates the interaction between ac-DGs and dc-DGs, and in next sections.

# 3.4 Control Strategy Proposed for dc-Microgrid

#### 3.4.1 Inner and Primary Control Loops

In dc-microgrids, several types of power electronic devices can be used to interface the dc-DGs with the network, depending on the system's requirements. Both unidirectional and bidirectional dc-to-dc converters can be used depending on the nature of the DG source. However, it must be able to operate in droop controlled mode, i.e., to synthesize voltages with variable magnitude. The structure and control loops for a droop controlled HBC is depicted in Fig. 3.4. A low-pass filter is considered to interface the dc-DGs with the dc-microgrid.

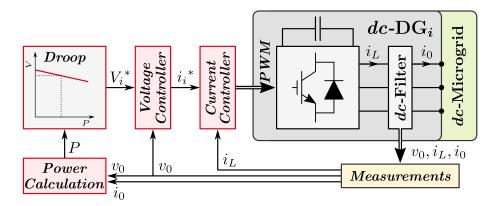


Figure 3.4: Inner and primary control loops for the dc-DGs.

A nested structure is considered for the controllers in the inner control loop of the dc-DGs, which consists of an outer voltage controller (slower) and an inner current controller (faster). Even though any kind of controller is able to synthesize the dc-DG's output dc-voltage, a proportional-integral (PI) controller is considered in this work. The typical structure for a PI controller ( $C_{PI}(s)$ ) is given by:

$$C_{\rm PI}(s) = k_P + \frac{k_{\rm i}}{s} \tag{3.6}$$

where  $k_P$  and  $k_i$  are the proportional and integral gains of the controller, respectively. As depicted in Fig. 3.4, the reference current  $(i_i^*)$  are set by the outer voltage loop, while the reference voltage (i.e., the output voltage magnitude) is set by the outer droop controllers and the secondary control loops, as will be described in the next Section. The typical structure for the droop controller is given by:

$$\delta V_{\rm D} = M_{\rm dc} P_{\rm dc} \tag{3.7}$$

where  $\delta V_{\rm D}$  is the output of the droop controller,  $M_{\rm dc} < 0$  is the droop slope, and  $P_{\rm dc}$  is the active power generated by the dc-DG.

## 3.4.2 Secondary Control Loop

The secondary control loop for dc-DGs aims to restore the dc-voltages  $V_i$  to their nominal value  $V^*$  while maintaining satisfactory power-sharing among DGs. In the proposed DSC for

the dc-DGs in the dc-microgrid (dc-DSC), the variable shared by the dc-DGs to the other DGs is the power  $P_i$  in p.u. generated by the  $i^{th}$  dc-DG, given by

$$P_{i} := P_{dc-i}/P_{max-i}, \qquad i \in \mathbb{N}_{dc},$$
 (3.8)

where  $P_{dc-i}$  is the instantaneous power generated by the i<sup>th</sup> dc-DG and  $P_{max-i}$  is the rated power of the i<sup>th</sup> dc-DG. For the dc-microgrid, the secondary variable is the dc-voltage  $V_i$ , while the consensus variable is the p.u. power  $P_i$ . Then, the proposed dc-DSC for power-sharing and voltage restoration is

$$V_{\rm i} = V^* + M_{\rm dc-i} \cdot P_{\rm dc-i} + \varphi_{\rm i} \tag{3.9a}$$

$$\rho_{i}\dot{\varphi}_{i} = -\gamma_{i} \left( V_{i} - V^{*} \right) + \varphi_{dc-i} + \varphi_{ac-i}$$
(3.9b)

$$\varphi_{\mathrm{dc-i}} = -c_{\mathrm{i}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{\mathrm{i}j} \left( P_{\mathrm{dc-i}} - P_{\mathrm{dc-j}} \right)$$
(3.9c)

$$\varphi_{\mathrm{ac-i}} = -c_{\mathrm{i}} \sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{\mathrm{i}j} \left( P_{\mathrm{dc-i}} - P_{\mathrm{ac-}j} \right)$$
(3.9d)

where  $i \in \mathbb{N}_{dc}$ ,  $M_{dc-i} < 0$  is the primary control gain, and  $\rho_i > 0$  is a time constant. The gains  $\gamma_i$  and  $c_i$  can be tuned to produce a trade-off between voltage regulation accuracy and power-sharing accuracy. A block diagram of the proposed dc-DSC is shown in Fig. 3.5.

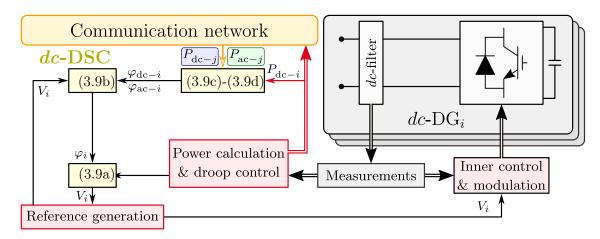


Figure 3.5: Proposed distributed secondary control for dc-microgrid (dc-DSC).

# 3.5 Control Strategy Proposed for a Single *IC*

## 3.5.1 Inner Control Loop

In hybrid ac/dc-microgrids, a power electronic device which allows ac/dc conversion and bidirectional power flow is required to interconnect the two sides of the microgrid. In this case, a two stage ac/dc/dc converter is considered for the IC, as shown in Fig. 3.6. Thus, the IC possesses two electrical ports; the first one is an ac-port which is connected to the ac-microgrid, while the second one is a dc-port connected to the dc-microgrid. The two ports are linked by a dc-link, which needs to maintain a constant dc-voltage  $(E_{dc})$ . A low-pass filter is considered at each port to interface the IC with the DGs on the respective side.

The controllers in the inner loop of the IC must control independently the ac-and dc-variables. Moreover, only the converter from one side must regulate the dc-link voltage,

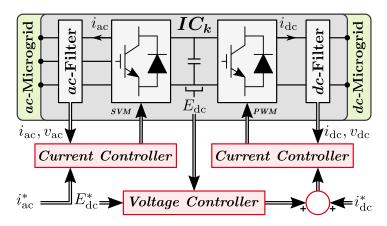


Figure 3.6: Inner control loop for the *ICs*.

while the other converter is in charge of regulating the power transfer. In this case, the converter on the dc-side of the IC is responsible for regulating the dc-link voltage  $(E_{dc}^*)$ . In addition, it is possible to feed-forward the current reference which allows to transfer the power set by the secondary control loop  $(i_{dc}^*)$ , as described in the next Section. Due to the nature of the signals controlled at this side (the voltage and the current are dc-signals), a nested PI structure  $(C_{PI}(s))$  is considered for the controllers in the inner control loop of the dc-side of the IC. On the other hand, a PR controller  $(C_{PR}(s))$  is used to control the power transfer by the IC, according to the current reference  $(i_{ac}^*)$  obtained from the power reference  $(P_{IC}^*)$  set by the secondary control loop, as will be described in next Section.

#### 3.5.2 Secondary Control Loop

The novel consensus-based DSC strategy for the ICs proposed in this work (ic-DSC) is slightly different than those used for ac-DGs and dc-DGs. The IC has to regulate the power transfer between the two sides of the microgrid. This has to be realized seamlessly, and without affecting the power-sharing among the DGs. To achieve this, the IC sends its own status (1: ON, 0: OFF) to the DGs in order to enable the power-consensus between the two sides of the microgrid, while the IC receives the power in p.u. being generated by the ac-DGs ( $P_{ac}$ ) and by the dc-DGs ( $P_{dc}$ ). Therefore, the power reference  $P_{IC-k}^*$  for the single  $k^{th}$  IC (k = n + m + 1) is updated as

$$\tau_k \dot{P}_{IC-k}^* = -\sum_{i \in \mathbb{N}_{ac}} \sum_{j \in \mathbb{N}_{dc}} a_{ik} a_{jk} \left( P_{ac-i} - P_{dc-j} \right)$$
 (3.10)

where  $\tau_k > 0$  is a time constant. Note that the DGs communicating with the IC are included in the control law (3.10). In this work, the sign convention is that  $P_{IC-k}^* > 0$  if power flows from the dc-microgrid to the ac-microgrid.

A block diagram of the proposed ic-DSC is shown in Fig. 3.7.

From (3.10), note that the power reference for the IC will be adjusted to ensure power-sharing among DGs in both sides of the hybrid ac/dc-microgrid. Additionally, if the IC is out of service, it is possible to split the proposed control system into two separate controllers (ac-DSC and dc-DSC), and change the global power-sharing to a sub-microgrid power-sharing.

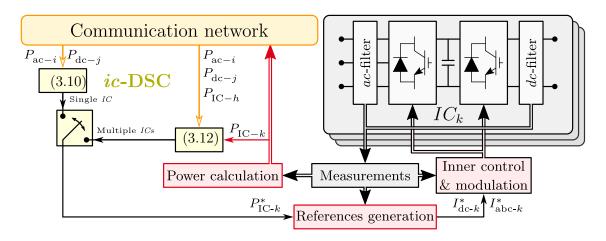


Figure 3.7: Proposed distributed secondary control for *IC*.

In this way, voltage/frequency regulation in each sub-microgrid can be achieved despite the temporary absence of the IC. Moreover, all the secondary control tasks are maintained within each microgrid, and the advantages of distributed controllers over the centralized ones are maintained. However, if the lone IC in a hybrid ac/dc-microgrid fails, it will be impossible to transfer power from the ac-side to dc-side or vice-versa. Hence, the use of multiple ICs in hybrid ac/dc-microgrids is highly recommended, as discussed in the next subsection.

Regarding the communication in (3.10), the proposed strategy works properly if at least one ac-DG and one dc-DG are communicating with the IC. This is illustrated using the hybrid microgrid topology shown in Fig. 3.8. Red dotted lines show the communication links between units. In this example, the hybrid ac/dc-microgrid is composed of 3 ac-DGs, 3 dc-DGs and 1 IC. The IC communicates with 2 DGs on each side of the microgrid. The adjacency matrix  $\mathbf{A}$  here models a connected bidirectional communication network. The elements in  $\mathbf{A}$  are  $a_{ij} = 1$  if a communication link exists between the  $i^{th}$  and the  $j^{th}$  unit, and  $a_{ij} = 0$  otherwise.

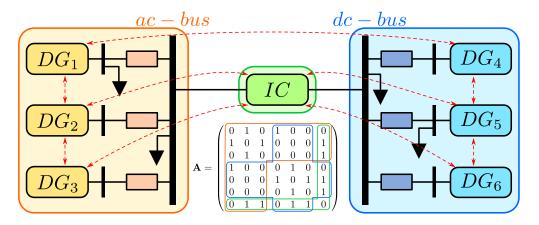


Figure 3.8: Example topology of a hybrid ac/dc-microgrid with the adjacency matrix.

# 3.6 Control Strategy Proposed for Multiple *ICs*

As mentioned before, by considering multiple ICs in the hybrid ac/dc-microgrid, it is possible to improve its reliability due to the existence of multiple paths for transferring power between sub-microgrids. However, the existence of multiple ICs increases the complexity of the microgrid control and —depending on the control strategy utilized— can generate circulating currents among the ICs. These circulating currents must be eliminated to avoid overloading of converters or line congestion [82].

Thus, based on the single IC controller proposed in (3.10), an additional term to achieve a power consensus among multiple ICs is introduced. The variable shared by the ICs is the power  $P_k$  in p.u. being transferred through the  $k^{th}$  IC.

$$P_k := P_{\mathrm{IC}-k}/P_{\mathrm{max}-k}, \quad k \in \mathbb{N}_{\mathrm{IC}}, \tag{3.11}$$

where  $P_{IC-k}$  is the instantaneous power through the  $k^{th}$  IC and  $P_{\max-k}$  is its rated power. The ic-DSC proposed in this work for the  $k^{th}$  IC, considering g ICs in the microgrid, is given by

$$\dot{P}_{\mathrm{IC}-k}^* = -\mu_k \sum_{i \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ik} a_{jk} \left( P_{\mathrm{ac}-i} - P_{\mathrm{dc}-j} \right) - \vartheta_k \sum_{h \in \mathbb{N}_{\mathrm{IC}}} a_{hk} \left( P_{\mathrm{IC}-k} - P_{\mathrm{IC}-h} \right)$$
(3.12)

where  $\mu_k$  and  $\vartheta_k$  are positive gains for adjusting the transient response and the accuracy of power-sharing. The proposed ic-DSC contributes to both power-sharing among DGs on the two sides of the hybrid ac/dc-microgrid and power-sharing among ICs. Additionally, if any IC is out of service, the others can be used for transferring power between the two sub-microgrids, as long as their power ratings are not exceeded. The ratio of power-sharing among ICs can also be controlled by adjusting the second term on the right side of (3.12). Nevertheless, to control this ratio, it is necessary to have a general knowledge of the hybrid ac/dc-microgrid and to perform additional studies (e.g., optimal power flow) which are typically utilized at the hierarchical tertiary control level. The tertiary control level is outside the scope of this work.

# 3.7 Summary

In this Chapter, a consensus-based distributed secondary control strategy for hybrid ac/dc-microgrids has been proposed. This strategy considers the hybrid ac/dc-microgrid as a single entity, not three independent ones interacting with one another. The strategy is capable of restoring the variables modified by the primary control loop to their nominal values, while maintaining an accurate power-sharing among DGs on both sides of the microgrid. Additionally, when the hybrid ac/dc-microgrid has multiple ICs, accurate power-sharing among them is also ensured.

The proposed strategy considers a reduced communication layer, as each DG is communicating only with its neighbouring DGs. Due to the fact that ICs also participate in the communication layer, the secondary control strategy can be adapted to each side separately in case the ICs are not available. The tuning of the controllers proposed in this Chapter is addressed in Chapter 6. The validation of the proposed control strategy is reported in Chapter 7 using an experimental 24kW hybrid ac/dc-microgrid.

# Chapter 4

# Control Strategy Proposed to Minimize Operating Costs

#### 4.1 Introduction

In this Chapter, a new consensus-based distributed secondary control (DSC) strategy is proposed for minimizing the operation cost among all the agents in the hybrid ac/dc-microgrid. In the DSC proposed in this Chapter, each DG achieves incremental cost consensus with all other DGs of the hybrid microgrid, i.e., ac-DGs and dc-DGs, while operating within limits. The main challenge for this Chapter is to design a control strategy for hybrid ac/dc-microgrids that minimizes the operational cost while restoring the secondary variables to their nominal values.

The formulation of the control strategy differs according to the number of ICs utilized in the microgrid. Thus, the main differences for the formulation of the control strategy considering one or more ICs are explained in this Chapter, which is organized as follows: the optimization problem considering a single IC is formulated in Section 4.2, while its proposed control strategy is presented in Section 4.3. Conversely, the formulation of the optimization problem considering multiple ICs and the control strategy proposed to solve this problem are presented in Section 4.4 and Section 4.5, respectively.

## 4.2 Single Interlinking Converter Formulation

First, the formulation of the optimization problem considers a single IC in the hybrid ac/dc-microgrid. The formulation of the optimization problem is explained in the following.

#### 4.2.1 System Modelling

Fig. 4.1 shows a simplified topology for a hybrid ac/dc-microgrid, which consists of ac-DGs, dc-DGs and a single IC. The set of these devices are labelled as  $\mathbb{N}_{ac} = \{1, \ldots, n\}$ ,  $\mathbb{N}_{dc} = \{n+1, \ldots, n+m\}$ , and  $\mathbb{N}_{IC} = \{n+m+1\}$ . For simplicity, a lumped load is modelled in each side of the microgrid (ac-Load and dc-Load).

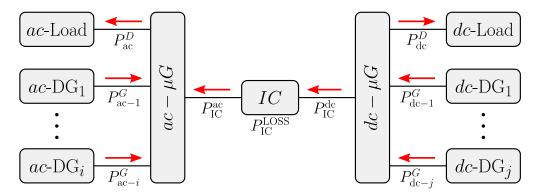


Figure 4.1: General topology for a hybrid ac/dc-microgrid with a single IC.

From Fig. 4.1 the following equations can be deduced:

$$P_{\rm IC}^{\rm ac} = P_{\rm ac}^{\rm D} - \sum_{\rm i \in \mathbb{N}_{ac}} P_{\rm ac-i}^{\rm G} \tag{4.1a}$$

$$P_{\mathrm{IC}}^{\mathrm{dc}} = \sum_{j \in \mathbb{N}_{\mathrm{dc}}} P_{\mathrm{dc}-j}^{\mathrm{G}} - P_{\mathrm{dc}}^{\mathrm{D}}$$

$$\tag{4.1b}$$

$$P_{\rm IC}^{\rm LOSS} = P_{\rm IC}^{\rm dc} - P_{\rm IC}^{\rm ac} \tag{4.1c}$$

where  $P_{\rm IC}^{\rm LOSS}$  is the power loss in the IC. Red arrows show the direction of the power flow in the microgrid: DGs supply power to the system, loads absorb power from the system, and the direction of the power flow in the IC has been arbitrarily defined.

Since the modelled IC lacks an energy storage unit, from (4.1c) follows that variables in the IC are not independent and two of them can be expressed as a function of the other variable in the IC (i.e., the power  $P_{\rm IC}^{\rm ac}$  or  $P_{\rm IC}^{\rm dc}$ ). Moreover, only power at one port of the IC can be controlled. Arbitrarily, in this case, the power in the ac-port  $P_{\rm IC}^{\rm ac}$  is the variable controlled in the IC and it is the variable defined as independent in the IC. The power losses in the IC can be empirically modelled as a fraction of the power flowing through this device ( $k^{\rm LOSS}$  %) [136]. Then, the variables in the IC are expressed as shown in (4.2), where constants  $k_{\rm IC}$  and  $k_{\rm IC}^{\rm LOSS}$  depend on the direction of the power through the IC, and are shown in Table 4.1.

$$P_{\rm IC}^{\rm dc}(P_{\rm IC}^{\rm ac}) = k_{\rm IC}P_{\rm IC}^{\rm ac} \tag{4.2a}$$

$$P_{\rm IC}^{\rm LOSS}(P_{\rm IC}^{\rm ac}) = k_{\rm IC}^{\rm LOSS}P_{\rm IC}^{\rm ac} \tag{4.2b}$$

Table 4.1: Parameters for equation (4.2).

	Power flow	$k_{ m IC}$	$k_{ m IC}^{ m LOSS}$
$P_{\rm IC}^{\rm ac} > 0$	from $dc$ to $ac$	$\frac{1}{1-k^{\text{LOSS}}}$	$\frac{k^{\rm LOSS}}{1 - k^{\rm LOSS}}$
$P_{\rm IC}^{\rm ac} < 0$	from $ac$ to $dc$	$1 - k^{\text{LOSS}}$	$-k^{\text{LOSS}}$

Now, replacing (4.2a) in (4.1b), and combining it with (4.1a), we obtain:

$$P_{\mathrm{IC}}^{\mathrm{ac}}(\mathbf{P_{ac}^{G}}, \mathbf{P_{dc}^{G}}) = \frac{1}{1 + k_{\mathrm{IC}}} \left( P_{\mathrm{ac}}^{\mathrm{D}} - \sum_{i \in \mathbb{N}_{ac}} P_{\mathrm{ac-i}}^{\mathrm{G}} + \sum_{j \in \mathbb{N}_{dc}} P_{\mathrm{dc-}j}^{\mathrm{G}} - P_{\mathrm{dc}}^{\mathrm{D}} \right)$$
(4.3)

where  $\mathbf{P_{ac}^G} = \{P_{ac-i}^G : i \in \mathbb{N}_{ac}\}$  and  $\mathbf{P_{dc}^G} = \{P_{dc-j}^G : j \in \mathbb{N}_{dc}\}$  are the set of power generated by the ac-DGs and dc-DGs, respectively.

In addition, a quadratic cost function for the operation cost of the ac-DGs ( $C_{ac-i}$ ,  $\forall i \in \mathbb{N}_{ac}$ ) and dc-DGs ( $C_{dc-j}$ ,  $\forall j \in \mathbb{N}_{dc}$ ) is considered, which is defined as follows:

$$C_{\text{ac-i}}(P_{\text{ac-i}}^{\text{G}}) = a_{\text{ac-i}} (P_{\text{ac-i}}^{\text{G}})^2 + b_{\text{ac-i}} P_{\text{ac-i}}^{\text{G}} + c_{\text{ac-i}} [\$/h], \quad \forall i \in \mathbb{N}_{\text{ac}}$$
 (4.4a)

$$C_{dc-j}(P_{dc-j}^{G}) = a_{dc-j} \left(P_{dc-j}^{G}\right)^{2} + b_{dc-j} P_{dc-j}^{G} + c_{dc-j} \quad [\$/h], \quad \forall j \in \mathbb{N}_{dc}$$
(4.4b)

where  $a_{\text{ac-i}} \in \mathbb{R}_{>0}$  and  $b_{\text{ac-i}}, c_{\text{ac-i}} \in \mathbb{R}_{\geq 0}$  are parameters for the cost function of the i<sup>th</sup> ac-DG, and  $a_{\text{dc-j}} \in \mathbb{R}_{>0}$  and  $b_{\text{dc-j}}, c_{\text{dc-j}} \in \mathbb{R}_{\geq 0}$  are parameters for the cost function of the j<sup>th</sup> dc-DG.

For future use, the following partial derivatives are calculated:

$$\frac{\partial P_{\rm IC}^{\rm ac}}{\partial P_{\rm ac-i}^{\rm G}} = \frac{-1}{1 + k_{\rm IC}}, \qquad \frac{\partial P_{\rm IC}^{\rm ac}}{\partial P_{\rm dc-j}^{\rm G}} = \frac{1}{1 + k_{\rm IC}}, \qquad \frac{\partial P_{\rm IC}^{\rm LOSS}}{\partial P_{\rm IC}^{\rm ac}} = k_{\rm IC}^{\rm LOSS}$$
(4.5)

## 4.2.2 Optimization Problem Formulation

The optimization problem considered in this work determines the least-cost dispatch of controllable DG units in a hybrid ac/dc-microgrid while maintaining generation power within limits. Constraints impose the later condition on DG power injections. The formulation is based on a system representation without line losses as follows:

minimize 
$$\mathbf{P_{ac}^{G}}, \mathbf{P_{dc}^{G}}$$
  $\sum_{i \in \mathbb{N}_{ac-i}} \left( P_{ac-i}^{G} \right) + \sum_{j \in \mathbb{N}_{dc}} C_{dc-j} \left( P_{dc-j}^{G} \right)$  (4.6a)

subject to 
$$P_{\text{ac}}^{\text{D}} + P_{\text{dc}}^{\text{D}} + P_{\text{IC}}^{\text{LOSS}} = \sum_{\mathbf{i} \in \mathbb{N}_{\text{ac}}} P_{\text{ac}-\mathbf{i}}^{\text{G}} + \sum_{j \in \mathbb{N}_{\text{dc}}} P_{\text{dc}-j}^{\text{G}},$$
 (4.6b)

$$P_{\text{ac-i}}^{\text{G-}} \le P_{\text{ac-i}}^{\text{G}} \le P_{\text{ac-i}}^{\text{G+}}, \quad i \in \mathbb{N}_{\text{ac}},$$
 (4.6c)

$$P_{\text{dc}-j}^{G-} \le P_{\text{dc}-j}^{G} \le P_{\text{dc}-j}^{G+}, \quad j \in \mathbb{N}_{\text{dc}},$$
 (4.6d)

$$P_{\rm IC}^{\rm ac-} \le P_{\rm IC}^{\rm ac} \le P_{\rm IC}^{\rm ac+} \tag{4.6e}$$

where (4.6b) is the power balance constraint (equality condition), and (4.6c)-(4.6e) are the power limits constraints (inequality conditions). The upper limits are defined by the superscripts G+ and ac+, while the lower limits are defined by the superscripts G- and ac-. In this problem

- (a) the power generated by the DGs  $(P_{ac-i}^G \text{ and } P_{dc-j}^G)$  are optimization variables,
- (b) the power loads  $(P_{ac}^{D})$  and  $(P_{ac}^{D})$  are **constants** for the optimization problem, and
- (c) the power transferred through the IC ( $P_{\rm IC}^{\rm ac}$ ) is a function of the optimization variables.

Considering the optimization problem formulated in (4.6a)-(4.6e), the Lagrangian function can be written as follows:

$$\mathbb{L}\left(P_{\text{ac-i}}^{G}, P_{\text{dc-j}}^{G}, \lambda, \alpha_{\text{ac-i}}^{+}, \alpha_{\text{ac-i}}^{-}, \alpha_{\text{dc-j}}^{+}, \alpha_{\text{dc-j}}^{-}, \alpha_{\text{IC}}^{+}, \alpha_{\text{IC}}^{-}\right) = \\
\sum_{i \in \mathbb{N}_{\text{ac}}} C_{\text{ac-i}} \left(P_{\text{ac-i}}^{G}\right) + \sum_{j \in \mathbb{N}_{\text{dc}}} C_{\text{dc-j}} \left(P_{\text{dc-j}}^{G}\right) \\
+ \lambda \left(P_{\text{ac}}^{D} + P_{\text{dc}}^{D} + P_{\text{IC}}^{LOSS} - \sum_{i \in \mathbb{N}_{\text{ac}}} P_{\text{ac-i}}^{G} - \sum_{j \in \mathbb{N}_{\text{dc}}} P_{\text{dc-j}}^{G}\right) \\
+ \sum_{i \in \mathbb{N}_{\text{ac}}} \left[\alpha_{\text{ac-i}}^{+} \left(P_{\text{ac-i}}^{G} - P_{\text{ac-i}}^{G+}\right) + \alpha_{\text{ac-i}}^{-} \left(P_{\text{ac-i}}^{G-} - P_{\text{ac-i}}^{G}\right)\right] \\
+ \sum_{j \in \mathbb{N}_{\text{dc}}} \left[\alpha_{\text{dc-j}}^{+} \left(P_{\text{dc-j}}^{G} - P_{\text{dc-j}}^{G+}\right) + \alpha_{\text{dc-j}}^{-} \left(P_{\text{dc-j}}^{G-} - P_{\text{dc-j}}^{G}\right)\right] \\
+ \sum_{k \in \mathbb{N}_{\text{IC}}} \left[\alpha_{\text{IC}}^{+} \left(P_{\text{IC}}^{\text{ac}} - P_{\text{IC}}^{\text{ac+}}\right) + \alpha_{\text{IC}}^{-} \left(P_{\text{IC}}^{\text{ac-}} - P_{\text{IC}}^{\text{ac}}\right)\right]$$

where the Lagrange multiplier  $\lambda$  is associated with the power balance constraint (4.6b),  $\{\alpha_{\text{ac-i}}^+, \alpha_{\text{ac-i}}^-, \alpha_{\text{dc-j}}^+, \alpha_{\text{dc-j}}^-\}$  with the maximum and minimum power outputs of DGs in equations (4.6c)-(4.6d), and  $\{\alpha_{\text{IC}}^+, \alpha_{\text{IC}}^-\}$  with the power limits of the IC in equation (4.6e). Since the objective function of the optimization problem is strictly convex and the constraints are linear, if the problem is feasible then it has a unique globally optimal solution.

Now, considering (4.5) and (4.7), the KKT optimality conditions of the optimization

problem are as follows:

Stationary condition:

$$\frac{\partial \mathbb{L}}{\partial P_{\text{ac-i}}^{G}} = \nabla C_{\text{ac-i}} \left( P_{\text{ac-i}}^{G} \right) + \lambda \left( \frac{-k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} - 1 \right) + \alpha_{\text{ac-i}}^{+} - \alpha_{\text{ac-i}}^{-} \\
+ \alpha_{\text{IC}}^{+} \left( \frac{-k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} \right) + \alpha_{\text{IC}}^{-} \left( \frac{k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} \right) = 0, \quad i \in \mathbb{N}_{\text{ac}} \qquad (4.8a)$$

$$\frac{\partial \mathbb{L}}{\partial P_{\text{dc-j}}^{G}} = \nabla C_{\text{dc-j}} \left( P_{\text{dc-j}}^{G} \right) + \lambda \left( \frac{k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} - 1 \right) + \alpha_{\text{dc-j}}^{+} - \alpha_{\text{dc-j}}^{-} \\
+ \alpha_{\text{IC}}^{+} \left( \frac{k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} \right) + \alpha_{\text{IC}}^{-} \left( \frac{-k_{\text{IC}}^{\text{LOSS}}}{1 + k_{\text{IC}}} \right) = 0, \quad j \in \mathbb{N}_{\text{dc}} \qquad (4.8b)$$

 $Complementary\ slackness:$ 

$$\alpha_{\text{ac}-i}^{+} \left( P_{\text{ac}-i}^{G} - P_{\text{ac}-i}^{G+} \right) = 0, \quad i \in \mathbb{N}_{\text{ac}}$$

$$(4.8c)$$

$$\alpha_{\text{ac-i}}^{-} \left( P_{\text{ac-i}}^{\text{G-}} - P_{\text{ac-i}}^{\text{G}} \right) = 0, \quad i \in \mathbb{N}_{\text{ac}}$$

$$(4.8d)$$

$$\alpha_{\mathrm{dc}-j}^{+} \left( P_{\mathrm{dc}-j}^{\mathrm{G}} - P_{\mathrm{dc}-j}^{\mathrm{G}+} \right) = 0, \quad j \in \mathbb{N}_{\mathrm{dc}}$$

$$(4.8e)$$

$$\alpha_{\mathrm{dc}-j}^{-} \left( P_{\mathrm{dc}-j}^{\mathrm{G}-} - P_{\mathrm{dc}-j}^{\mathrm{G}} \right) = 0, \quad j \in \mathbb{N}_{\mathrm{dc}}$$

$$(4.8f)$$

$$\alpha_{\rm IC}^+ \left( P_{\rm IC}^{\rm ac} - P_{\rm IC}^{\rm ac+} \right) = 0 \tag{4.8g}$$

$$\alpha_{\rm IC}^- \left( P_{\rm IC}^{\rm ac} - P_{\rm IC}^{\rm ac} \right) = 0 \tag{4.8h}$$

Primal feasibility:

$$(4.6b)$$
,  $(4.6c)$ ,  $(4.6d)$  and  $(4.6e)$ 

Dual feasibility:

$$\alpha_{ac-i}^+, \alpha_{ac-i}^- \ge 0, \qquad i \in \mathbb{N}_{ac}$$
 (4.8i)

$$\alpha_{\mathrm{dc}-j}^+, \alpha_{\mathrm{dc}-j}^- \ge 0, \qquad j \in \mathbb{N}_{\mathrm{dc}}$$
 (4.8j)

$$\alpha_{\rm IC}^+, \alpha_{\rm IC}^- \ge 0$$
 (4.8k)

Note that for each  $i \in \mathbb{N}_{ac}$  and  $j \in \mathbb{N}_{dc}$ , (4.8a) and (4.8b) can each be solved to obtain the Lagrange multiplier  $\lambda$ ; as useful notation, the corresponding solutions are denoted as

$$\lambda_{i}^{ac} := \frac{1 + k_{IC}}{1 + k_{IC} + k_{IC}^{LOSS}} \left( \nabla C_{ac-i} \left( P_{ac-i}^{G} \right) + \alpha_{ac-i}^{+} - \alpha_{ac-i}^{-} \right) \\
- \frac{k_{IC}^{LOSS}}{1 + k_{IC} + k_{IC}^{LOSS}} \left( \alpha_{IC}^{+} - \alpha_{IC}^{-} \right), \quad \forall i \in \mathbb{N}_{ac}$$

$$\lambda_{j}^{dc} := \frac{1 + k_{IC}}{1 + k_{IC} - k_{IC}^{LOSS}} \left( \nabla C_{dc-j} \left( P_{dc-j}^{G} \right) + \alpha_{dc-j}^{+} - \alpha_{dc-j}^{-} \right) \\
+ \frac{k_{IC}^{LOSS}}{1 + k_{IC} - k_{IC}^{LOSS}} \left( \alpha_{IC}^{+} - \alpha_{IC}^{-} \right), \quad \forall j \in \mathbb{N}_{dc}$$
(4.9a)

where, by optimality, we must have that (4.10) holds. We can interpret  $\lambda_i^{ac}$  as a Lagrange multiplier for the  $i^{th}$  DG on the ac-side, and  $\lambda_j^{dc}$  as a Lagrange multiplier for the  $j^{th}$  DG on the dc-side.

$$\lambda = \lambda_{\rm i}^{\rm ac} = \lambda_{j}^{\rm dc}, \quad \forall {\rm i} \in \mathbb{N}_{\rm ac}, \ \forall j \in \mathbb{N}_{\rm dc}$$
 (4.10)

Replacing (4.2) in (4.1c), the following expression can be obtained:

$$k_{\rm IC}^{\rm LOSS} = k_{\rm IC} - 1 \tag{4.11}$$

Now, replacing (4.11) in (4.9), we obtain a reduced expression in terms of  $k_{\rm IC}$  for  $\lambda_{\rm i}^{\rm ac}$  ( $\forall i \in \mathbb{N}_{\rm ac}$ ) and  $\lambda_{j}^{\rm dc}$  ( $\forall j \in \mathbb{N}_{\rm dc}$ ), as shown in (4.12). In this formulation, the DGs need to know the constant  $k_{\rm IC}$  [see Table 4.1] and the variables  $\{\alpha_{\rm IC}^+, \alpha_{\rm IC}^-\}$  from the IC; therefore, it is necessary to have a communication link between the DGs and the IC. In case the communication link does not exist (e.g., in a distributed configuration), the variables can be estimated using a distributed observer.

$$\lambda_{i}^{ac} = \frac{k_{IC} + 1}{2k_{IC}} \left( \nabla C_{ac-i} \left( P_{ac-i}^{G} \right) + \alpha_{ac-i}^{+} - \alpha_{ac-i}^{-} \right) - \frac{k_{IC} - 1}{2k_{IC}} \left( \alpha_{IC}^{+} - \alpha_{IC}^{-} \right), \quad \forall i \in \mathbb{N}_{ac} \quad (4.12a)$$

$$\lambda_j^{\text{dc}} = \frac{k_{\text{IC}} + 1}{2} \left( \nabla C_{\text{dc}-j} \left( P_{\text{dc}-j}^{\text{G}} \right) + \alpha_{\text{dc}-j}^{+} - \alpha_{\text{dc}-j}^{-} \right) + \frac{k_{\text{IC}} - 1}{2} \left( \alpha_{\text{IC}}^{+} - \alpha_{\text{IC}}^{-} \right), \forall j \in \mathbb{N}_{\text{dc}} \quad (4.12b)$$

Based on the optimality conditions of the optimal dispatch problem, a distributed secondary control strategy is designed with the objective of providing secondary variables regulation in the hybrid ac/dc-microgrid (i.e., frequency and voltage amplitude regulation in the ac-microgrid, and voltage regulation in the dc-microgrid), while driving the hybrid ac/dc-microgrid with a single IC to an optimal dispatch that complies with the KKT conditions (4.8).

# 4.3 Proposed Control Scheme for a Single IC

The distributed control scheme proposed in this Chapter aims to regulate the secondary variables in a hybrid ac/dc-microgrid, while maintaining optimality of dispatch. As explained in Chapter 3, the control scheme is designed for the three components of the hybrid ac/dc-microgrid, which are the ac-DGs, the dc-DGs, and the IC. On the other hand, the design of the control scheme is based on the convex optimization problem (4.6a), (4.6b), (4.6c) and (4.6d) presented in Section 4.2.2.

## 4.3.1 Control scheme proposed for the *ac*-DGs

The control scheme proposed for the ac-DGs is responsible for regulating the frequency on the ac-side of the microgrid, while minimizing the operation cost of all the DGs in the hybrid ac/dc-microgrid (4.13). It is important to note that the formulation of the optimization problem does not include reactive power injections, since it is assumed that the ac-DSC described in Section 3.3 controls reactive power injections to maintain voltages in the ac-side regulated. Thus, this proposal focuses on frequency and dc-voltage control by means of

optimally dispatching of real power of DG units.

$$\omega_{\mathbf{i}} = \omega^* + M_{\mathbf{ac}-\mathbf{i}} P_{\mathbf{ac}-\mathbf{i}}^{\mathbf{G}} + \Omega_{\mathbf{i}}^{\mathbf{ac}} \tag{4.13a}$$

$$\dot{\Omega}_{i}^{ac} = -k_{ac-i}^{a} \left(\omega_{i} - \omega^{*}\right) - k_{ac-i}^{b} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{ac}\right) - k_{ac-i}^{c} \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{dc}\right)$$
(4.13b)

$$\dot{\alpha}_{\text{ac-i}}^{+} = \mu_{\text{ac-i}}^{a} \max \left\{ P_{\text{ac-i}}^{G} - P_{\text{ac-i}}^{G+} + \frac{k_{\text{ac-i}}^{d}}{\mu_{\text{ac-i}}^{a}} \alpha_{\text{ac-i}}^{+}, 0 \right\} - k_{\text{ac-i}}^{d} \alpha_{\text{ac-i}}^{+}$$

$$(4.13c)$$

$$\dot{\alpha}_{\text{ac-i}}^{-} = \mu_{\text{ac-i}}^{b} \max \left\{ P_{\text{ac-i}}^{\text{G-}} - P_{\text{ac-i}}^{\text{G}} + \frac{k_{\text{ac-i}}^{\text{e}}}{\mu_{\text{ac-i}}^{b}} \alpha_{\text{ac-i}}^{-}, 0 \right\} - k_{\text{ac-i}}^{\text{e}} \alpha_{\text{ac-i}}^{-}$$
(4.13d)

$$\lambda_{i}^{ac} = \frac{\hat{k}_{i}^{IC} + 1}{2\hat{k}_{i}^{IC}} \left( \nabla C_{ac-i} \left( P_{ac-i}^{G} \right) + \alpha_{ac-i}^{+} - \alpha_{ac-i}^{-} \right) - \frac{\hat{k}_{i}^{IC} - 1}{2\hat{k}_{i}^{IC}} \hat{\alpha}_{i}^{IC}$$
(4.13e)

The proposed controller drives the system to an optimal dispatch point, while complying with the KKT conditions of the optimization problem. The term  $\Omega_{\rm i}^{\rm ac}$  in (4.13a) is a secondary control action to drive the units to their optimal dispatch level. Specifically, the frequency droop controller in (4.13a) is perturbed by the control action  $\Omega_{\rm i}^{\rm ac}$ , in order to change the dispatch of the ac-DGs until all DGs satisfy the consensus condition (4.10), which corresponds to the (unique) dual variable associated with the demand-supply balance equation of the hybrid ac/dc-microgrid's optimal dispatch problem, (4.6b). Variables  $k_{\rm ac-i}^a$ ,  $k_{\rm ac-i}^b$  and  $k_{\rm ac-i}^c$  are positive gains of the controller. In steady-state, the optimality condition (4.10) is enforced by (4.13b), (4.15b) and (4.16a).

The  $\lambda_{\rm i}^{\rm ac}$  of each ac-DG that complies with the stationarity condition can be calculated from (4.13e). The terms  $\hat{k}_{\rm i}^{\rm IC}$  and  $\hat{\alpha}_{\rm i}^{\rm IC}$  are the estimate of the variables  $k_{\rm IC}$  and  $\alpha_{\rm IC}^+ - \alpha_{\rm IC}^-$  from the IC, respectively. Since the ac-DGs need information from the IC and a communication link between all the DGs and the IC is not mandatory (the distributed approach is based on the existence of communication links between neighbouring agents), the cooperative tracking explained in Section 2.5 is considered among the ac-DGs to communicate the information from the IC to all the ac-DGs. Thus, the observers implemented are as follows:

$$\dot{\hat{k}}_{i}^{IC} = -\sum_{j \in \mathbb{N}_{ac}} a_{ij} \left( \hat{k}_{i}^{IC} - \hat{k}_{j}^{IC} \right) - \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left( \hat{k}_{i}^{IC} - \hat{k}_{j}^{IC} \right) - g_{i} \left( \hat{k}_{i}^{IC} - k_{IC} \right)$$

$$(4.14a)$$

$$\dot{\hat{\alpha}}_{i}^{IC} = -\sum_{j \in \mathbb{N}_{2c}} a_{ij} \left( \hat{\alpha}_{i}^{IC} - \hat{\alpha}_{j}^{IC} \right) - \sum_{j \in \mathbb{N}_{4c}} a_{ij} \left( \hat{\alpha}_{i}^{IC} - \hat{\alpha}_{j}^{IC} \right) - g_{i} \left( \hat{\alpha}_{i}^{IC} - (\alpha_{IC}^{+} - \alpha_{IC}^{-}) \right)$$
(4.14b)

where  $g_i > 0$  if ac-DG<sub>i</sub> has a direct communication link with the IC and  $g_i = 0$  otherwise.

In (4.13e), variables  $\alpha_{\rm ac-i}^+$  and  $\alpha_{\rm ac-i}^-$  are local control actions to keep the active power dispatch of ac-DGs within limits, which in equilibrium correspond to the dual variables associated with maximum and minimum active power limits, respectively. The control actions  $\alpha_{\rm ac-i}^+$  and  $\alpha_{\rm ac-i}^-$  are obtained from equations (4.13c) and (4.13d), respectively. In these equations, an increase in the values of the control actions is induced whenever ac-DG<sub>i</sub> goes beyond its maximum or minimum active power dispatch levels, respectively. Also, control actions  $\alpha_{\rm ac-i}^+$  and  $\alpha_{\rm ac-i}^-$  are driven down to zero by the controller if the active power dispatch of

ac-DG<sub>i</sub> is strictly within limits, where  $k_{\rm ac-i}^{\rm d}$ ,  $k_{\rm ac-i}^{\rm e}$ ,  $\mu_{\rm ac-i}^{a}$  and  $\mu_{\rm ac-i}^{b}$  are positive gains of the controllers.

In summary, the ac-DG<sub>i</sub> shares the following information with its neighbours:

- It sends the value of  $\lambda_i^{ac}$ ,  $i \in \mathbb{N}_{ac}$ , to the ac-DGs, dc-DGs and to the IC [see (4.12a)].
- It receives the value of  $\lambda_i^{ac}$ ,  $j \in \mathbb{N}_{ac}$ , from the ac-DGs [see (4.13b)].
- It receives the value of  $\lambda_i^{\text{dc}}$ ,  $j \in \mathbb{N}_{\text{dc}}$ , from the dc-DGs [see (4.13b)].
- It sends/receives the values of  $\hat{k}_{i}^{IC}$  and  $\hat{\alpha}_{i}^{IC}$  to the ac-DGs and dc-DGs [see (4.14)].
- It receives the values of  $k_{\rm IC}$ ,  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  from the IC [see (4.16b) and (4.16c)].

#### 4.3.2 Control scheme proposed for the dc-DGs

The control scheme proposed for the dc-DGs is responsible for regulating the voltage of the dc-side of the microgrid, while minimizing the operation cost of all the DGs in the hybrid ac/dc-microgrid (4.15).

$$V_j = V^* + M_{\text{dc}-j} P_{\text{dc}-j}^{G} + \Omega_j^{\text{dc}}$$
(4.15a)

$$\dot{\Omega}_{j}^{\text{dc}} = -k_{\text{dc}-j}^{a} \left(V_{j} - V^{*}\right) - k_{\text{dc}-j}^{b} \sum_{i \in \mathbb{N}_{\text{dc}}} a_{ij} \left(\lambda_{j}^{\text{dc}} - \lambda_{i}^{\text{dc}}\right) - k_{\text{dc}-j}^{c} \sum_{i \in \mathbb{N}_{\text{ac}}} a_{ij} \left(\lambda_{j}^{\text{dc}} - \lambda_{i}^{\text{ac}}\right)$$
(4.15b)

$$\dot{\alpha}_{\text{dc}-j}^{+} = \mu_{\text{dc}-j}^{a} \max \left\{ P_{\text{dc}-j}^{G} - P_{\text{dc}-j}^{G+} + \frac{k_{\text{dc}-j}^{d}}{\mu_{\text{dc}-j}^{a}} \alpha_{\text{dc}-j}^{+}, 0 \right\} - k_{\text{dc}-j}^{d} \alpha_{\text{dc}-j}^{+}$$
(4.15c)

$$\dot{\alpha}_{\mathrm{dc}-j}^{-} = \mu_{\mathrm{dc}-j}^{b} \max \left\{ P_{\mathrm{dc}-j}^{\mathrm{G}-} - P_{\mathrm{dc}-j}^{\mathrm{G}} + \frac{k_{\mathrm{dc}-j}^{\mathrm{e}}}{\mu_{\mathrm{dc}-j}^{b}} \alpha_{\mathrm{dc}-j}^{-}, 0 \right\} - k_{\mathrm{dc}-j}^{\mathrm{e}} \alpha_{\mathrm{dc}-j}^{-}$$
(4.15d)

$$\lambda_j^{\text{dc}} = \frac{\hat{k}_j^{\text{IC}} + 1}{2} \left( \nabla C_{\text{dc}-j} \left( P_{\text{dc}-j}^{\text{G}} \right) + \alpha_{\text{dc}-j}^+ - \alpha_{\text{dc}-j}^- \right) + \frac{\hat{k}_j^{\text{IC}} - 1}{2} \hat{\alpha}_j^{\text{IC}}$$
(4.15e)

The proposed controller drives the system to an optimal dispatch point, i.e., it complies with the KKT conditions of the optimization problem. The term  $\Omega_j^{\text{dc}}$  in (4.15a) is a secondary control action to drive the units to their optimal dispatch level. Specifically, the voltage droop controller in (4.15a) is perturbed by the control action  $\Omega_j^{\text{dc}}$ , in order to change the dispatch of the dc-DGs until all DGs reach the same value of  $\lambda$ , which corresponds to the (unique) dual variable associated with the demand-supply balance equation of the hybrid ac/dc-microgrid's optimal dispatch problem, (4.6b). Variables  $k_{\text{dc}-j}^a$ ,  $k_{\text{dc}-j}^b$  and  $k_{\text{dc}-j}^c$  are positive gains of the controller. In steady-state, the optimality condition (4.10) is enforced by (4.13b), (4.15b) and (4.16a)

The  $\lambda_j^{\text{dc}}$  of each dc-DG that complies with the stationarity condition can be calculated from (4.15e). The terms  $\hat{k}_j^{\text{IC}}$  and  $\hat{\alpha}_j^{\text{IC}}$  are the estimate of the variables  $k_{\text{IC}}$  and  $\alpha_{\text{IC}}^+ - \alpha_{\text{IC}}^-$  from the IC, respectively, and were explained in (4.14).

Variables  $\alpha_{\mathrm{dc}-j}^+$  and  $\alpha_{\mathrm{dc}-j}^-$  are local control actions to keep the active power dispatch of dc-DGs within limits, which in equilibrium correspond to the dual variables associated with maximum and minimum active power limits, respectively. The control actions  $\alpha_{\mathrm{dc}-j}^+$  and  $\alpha_{\mathrm{dc}-j}^-$  are obtained from equations (4.15c) and (4.15d), respectively. In these equations, an increase in the values of the control actions is induced whenever dc-DG<sub>i</sub> goes beyond its maximum or minimum active power dispatch levels, respectively. Also, control actions  $\alpha_{\mathrm{dc}-j}^+$  and  $\alpha_{\mathrm{dc}-j}^-$  are driven down to zero by the controller if the active power dispatch of dc-DG<sub>j</sub> is strictly within limits, where  $k_{\mathrm{dc}-j}^{\mathrm{d}}$ ,  $k_{\mathrm{dc}-j}^{\mathrm{e}}$ ,  $\mu_{\mathrm{dc}-i}^{\mathrm{a}}$  and  $\mu_{\mathrm{dc}-i}^{\mathrm{b}}$  are positive gains of the controllers.

In summary, the dc-DG<sub>i</sub> shares the following information with its neighbours:

- It sends the value of  $\lambda_j^{\text{dc}}$ ,  $j \in \mathbb{N}_{\text{dc}}$ , to the dc-DGs, ac-DGs and to the IC [see (4.12b)].
- It receives the value of  $\lambda_i^{dc}$ ,  $i \in \mathbb{N}_{dc}$ , from the dc-DGs [see (4.15b)].
- It receives the value of  $\lambda_i^{ac}$ ,  $i \in \mathbb{N}_{ac}$ , from the *ac-DGs* [see (4.15b)].
- It sends/receives the values of  $\hat{k}_j^{\rm IC}$  and  $\hat{\alpha}_j^{\rm IC}$  to the ac-DGs and dc-DGs [see (4.14)].
- It receives the values of  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  from the IC [see (4.16b) and (4.16c)].

#### 4.3.3 Control scheme proposed for the *IC*

The control scheme proposed for the IC is responsible for transferring power between both sides of the hybrid ac/dc-microgrid  $(P_{IC-k}^*)$ , in order to help to minimize the operation cost of all the DGs (4.16). In this work, the power reference obtained from the controller corresponds to the power at the ac-port of the IC since it is the controllable power defined in section 4.2.1.

$$\tau_{\mathrm{IC}-k}\dot{P}_{\mathrm{IC}-k}^{*} = -\sum_{i \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ik} a_{jk} \left(\lambda_{i}^{\mathrm{ac}} - \lambda_{j}^{\mathrm{dc}}\right)$$

$$(4.16a)$$

$$\dot{\alpha}_{\rm IC}^{+} = \mu_{\rm IC}^{a} \max \left\{ P_{\rm IC}^{\rm ac} - P_{\rm IC}^{\rm ac+} + \frac{\tau_{\rm IC}^{a}}{\mu_{\rm IC}^{a}} \alpha_{\rm IC}^{+}, 0 \right\} - \tau_{\rm IC}^{a} \alpha_{\rm IC}^{+}$$
(4.16b)

$$\dot{\alpha}_{\rm IC}^{-} = \mu_{\rm IC}^{b} \max \left\{ P_{\rm IC}^{\rm ac-} - P_{\rm IC}^{\rm ac} + \frac{\tau_{\rm IC}^{b}}{\mu_{\rm IC}^{b}} \alpha_{\rm IC}^{-}, 0 \right\} - \tau_{\rm IC}^{b} \alpha_{\rm IC}^{-}$$
(4.16c)

The proposed controller drives the system to an optimal dispatch point transferring power between the two sides of the hybrid ac/dc-microgrid, while complying with the KKT conditions of the optimization problem. In this case, the IC transfers power from the cheapest side to the most expensive one by comparing the values of  $\lambda_i^{\rm ac}$  and  $\lambda_j^{\rm dc}$ , as shown in (4.16a). Variable  $\tau_{\rm IC-k}$  is a positive gain of the controller. Variables  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  are local control actions to keep the active power transferred through the IC within limits, which in equilibrium correspond to the dual variables associated with maximum and minimum active power limits, respectively.

The control actions  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  are obtained from equations (4.16b) and (4.16c), respectively. In these equations, an increase in the values of the control actions is induced whenever

the IC goes beyond its maximum or minimum active power levels, respectively. Also, control actions  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  are driven down to zero by the controller if the active power transferred through the IC is strictly within limits, where  $\tau_{\rm IC}^a$ ,  $\tau_{\rm IC}^b$ ,  $\mu_{\rm IC}^a$  and  $\mu_{\rm IC}^b$  are positive gains of the controllers.

In summary, the IC shares the following information with its neighbours:

- It sends the values of  $k_{\rm IC}$ ,  $\alpha_{\rm IC}^+$  and  $\alpha_{\rm IC}^-$  to the ac-DGs and dc-DGs [see (4.12a)-(4.12b)].
- It receives the value of  $\lambda_i^{ac}$ ,  $i \in \mathbb{N}_{ac}$  from the *ac-DGs* [see (4.16a)].
- It receives the value of  $\lambda_j^{\text{dc}}$ ,  $j \in \mathbb{N}_{\text{dc}}$  from the dc-DGs [see (4.16a)].

# 4.4 Multiple Interlinking Converters Formulation

Now, the formulation of the optimization problem considering multiple ICs in the hybrid ac/dc-microgrid is presented. In this case, the power transferred through each IC can be considered as an optimization variable since it is possible to dispatch the amount of power transferred through each one, according to their efficiency (i.e., according to the percentage of power lost in the ac-to-dc conversion). Moreover, the total amount of power transferred from one side of the microgrid to the other (i.e., the sum of the individual power transferred through each IC) must help to reach the least-cost dispatch of controllable DGs units as presented in Section 4.2. This is further explained in the following.

## 4.4.1 System Modelling

Fig. 4.2 shows a simplified topology for a hybrid ac/dc-microgrid, which consists of ac-DGs, dc-DGs and multiple ICs. The set of these devices are labelled as  $\mathbb{N}_{ac} = \{1, \ldots, n\}$ ,  $\mathbb{N}_{dc} = \{n+1, \ldots, n+m\}$ , and  $\mathbb{N}_{IC} = \{n+m+1, \ldots, n+m+g\}$ . For simplicity, a lumped load is modelled in each side of the microgrid (ac-Load and dc-Load).

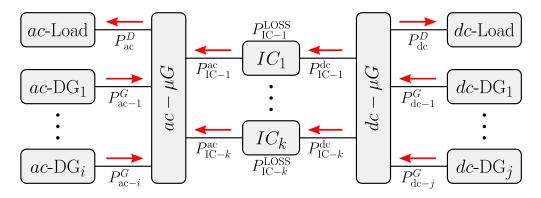


Figure 4.2: General topology for a hybrid ac/dc-microgrid with multiple ICs.

From Fig. 4.2 the following equations can be deduced:

$$\sum_{k \in \mathbb{N}_{IC}} P_{IC-k}^{ac} = P_{ac}^{D} - \sum_{i \in \mathbb{N}_{ac}} P_{ac-i}^{G}$$

$$(4.17a)$$

$$\sum_{k \in \mathbb{N}_{IC}} P_{IC-k}^{dc} = \sum_{j \in \mathbb{N}_{dc}} P_{dc-j}^{G} - P_{dc}^{D}$$

$$(4.17b)$$

where red arrows show the direction of the power flow in the microgrid: DGs supply power to the system, loads absorb power from the system, and the direction of the power flow in the ICs has been arbitrarily defined (it is positive from the dc- to the ac-side). In this case, the losses in the ICs are defined as explained in previous Section (see (4.2) and Table 4.1).

The total power transferred from the *ac*-microgrid to the *dc*-microgrid (and vice versa) can be defined as shown in (4.18). Moreover, the relationship between  $P_{\rm IC}^{\rm ac}$  and  $P_{\rm IC}^{\rm dc}$  can be defined as explained in previous Section [see (4.2a)].

$$P_{\mathrm{IC}}^{\mathrm{ac}} = \sum_{k \in \mathbb{N}_{\mathrm{IC}}} P_{\mathrm{IC}-k}^{\mathrm{ac}} , \qquad P_{\mathrm{IC}}^{\mathrm{dc}} = \sum_{k \in \mathbb{N}_{\mathrm{IC}}} P_{\mathrm{IC}-k}^{\mathrm{dc}}$$

$$(4.18)$$

Now, the total power transferred through multiple ICs ( $P_{\rm IC}^{\rm ac}$ ) is defined as a function of the generated powers, as shown in (4.19). On the contrary, the power transferred through each IC ( $P_{\rm IC}^{\rm ac}$ ) is not a function of the generated powers and it can be dispatched in order to reduce the losses and, therefore, minimize the operation cost of the hybrid ac/dc-microgrid.

$$P_{\mathrm{IC}}^{\mathrm{ac}}(\mathbf{P_{ac}^{G}}, \mathbf{P_{dc}^{G}}) = \frac{1}{1 + k_{\mathrm{IC}}} \left( P_{\mathrm{ac}}^{\mathrm{D}} - \sum_{i \in \mathbb{N}_{\mathrm{ac}}} P_{\mathrm{ac-i}}^{\mathrm{G}} + \sum_{j \in \mathbb{N}_{\mathrm{dc}}} P_{\mathrm{dc}-j}^{\mathrm{G}} - P_{\mathrm{dc}}^{\mathrm{D}} \right)$$
(4.19)

where  $\mathbf{P_{ac}^G} = \{P_{ac-i}^G : i \in \mathbb{N}_{ac}\}$  and  $\mathbf{P_{dc}^G} = \{P_{dc-j}^G : j \in \mathbb{N}_{dc}\}$  are the set of power generated by the ac-DGs and dc-DGs, respectively.

In addition, the power transferred through the  $k^{th}$  IC  $(P_{\text{IC}-k}^{\text{ac}})$  is defined as a percentage  $(\zeta_{\text{IC}-k}^{\text{ac}})$  of the total power transferred from one side of the hybrid ac/dc-microgrid to the other  $(P_{\text{IC}}^{\text{ac}})$ , as shown in (4.20a). Thus, the condition shown in (4.20b) must always be met.

$$P_{\mathrm{IC}-k}^{\mathrm{ac}} = \zeta_{\mathrm{IC}-k}^{\mathrm{ac}} P_{\mathrm{IC}}^{\mathrm{ac}} , \qquad \forall k \in \mathbb{N}_{\mathrm{IC}}$$
 (4.20a)

$$\sum_{k \in \mathbb{N}_{IC}} \zeta_{IC-k}^{\text{ac}} = 1 \tag{4.20b}$$

Finally, rearranging (4.20), the following expressions  $\forall k \in \mathbb{N}_{IC}$  are obtained for future use in the next Section:

$$P_{\rm IC}^{\rm ac} = \frac{P_{\rm IC-k}^{\rm ac}}{\zeta_{\rm IC-k}^{\rm ac}}, \quad \zeta_{\rm IC-k}^{\rm ac} = \frac{P_{\rm IC-k}^{\rm ac}}{P_{\rm IC}^{\rm ac}}, \quad \frac{\partial P_{\rm IC}^{\rm ac}}{\partial P_{\rm IC-k}^{\rm ac}} = \frac{1}{\zeta_{\rm IC-k}^{\rm ac}}$$
(4.21)

#### Optimization Problem Formulation 4.4.2

The optimization problem considered in this part of the work determines the least-cost dispatch of controllable DGs and ICs units in a hybrid ac/dc-microgrid while maintaining generation and transfer power within limits. Constraints impose the later condition on DGs power injections and ICs power transfer. The formulation is based on a system representation without line losses as follows:

$$\underset{\mathbf{P_{ac}^{G}, P_{dc}^{G}, P_{IC}^{ac}}{\text{minimize}} \quad \sum_{i \in \mathbb{N}_{ac}} C_{ac-i} \left( P_{ac-i}^{G} \right) + \sum_{j \in \mathbb{N}_{dc}} C_{dc-j} \left( P_{dc-j}^{G} \right) \tag{4.22a}$$

subject to 
$$P_{\text{ac}}^{\text{D}} + P_{\text{dc}}^{\text{D}} + P_{\text{IC}}^{\text{LOSS}} = \sum_{i \in \mathbb{N}_{\text{ac}}} P_{\text{ac-i}}^{\text{G}} + \sum_{j \in \mathbb{N}_{\text{dc}}} P_{\text{dc-j}}^{\text{G}},$$
 (4.22b)

$$P_{\rm IC}^{\rm ac} = \sum_{k \in \mathbb{N}_{\rm IC}} P_{{\rm IC}-k}^{\rm ac}, \tag{4.22c}$$

$$P_{\text{ac}-i}^{\text{G}-} \le P_{\text{ac}-i}^{\text{G}} \le P_{\text{ac}-i}^{\text{G}+}, \quad i \in \mathbb{N}_{\text{ac}},$$
 (4.22d)  
 $P_{\text{dc}-j}^{\text{G}-} \le P_{\text{dc}-j}^{\text{G}} \le P_{\text{dc}-j}^{\text{G}+}, \quad j \in \mathbb{N}_{\text{dc}},$  (4.22e)

$$P_{\mathrm{dc}-j}^{\mathrm{G}-} \le P_{\mathrm{dc}-j}^{\mathrm{G}} \le P_{\mathrm{dc}-j}^{\mathrm{G}+}, \quad j \in \mathbb{N}_{\mathrm{dc}}, \tag{4.22e}$$

$$P_{\mathrm{IC}-k}^{\mathrm{ac}-} \le P_{\mathrm{IC}-k}^{\mathrm{ac}} \le P_{\mathrm{IC}-k}^{\mathrm{ac}+}, \quad k \in \mathbb{N}_{\mathrm{IC}}$$

$$(4.22f)$$

where  $\mathbf{P}_{\mathrm{IC}}^{\mathrm{ac}} = \{P_{\mathrm{IC}-k}^{\mathrm{ac}} : k \in \mathbb{N}_{\mathrm{IC}}\}$  is the set of power transfer through the ICs, (4.22b) is the power balance constraint (first equality condition), (4.22c) is the power transfer constraint (second equality condition), and (4.22d)-(4.22f) are the power limits constraints (inequality conditions). The upper limits are defined by the superscripts G+ and ac+, while the lower limits are defined by the superscripts G- and ac-. In this problem

- (a) the power generated by the DGs  $(P_{ac-i}^G \text{ and } P_{dc-j}^G)$  are **optimization variables**,
- (b) the power transferred through the ICs ( $P_{IC-k}^{ac}$ ) are **optimization variables**, and
- (c) the power loads ( $P_{\rm ac}^{\rm D}$  and  $P_{\rm dc}^{\rm D}$ ) are **constants** for the optimization problem.

Considering the optimization problem formulated in (4.22a)-(4.22f), the Lagrangian function can be written as follows:

$$\mathbb{L}\left(P_{\text{ac-i}}^{G}, P_{\text{dc-}j}^{G}, P_{\text{IC-}k}^{\text{ac}}, \lambda^{G}, \lambda^{\text{IC}}, \alpha_{\text{ac-i}}^{+}, \alpha_{\text{ac-i}}^{-}, \alpha_{\text{dc-}j}^{+}, \alpha_{\text{dc-}j}^{-}, \alpha_{\text{IC-}k}^{+}, \alpha_{\text{IC-}k}^{-}\right) = \\
\sum_{i \in \mathbb{N}_{\text{ac}}} C_{\text{ac-i}} \left(P_{\text{ac-i}}^{G}\right) + \sum_{j \in \mathbb{N}_{\text{dc}}} C_{\text{dc-}j} \left(P_{\text{dc-}j}^{G}\right) \\
+ \lambda^{G} \left(P_{\text{ac}}^{D} + P_{\text{dc}}^{D} + P_{\text{IC}}^{\text{LOSS}} - \sum_{i \in \mathbb{N}_{\text{ac}}} P_{\text{ac-i}}^{G} - \sum_{j \in \mathbb{N}_{\text{dc}}} P_{\text{dc-}j}^{G}\right) \\
+ \lambda^{\text{IC}} \left(P_{\text{IC}}^{\text{ac}} - \sum_{k \in \mathbb{N}_{\text{IC}}} P_{\text{IC-}k}^{\text{ac}}\right) \\
+ \sum_{i \in \mathbb{N}_{\text{ac}}} \left[\alpha_{\text{ac-i}}^{+} \left(P_{\text{ac-i}}^{G} - P_{\text{ac-i}}^{G+}\right) + \alpha_{\text{ac-i}}^{-} \left(P_{\text{ac-i}}^{G-} - P_{\text{ac-i}}^{G}\right)\right] \\
+ \sum_{j \in \mathbb{N}_{\text{dc}}} \left[\alpha_{\text{dc-}j}^{+} \left(P_{\text{dc-}j}^{G} - P_{\text{dc-}j}^{G+}\right) + \alpha_{\text{IC-}k}^{-} \left(P_{\text{Ic-}k}^{G-} - P_{\text{ic-}k}^{G}\right)\right] \\
+ \sum_{k \in \mathbb{N}_{\text{IC}}} \left[\alpha_{\text{IC-}k}^{+} \left(P_{\text{IC-}k}^{\text{ac}} - P_{\text{IC-}k}^{\text{ac+}}\right) + \alpha_{\text{IC-}k}^{-} \left(P_{\text{IC-}k}^{\text{ac-}} - P_{\text{IC-}k}^{\text{ac}}\right)\right]$$

where the Lagrange multiplier  $\lambda^{\rm G}$  is associated with the power balance constraint (4.22b),  $\lambda^{\rm IC}$ with the power transfer constraint (4.22c),  $\{\alpha_{\text{ac-i}}^+, \alpha_{\text{ac-i}}^-, \alpha_{\text{dc-j}}^+, \alpha_{\text{dc-j}}^-\}$  with the maximum and minimum power outputs of DGs in equations (4.22d)-(4.22e), and  $\{\alpha_{\text{IC}-k}^+, \alpha_{\text{IC}-k}^-\}$  with the power limits of the ICs in equation (4.22f). Since the objective function of the optimization problem is strictly convex and the constraints are linear, if the problem is feasible then it has a unique globally optimal solution.

Now, considering (4.21) and (4.23), the KKT optimality conditions of the optimization problem are as follows:

 $Stationary\ condition:$ 

$$\begin{split} \frac{\partial \mathbb{L}}{\partial P_{\text{ac-i}}^{\text{G}}} &= \nabla C_{\text{ac-i}} \left( P_{\text{ac-i}}^{\text{G}} \right) - \lambda^{\text{G}} \left( \frac{2k_{\text{IC}}}{k_{\text{IC}} + 1} \right) - \lambda^{\text{IC}} \left( \frac{1}{k_{\text{IC}} + 1} \right) \\ &+ \alpha_{\text{ac-i}}^{+} - \alpha_{\text{ac-i}}^{-} = 0, \quad \text{i} \in \mathbb{N}_{\text{ac}} \end{split} \tag{4.24a}$$

$$\frac{\partial \mathbb{L}}{\partial P_{\mathrm{dc}-j}^{\mathrm{G}}} = \nabla C_{\mathrm{dc}-j} \left( P_{\mathrm{dc}-j}^{\mathrm{G}} \right) - \lambda^{\mathrm{G}} \left( \frac{2}{k_{\mathrm{IC}} + 1} \right) + \lambda^{\mathrm{IC}} \left( \frac{1}{k_{\mathrm{IC}} + 1} \right)$$

$$+\alpha_{\mathrm{dc}-j}^{+} - \alpha_{\mathrm{dc}-j}^{-} = 0, \quad j \in \mathbb{N}_{\mathrm{dc}}$$
 (4.24b)

$$\frac{\partial \mathbb{L}}{\partial P_{\mathrm{IC}-k}^{\mathrm{ac}}} = \lambda^{\mathrm{G}} k_{\mathrm{IC}-k}^{\mathrm{LOSS}} - \lambda^{\mathrm{IC}} \left( \frac{P_{\mathrm{IC}-k}^{\mathrm{ac}} - P_{\mathrm{IC}}^{\mathrm{ac}}}{P_{\mathrm{IC}-k}^{\mathrm{ac}}} \right) + \alpha_{\mathrm{IC}-k}^{+} - \alpha_{\mathrm{IC}-k}^{-} = 0, \quad k \in \mathbb{N}_{\mathrm{IC}}$$
(4.24c)

 $Complementary\ slackness:$ 

$$\alpha_{\text{ac-i}}^{+} \left( P_{\text{ac-i}}^{\text{G}} - P_{\text{ac-i}}^{\text{G+}} \right) = 0, \quad i \in \mathbb{N}_{\text{ac}}$$

$$(4.24d)$$

$$\alpha_{\mathrm{ac-i}}^{-} \left( P_{\mathrm{ac-i}}^{\mathrm{G-}} - P_{\mathrm{ac-i}}^{\mathrm{G}} \right) = 0, \quad i \in \mathbb{N}_{\mathrm{ac}}$$

$$(4.24e)$$

$$\alpha_{\mathrm{dc}-j}^{+} \left( P_{\mathrm{dc}-j}^{\mathrm{G}} - P_{\mathrm{dc}-j}^{\mathrm{G}+} \right) = 0, \quad j \in \mathbb{N}_{\mathrm{dc}}$$

$$(4.24f)$$

$$\alpha_{\mathrm{dc}-j}^{-} \left( P_{\mathrm{dc}-j}^{\mathrm{G}-} - P_{\mathrm{dc}-j}^{\mathrm{G}} \right) = 0, \quad j \in \mathbb{N}_{\mathrm{dc}}$$

$$(4.24g)$$

$$\alpha_{\mathrm{IC}-k}^{+} \left( P_{\mathrm{IC}-k}^{\mathrm{ac}} - P_{\mathrm{IC}-k}^{\mathrm{ac+}} \right) = 0, \quad k \in \mathbb{N}_{\mathrm{IC}}$$

$$(4.24h)$$

$$\alpha_{\mathrm{IC}-k}^{-} \left( P_{\mathrm{IC}-k}^{\mathrm{ac}-} - P_{\mathrm{IC}-k}^{\mathrm{ac}} \right) = 0, \quad k \in \mathbb{N}_{\mathrm{IC}}$$

$$(4.24i)$$

Primal feasibility:

$$(4.22b)$$
,  $(4.22c)$ ,  $(4.22d)$ ,  $(4.22e)$  and  $(4.22f)$ 

 $Dual\ feasibility:$ 

$$\alpha_{ac-i}^+, \alpha_{ac-i}^- \ge 0, \qquad i \in \mathbb{N}_{ac}$$
 (4.24j)

$$\alpha_{\mathrm{dc}-j}^+, \alpha_{\mathrm{dc}-j}^- \ge 0, \qquad j \in \mathbb{N}_{\mathrm{dc}}$$

$$(4.24k)$$

$$\alpha_{\mathrm{IC}-k}^+, \alpha_{\mathrm{IC}-k}^- \ge 0, \qquad k \in \mathbb{N}_{\mathrm{IC}}$$
 (4.241)

where  $k_{\rm IC}$  and  $P_{\rm IC}^{\rm ac}$  are variables from the group of ICs and will be explained in section 4.5.3.

Note that for each  $i \in \mathbb{N}_{ac}$ ,  $j \in \mathbb{N}_{dc}$  and  $k \in \mathbb{N}_{IC}$ , (4.24a), (4.24b) and (4.24c) can each be solved to obtain the Lagrange multipliers  $\lambda^{G}$  and  $\lambda^{IC}$ ; as useful notation, the corresponding solutions are denoted as

$$\lambda_{\mathrm{ac-i}}^{\mathrm{G}} := \frac{k_{\mathrm{IC}} + 1}{2k_{\mathrm{IC}}} \left( \nabla C_{\mathrm{ac-i}} \left( P_{\mathrm{ac-i}}^{\mathrm{G}} \right) + \alpha_{\mathrm{ac-i}}^{+} - \alpha_{\mathrm{ac-i}}^{-} \right) - \frac{1}{2k_{\mathrm{IC}}} \lambda^{\mathrm{IC}}, \quad \forall i \in \mathbb{N}_{\mathrm{ac}}$$

$$(4.25a)$$

$$\lambda_{\mathrm{dc}-j}^{\mathrm{G}} := \frac{k_{\mathrm{IC}} + 1}{2} \left( \nabla C_{\mathrm{dc}-j} \left( P_{\mathrm{dc}-j}^{\mathrm{G}} \right) + \alpha_{\mathrm{dc}-j}^{+} - \alpha_{\mathrm{dc}-j}^{-} \right) + \frac{1}{2} \lambda^{\mathrm{IC}}, \qquad \forall j \in \mathbb{N}_{\mathrm{dc}}$$
(4.25b)

$$\lambda_k^{\mathrm{IC}} := \frac{P_{\mathrm{IC}-k}^{\mathrm{ac}}}{P_{\mathrm{IC}-k}^{\mathrm{ac}} - P_{\mathrm{IC}}^{\mathrm{ac}}} \left[ (k_{\mathrm{IC}-k} - 1) \lambda^{\mathrm{G}} + \alpha_{\mathrm{IC}-k}^{+} - \alpha_{\mathrm{IC}-k}^{-} \right], \qquad \forall k \in \mathbb{N}_{\mathrm{IC}}$$
(4.25c)

where, by optimality, we must have that (4.26a) and (4.26b) hold. We can interpret  $\lambda_{\text{ac}-i}^{\text{G}}$  as a Lagrange multiplier for the  $i^{th}$  DG on the ac-side,  $\lambda_{\text{dc}-j}^{\text{G}}$  as a Lagrange multiplier for the  $j^{th}$  DG on the dc-side, and  $\lambda_k^{\text{IC}}$  as a Lagrange multiplier of the  $k^{th}$  IC. In this formulation, the DGs receive information from the ICs ( $k_{\text{IC}}$  and  $\lambda^{\text{IC}}$ ), while the ICs also receive information from the DGs ( $\lambda^{\text{G}}$ ); therefore, it is necessary to have communication links between the DGs and the ICs. This is further explained in the next Section.

$$\lambda^{G} = \lambda_{ac-i}^{G} = \lambda_{dc-j}^{G}, \quad \forall i \in \mathbb{N}_{ac}, \ \forall j \in \mathbb{N}_{dc}$$
 (4.26a)

$$\lambda^{\rm IC} = \lambda_k^{\rm IC}, \qquad \forall k \in \mathbb{N}_{\rm IC}$$
 (4.26b)

Based on the optimality conditions of the optimal dispatch problem, a distributed control strategy is designed with the objective of providing secondary variables regulation (i.e., frequency and voltage amplitude regulation in the ac-microgrid, and voltage regulation in the dc-microgrid), while driving the hybrid ac/dc-microgrid with multiple ICs to an optimal dispatch that complies with the KKT conditions (4.24).

# 4.5 Proposed Control Scheme for Multiple *ICs*

The distributed control scheme proposed in this Chapter aims to regulate the secondary variables in a hybrid ac/dc-microgrid, while maintaining optimality of dispatch. As described before, the control scheme is designed for the three components of the hybrid ac/dc-microgrid. On the other hand, the design of the control scheme is based on the convex optimization problem (4.22a), (4.22b), (4.22c), (4.22d), (4.22e) and (4.22f) presented in Section 4.4.2.

#### 4.5.1 Control scheme proposed for the ac-DGs

As explained in Section 4.3.1, the control scheme proposed for the ac-DGs is responsible for regulating the frequency on the ac-side of the microgrid, while minimizing the operation cost of all the DGs in the hybrid ac/dc-microgrid (4.27). Again, the reactive power injections are not considered since it is assumed that the ac-DSC described in Section 3.3 is implemented. Thus, this proposal focuses on frequency and dc-voltage control by means of optimally dispatching of real power of DG units.

$$\omega_{i} = \omega^* + M_{ac-i} P_{ac-i}^G + \Omega_{ac-i}^G$$

$$(4.27a)$$

$$\dot{\Omega}_{\mathrm{ac-i}}^{\mathrm{G}} = -k_{\mathrm{ac-i}}^{a} \left(\omega_{\mathrm{i}} - \omega^{*}\right) - k_{\mathrm{ac-i}}^{b} \sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{\mathrm{i}j} \left(\lambda_{\mathrm{ac-i}}^{\mathrm{G}} - \lambda_{\mathrm{ac-j}}^{\mathrm{G}}\right)$$

$$-k_{\mathrm{ac-i}}^{c} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ij} \left( \lambda_{\mathrm{ac-i}}^{\mathrm{G}} - \lambda_{\mathrm{dc-}j}^{\mathrm{G}} \right)$$
 (4.27b)

$$\dot{\alpha}_{\text{ac-i}}^{+} = \mu_{\text{ac-i}}^{a} \max \left\{ P_{\text{ac-i}}^{G} - P_{\text{ac-i}}^{G+} + \frac{k_{\text{ac-i}}^{d}}{\mu_{\text{ac-i}}^{a}} \alpha_{\text{ac-i}}^{+}, 0 \right\} - k_{\text{ac-i}}^{d} \alpha_{\text{ac-i}}^{+}$$
(4.27c)

$$\dot{\alpha}_{\text{ac-i}}^{-} = \mu_{\text{ac-i}}^{b} \max \left\{ P_{\text{ac-i}}^{\text{G-}} - P_{\text{ac-i}}^{\text{G}} + \frac{k_{\text{ac-i}}^{\text{e}}}{\mu_{\text{ac-i}}^{b}} \alpha_{\text{ac-i}}^{-}, 0 \right\} - k_{\text{ac-i}}^{\text{e}} \alpha_{\text{ac-i}}^{-}$$
(4.27d)

$$\lambda_{\text{ac-i}}^{\text{G}} = \frac{\hat{k}_{\text{i}}^{\text{IC}} + 1}{2\hat{k}_{\text{i}}^{\text{IC}}} \left( \nabla C_{\text{ac-i}} \left( P_{\text{ac-i}}^{\text{G}} \right) + \alpha_{\text{ac-i}}^{+} - \alpha_{\text{ac-i}}^{-} \right) - \frac{1}{2\hat{k}_{\text{i}}^{\text{IC}}} \hat{\lambda}_{\text{i}}^{\text{IC}}$$
(4.27e)

The proposed controller is similar to the one described in Section 4.3.1; therefore, the terms in (4.27) can be found in the aforementioned Section. The main change with respect to the controller proposed for the case with a single IC is in (4.27e). Here, the estimates of the variables from the ICs (i.e.,  $\hat{k}_{i}^{IC}$  and  $\hat{\lambda}_{i}^{IC}$ ) are calculated with the observers shown in (4.28), where variables  $\hat{k}_{IC-k}$  and  $\lambda_{k}^{IC}$  are received from the ICs and will be explained in Section 4.5.3. The term  $\lambda^{IC}$  corresponds to the (unique) dual variable associated with the power transfer through the ICs (4.22c).

$$\dot{\hat{k}}_{i}^{IC} = -\sum_{j \in \mathbb{N}_{ac}} a_{ij} \left( \hat{k}_{i}^{IC} - \hat{k}_{j}^{IC} \right) - \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left( \hat{k}_{i}^{IC} - \hat{k}_{j}^{IC} \right) - \sum_{k \in \mathbb{N}_{IC}} a_{ik} \left( \hat{k}_{i}^{IC} - \hat{k}_{IC-k} \right)$$
(4.28a)

$$\dot{\hat{\lambda}}_{i}^{IC} = -\sum_{j \in \mathbb{N}_{ac}} a_{ij} \left( \hat{\lambda}_{i}^{IC} - \hat{\lambda}_{j}^{IC} \right) - \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left( \hat{\lambda}_{i}^{IC} - \hat{\lambda}_{j}^{IC} \right) - \sum_{k \in \mathbb{N}_{IC}} a_{ik} \left( \hat{\lambda}_{i}^{IC} - \lambda_{k}^{IC} \right)$$
(4.28b)

In summary, the ac-DG<sub>i</sub> shares the following information with its neighbours:

- It sends the value of  $\lambda_{ac-i}^G$ ,  $i \in \mathbb{N}_{ac}$  to the ac-DGs, dc-DGs and ICs [see (4.27e)].
- It receives the value of  $\lambda_{\text{ac}-j}^{\text{G}}$ ,  $j \in \mathbb{N}_{\text{ac}}$ , from the ac-DGs [see (4.27b)].
- It receives the value of  $\lambda_{\mathrm{dc}-j}^{\mathrm{G}}$ ,  $j \in \mathbb{N}_{\mathrm{dc}}$ , from the dc-DGs [see (4.27b)].
- It sends/receives the values of  $\hat{k}_{\rm i}^{\rm IC}$  and  $\hat{\lambda}_{\rm i}^{\rm IC}$  to the ac-DGs and dc-DGs [see (4.28)].
- It receives the values of  $\lambda_k^{\text{IC}}$  and  $\hat{k}_{\text{IC}-k}$  from the *ICs* [see (4.30d) and (4.35)].

#### 4.5.2 Control scheme proposed for the dc-DGs

As explained in Section 4.3.2, the control scheme proposed for the dc-DGs is responsible for regulating the voltage of the dc-side of the microgrid, while minimizing the operation cost of all the DGs in the hybrid ac/dc-microgrid (4.15).

$$V_j = V^* + M_{dc-j} P_{dc-j}^G + \Omega_{dc-j}^G$$
(4.29a)

$$\dot{\Omega}_{\mathrm{dc}-j}^{\mathrm{G}} = -k_{\mathrm{dc}-j}^{a} \left( V_{j} - V^{*} \right) - k_{\mathrm{dc}-j}^{b} \sum_{\mathrm{i} \in \mathbb{N}_{\mathrm{dc}}} a_{\mathrm{i}j} \left( \lambda_{\mathrm{dc}-j}^{\mathrm{G}} - \lambda_{\mathrm{dc}-\mathrm{i}}^{\mathrm{G}} \right)$$

$$-k_{\mathrm{dc}-j}^{c} \sum_{\mathbf{i} \in \mathbb{N}_{\mathrm{ac}}} a_{\mathbf{i}j} \left( \lambda_{\mathrm{dc}-j}^{\mathrm{G}} - \lambda_{\mathrm{ac}-\mathbf{i}}^{\mathrm{G}} \right)$$
 (4.29b)

$$\dot{\alpha}_{dc-j}^{+} = \mu_{dc-j}^{a} \max \left\{ P_{dc-j}^{G} - P_{dc-j}^{G+} + \frac{k_{dc-j}^{d}}{\mu_{dc-j}^{a}} \alpha_{dc-j}^{+}, 0 \right\} - k_{dc-j}^{d} \alpha_{dc-j}^{+}$$
(4.29c)

$$\dot{\alpha}_{\mathrm{dc}-j}^{-} = \mu_{\mathrm{dc}-j}^{b} \max \left\{ P_{\mathrm{dc}-j}^{\mathrm{G}-} - P_{\mathrm{dc}-j}^{\mathrm{G}} + \frac{k_{\mathrm{dc}-j}^{\mathrm{e}}}{\mu_{\mathrm{dc}-j}^{b}} \alpha_{\mathrm{dc}-j}^{-}, 0 \right\} - k_{\mathrm{dc}-j}^{\mathrm{e}} \alpha_{\mathrm{dc}-j}^{-}$$
(4.29d)

$$\lambda_{\text{dc}-j}^{G} = \frac{\hat{k}_{j}^{\text{IC}} + 1}{2} \left( \nabla C_{\text{dc}-j} \left( P_{\text{dc}-j}^{G} \right) + \alpha_{\text{dc}-j}^{+} - \alpha_{\text{dc}-j}^{-} \right) + \frac{1}{2} \hat{\lambda}_{j}^{\text{IC}}$$
(4.29e)

The proposed controller is similar to the one described in Section 4.3.2; therefore, the terms in (4.29) can be found in the aforementioned Section. The main change with respect to the controller proposed for the case with a single IC is in (4.29e). Here, the estimates of the variables from the ICs (i.e.,  $\hat{k}_j^{\rm IC}$  and  $\hat{\lambda}_j^{\rm IC}$ ) are calculated with the observers shown in (4.28), where variables  $\hat{k}_{\rm IC-k}$  and  $\lambda_k^{\rm IC}$  are received from the ICs and will be explained in Section 4.5.3. The term  $\lambda^{\rm IC}$  corresponds to the (unique) dual variable associated with the power transfer through the ICs (4.22c).

In summary, the dc-DG<sub>i</sub> shares the following information with its neighbours:

- It sends the value of  $\lambda_{dc-j}^G$ ,  $j \in \mathbb{N}_{dc}$ , to the dc-DGs, ac-DGs and ICs [see (4.29e)].
- It receives the value of  $\lambda_{dc-i}^{G}$ ,  $i \in \mathbb{N}_{dc}$ , from the dc-DGs [see (4.29b)].
- It receives the value of  $\lambda_{\text{ac-i}}^{\text{G}}$ ,  $i \in \mathbb{N}_{\text{ac}}$ , from the *ac-DGs* [see (4.29b)].
- It sends/receives the values of  $\hat{k}_{j}^{\text{IC}}$  and  $\hat{\lambda}_{j}^{\text{IC}}$  to the dc-DGs and ac-DGs [see (4.28)].
- It receives the values of  $\lambda_k^{\rm IC}$  and  $\hat{k}_{{\rm IC}-k}$  from the ICs [see (4.30d) and (4.35)].

#### 4.5.3 Control scheme proposed for the *ICs*

In addition to contribute to the minimization of the operation cost by transferring power between both sides of the hybrid ac/dc-microgrid, now the ICs must dispatch the power being transferred in order to minimize the losses in this stage and, therefore, contribute to reach the optimal dispatch point. For achieving the two aforementioned control tasks, the following controller is proposed:

$$\dot{P}_{\text{IC}-k}^* = -\tau_{\text{IC}-k}^a \sum_{i \in \mathbb{N}_{\text{ac}}} \sum_{j \in \mathbb{N}_{\text{dc}}} a_{ik} a_{jk} \left( \lambda_{\text{ac}-i}^{\text{G}} - \lambda_{\text{dc}-j}^{\text{G}} \right) - \tau_{\text{IC}-k}^b \sum_{h \in \mathbb{N}_{\text{IC}}} a_{kh} \left( \lambda_k^{\text{IC}} - \lambda_h^{\text{IC}} \right)$$
(4.30a)

$$\dot{\alpha}_{\text{IC}-k}^{+} = \mu_{\text{IC}-k}^{a} \max \left\{ P_{\text{IC}-k}^{\text{ac}} - P_{\text{IC}-k}^{\text{ac}+} + \frac{\tau_{\text{IC}-k}^{a}}{\mu_{\text{IC}-k}^{a}} \alpha_{\text{IC}-k}^{+}, \ 0 \right\} - \tau_{\text{IC}-k}^{a} \alpha_{\text{IC}-k}^{+}$$
(4.30b)

$$\dot{\alpha}_{\text{IC}-k}^{-} = \mu_{\text{IC}-k}^{b} \max \left\{ P_{\text{IC}-k}^{\text{ac}-} - P_{\text{IC}-k}^{\text{ac}} + \frac{\tau_{\text{IC}-k}^{b}}{\mu_{\text{IC}-k}^{b}} \alpha_{\text{IC}-k}^{-}, \ 0 \right\} - \tau_{\text{IC}-k}^{b} \alpha_{\text{IC}-k}^{-}$$
(4.30c)

$$\lambda_k^{\rm IC} = \frac{P_{\rm IC-k}^{\rm ac}}{P_{\rm IC-k}^{\rm ac} - P_{\rm IC}^{\rm ac}} \left[ \left( k_{\rm IC-k} - 1 \right) \overline{\lambda^{\rm G}} + \alpha_{\rm IC-k}^+ - \alpha_{\rm IC-k}^- \right]$$

$$(4.30d)$$

where  $P_{\mathrm{IC}-k}^*$  is the power transferred through the  $k^{th}$  IC, which is positive when transferred from the dc-side to the ac-side, and negative in the opposite direction,  $\overline{\lambda^{\mathrm{G}}}$  is the average value of the variables  $\lambda_{\mathrm{ac}}^{\mathrm{G}}$  and  $\lambda_{\mathrm{dc}}^{\mathrm{G}}$  received from the DGs communicating with  $IC_k$ . The control actions  $\alpha_{\mathrm{IC}-k}^+$  and  $\alpha_{\mathrm{IC}-k}^-$  were already explained in Section 4.3.3 and are used to maintain the power transferred through  $IC_k$  within limits. The first term at the right-hand side in (4.30a) produces a power transfer from the cheapest side to the most expensive one by comparing the value of  $\lambda_{\mathrm{ac}}^{\mathrm{G}}$  and  $\lambda_{\mathrm{dc}}^{\mathrm{G}}$ . The second term at the right-hand side in (4.30a) changes the dispatch of the ICs until they reach the same value of  $\lambda_{\mathrm{IC}}^{\mathrm{IC}}$  ( $\lambda_{\mathrm{IC}}^{\mathrm{IC}} = \lambda_{k}^{\mathrm{IC}}$ ,  $\forall k \in \mathbb{N}_{\mathrm{IC}}$ ), which

corresponds to the (unique) dual variable associated with the power transfer equation of the hybrid ac/dc-microgrid's optimal dispatch problem, (4.22c).

For the implementation of the controllers proposed in (4.27e) and (4.29e), it is necessary to know a constant related to the power transferred through the ICs in a hybrid ac/dc-microgrid with multiple ICs. Specifically, the constant  $k_{\rm IC}$  must be known by all the ac-DGs [see (4.27e)], by all the dc-DGs [see (4.29e)] and by all the ICs [see (4.30d)]. This constant can be easily calculated in a hybrid ac/dc-microgrid with a single IC as the power in the dc-side of the IC over that at the ac-side, as described in (4.2a) and repeated in (4.31) for reader's convenience. However, the calculation of  $k_{\rm IC}$  is more complicated when multiple ICs and a distributed communication network are considered since each IC does not know the amount of power being transferred through all the others.

$$k_{\rm IC} = \frac{P_{\rm IC}^{\rm dc}}{P_{\rm IC}^{\rm ac}} \tag{4.31}$$

where  $P_{\text{IC}}^{\text{dc}}$  is the total power at the dc-side of the ICs,  $P_{\text{IC}}^{\text{ac}}$  is the total power at the ac-side of the ICs, and  $k_{\text{IC}}$  is the relationship between the two aforementioned variables.

In this case, the total power transferred through the ICs in a hybrid ac/dc-microgrid can be estimated with local information from the  $IC_k$  ( $k \in \mathbb{N}_{IC} = \{n+m+1, \ldots, n+m+g\}$ ) and its neighbouring ICs [94,137,138]. Let  $\hat{\Gamma}_k$  be the estimate of the variable  $\Gamma$  (e.g., the total power transferred through the ICs at the ac-side  $P_{IC}^{ac}$  or the dc-side  $P_{IC}^{dc}$ ), let  $\gamma_k$  be the local variable ( $P_{IC-k}^{ac}$  or  $P_{IC-k}^{dc}$ ), let  $a_{kh}$  be the element of the adjacency matrix which represent the communication channel between  $IC_k$  and  $IC_h$  ( $k, h \in \mathbb{N}_{IC} = \{n+m+1, \ldots, n+m+g\}$ ), and let g be the total number of ICs in the hybrid ac/dc-microgrid. Then, the total power transferred through the ICs can be estimated with a distributed observer [137], as follows:

$$\hat{\Gamma}_k = g \cdot \gamma_k + \int \sum_{h \in \mathbb{N}_{IC}} a_{kh} \left( \hat{\Gamma}_k(\tau) - \hat{\Gamma}_h(\tau) \right) d\tau$$
(4.32)

Considering the observer in (4.32), the following observers are proposed to estimate the total power transferred between the two sides of the hybrid microgrid with multiple ICs.

$$\hat{P}_{\text{IC}-k}^{\text{ac}} = g \cdot P_{\text{IC}-k}^{\text{ac}} + \int \sum_{h \in \mathbb{N}_{\text{IC}}} a_{kh} \left( \hat{P}_{\text{IC}-k}^{\text{ac}}(\tau) - \hat{P}_{\text{IC}-h}^{\text{ac}}(\tau) \right) d\tau$$

$$(4.33a)$$

$$\hat{P}_{\text{IC}-k}^{\text{dc}} = g \cdot P_{\text{IC}-k}^{\text{dc}} + \int \sum_{h \in \mathbb{N}_{\text{IC}}} a_{kh} \left( \hat{P}_{\text{IC}-k}^{\text{dc}}(\tau) - \hat{P}_{\text{IC}-h}^{\text{dc}}(\tau) \right) d\tau \tag{4.33b}$$

According to [138], all estimates would converge to a consensus value as follows:

$$\hat{P}_{\text{IC}-1}^{\text{ac}} = \hat{P}_{\text{IC}-2}^{\text{ac}} = \dots = \hat{P}_{\text{IC}-g}^{\text{ac}} = \sum_{k \in \mathbb{N}_{\text{IC}}} P_{\text{IC}-k}^{\text{ac}}$$
(4.34a)

$$\hat{P}_{\text{IC}-1}^{\,dc} = \hat{P}_{\text{IC}-2}^{\,dc} = \dots = \hat{P}_{\text{IC}-g}^{\,dc} = \sum_{k \in \mathbb{N}_{\text{IC}}} P_{\text{IC}-k}^{\text{dc}}$$
(4.34b)

Therefore, the  $k^{th}$  IC can estimate the constant  $\hat{k}_{\text{IC}-k}$  for implementing the controller proposed in (4.32) as follows:

$$\hat{k}_{\text{IC}-k} = \frac{\hat{P}_{\text{IC}-k}^{\text{dc}}}{\hat{P}_{\text{IC}-k}^{\text{ac}}}, \qquad \forall k \in \mathbb{N}_{\text{IC}}$$

$$(4.35)$$

The *ICs* share the following information with its neighbours:

- It sends the values of  $\lambda_k^{\text{IC}}$  and  $\hat{k}_{\text{IC}-k}$  to the ac-DGs and dc-DGs [see (4.27e) and (4.29e)].
- It receives the value of  $\lambda_i^{ac}$ ,  $i \in \mathbb{N}_{ac}$  from the ac-DGs [see (4.30a)].
- It receives the value of  $\lambda_j^{\text{dc}}$ ,  $j \in \mathbb{N}_{\text{dc}}$  from the dc-DGs [see (4.30a)].
- It receives/sends the value of  $\lambda_h^{\text{IC}}$ ,  $h \in \mathbb{N}_{\text{IC}}$  from/to the ICs [see (4.30a)].
- It receives/sends the values of  $\hat{P}_{IC-k}^{ac}$  and  $\hat{P}_{IC-k}^{dc}$ ,  $h \in \mathbb{N}_{IC}$  from/to the ICs [see (4.34)].

## 4.6 Summary

In this Chapter, a new consensus-based distributed secondary control strategy for hybrid ac/dc-microgrids has been proposed. In the same way as the control strategy proposed in Chapter 3, this strategy also considers the hybrid ac/dc-microgrid as a single entity. The strategy is capable of restoring the variables modified by the primary control loop to their nominal values, while optimizing the dispatch of the DGs, in order to minimize the operation cost of the hybrid ac/dc-microgrid. Additionally, when the hybrid ac/dc-microgrid has multiple ICs, the power transfer is also dispatched between them for contributing to the total cost minimization.

The proposed strategy also considers a reduced communication layer, as each agent in the hybrid ac/dc-microgrid (i.e., DGs and ICs) is communicating only with its neighbouring DGs and ICs. The proposed controller is capable of driving the microgrid to the optimal operation point while considering the output limits of DGs and ICs. The tuning of the controllers proposed in this Chapter is addressed in Chapter 6. The validation of the proposed control strategy is performed in Chapter 8 using a simulated hybrid ac/dc-microgrid.

# Chapter 5

# Experimental ac/dc-Microgrid

#### 5.1 Introduction

This Chapter describes the design and construction of the experimental hybrid ac/dc-microgrid used for validating the distributed secondary control strategies for power-sharing proposed in this thesis (see Section 3). Details of the different components of the microgrid are presented with emphasis on the DGs, ICs and loads<sup>1</sup>. The emulation of DGs is performed by real-time controlled power units whose manufacturer is the company Triphase, part of National Instruments. Each Triphase unit consists of two voltage-source converters (VSCs) connected in back-to-back configuration through a DC-Link, fed by the main grid through an isolation transformer [29].

This Chapter is organized as follows: Section 5.2 details the main characteristics of the ac-side of the microgrid, while the dc-side is described in Section 5.3. The IC for connecting the two sides is described in Section 5.4. Finally, Section 5.5 presents the experimental hybrid ac/dc-microgrid built to carry out the studies reported in this thesis and Section 5.6 provides a summary of the Chapter.

<sup>&</sup>lt;sup>1</sup>https://www.die.cl/sitio/home/investigacion/laboratorios/laboratorio-de-control-de-micro-redes/

#### 5.2 ac-Microgrid

The ac-microgrid is emulated using the Triphase units shown in Fig. 5.1, which are called "PM15F120C" and "PM5F60R". The first unit is composed of 4 VSCs (VSC1, VSC2, VSC3, VSC4) as shown in Fig. 5.1a. This unit allows to emulate 2 independent three-phase ac-DGs utilizing VSC3 and VSC4. The second unit is composed of 2 inverters (VSC1, VSC2) as shown in Fig. 5.1b. This unit allows to emulate 1 three-phase ac-DG utilizing VSC2. Therefore, it is possible to emulate a three-phase three-wire ac-microgrid composed of three ac-DGs, as explained in next section.

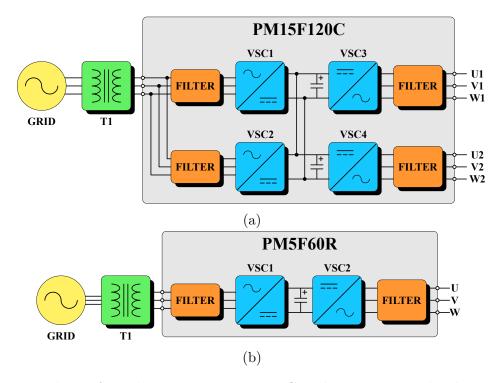


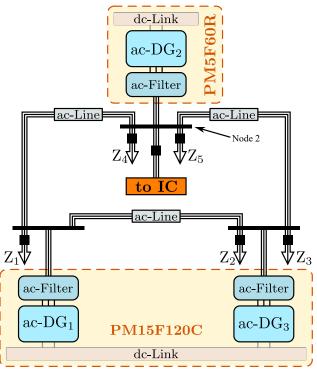
Figure 5.1: Topology of Triphase units PM15F120C and PM5F60R utilized to emulate the *ac*-microgrid.

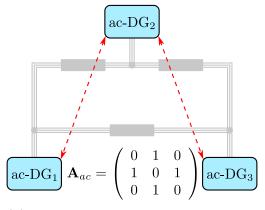
#### 5.2.1 Topology

Considering an *ac*-microgrid composed of three *ac*-DGs (each DG has its own *ac*-Filter), the topology shown in Fig. 5.2a is proposed. It is important to note that *ac*-Lines are emulated with inductors; therefore, the resistive component of the *ac*-Lines is small and corresponds to the inductor's resistance.

The loads include both controllable  $(Z_1; Z_3)$  and fixed  $(Z_2; Z_4; Z_5)$  loads. Controllable loads are emulated using back-to-back converters (Triphase unit "PM15I30F60") to be able to produce active and reactive load impacts, while the fixed loads are resistive load banks.

The orange box indicates the connection point for the IC, which is connected to node 2 of the ac-microgrid. The IC is connected through an ac circuit breaker (small black box in Fig. 5.2a); thus, it is possible to disconnect it from the ac-microgrid. The IC will be further explained in Section 5.4.





(b) Communication network of the *ac*-microgrid proposed in Fig. 5.2a.

(a) Proposed topology of an *ac*-microgrid composed of three *ac*-DGs.

Figure 5.2: Proposed *ac*-microgrid.

#### 5.2.2 Communication Network

In Fig. 5.2b the distributed communication network is shown and the communication links have been arbitrarily assigned. The bidirectional communication links are depicted by the red arrows. The adjacency matrix for this topology ( $\mathbf{A}_{ac}$ ) is also shown in the Fig. In addition,  $ac\text{-DG}_1$  and  $ac\text{-DG}_3$  have communication links to the IC, and to  $dc\text{-DG}_2$  and  $dc\text{-DG}_4$ , respectively, while  $ac\text{-DG}_2$  has communication link to  $dc\text{-DG}_6$  only, as will be explained in Section 5.5. The communication links between  $ac\text{-DG}_3$  and  $dc\text{-DG}_3$  are not shown in Fig. 5.2b.

#### 5.3 dc-Microgrid

The dc-microgrid is emulated using the Triphase unit called "PM15I60F06". This unit is composed of 2 VSC (VSC1 and VSC2) and 2 DC/DC converters (DCDC1 and DCDC2), each composed of three channels, as shown in Fig. 5.3. This unit allows to emulate 6 independent dc-DGs utilizing DCDC1 and DCDC2 ( $DCi\pm$ ,  $i \in \{1, ..., 6\}$ ). Therefore, it is possible to emulate a dc-microgrid composed of six dc-DGs, as explained in the following.

#### 5.3.1 Topology

Considering a dc-microgrid composed of six dc-DGs (each DG has its own dc-Filter), the topology shown in Fig. 5.4 is proposed. It is important to note that dc-Lines are mainly resistive; however, in this experimental rig, inductors in series with the resistors were also

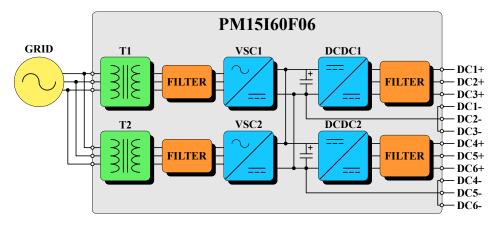


Figure 5.3: Topology of Triphase unit PM15I60F06 utilized to emulate the dc-microgrid.

considered to avoid current peaks due to the switching of power converters. In this case, all the loads  $(R_1, \ldots, R_6)$  are fixed resistive load banks.

The IC is connected to node 4 of the dc-microgrid (orange box) through a dc circuit breaker (small black box); thus, it is possible to disconnect it from the dc-microgrid. The IC is further explained in Section 5.4.

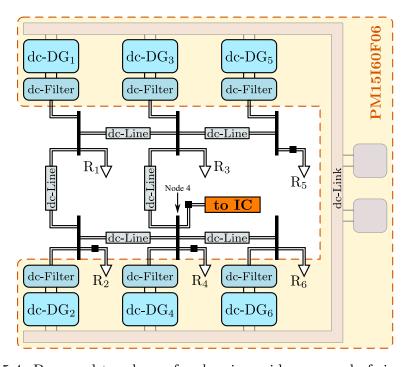


Figure 5.4: Proposed topology of a dc-microgrid composed of six dc-DGs.

#### 5.3.2 Communication Network

In Fig. 5.5, it can be seen that the distributed communication network has been arbitrarily assigned (see red arrows). The adjacency matrix for this topology ( $\mathbf{A}_{dc}$ ) is also shown in the same Fig. Additionally, dc-DG<sub>2</sub>, dc-DG<sub>4</sub> and dc-DG<sub>6</sub> have communication links to the IC and to ac-DG<sub>1</sub>, ac-DG<sub>2</sub> and ac-DG<sub>3</sub>, respectively, as will be explained in Section 5.5. The communication links between dc-DGs and ac-DGs (or ICs) are not shown in Fig. 5.5.

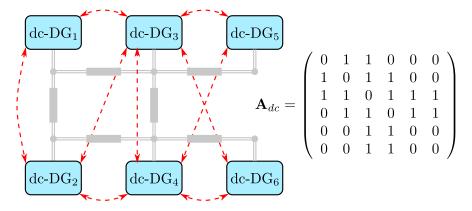


Figure 5.5: Communication network of the dc-microgrid proposed in Fig. 5.4.

# 5.4 Interlinking Converter (IC)

The IC is emulated using the Triphase unit shown in Fig. 5.6, which is called "PM5F42R". This unit is composed of one VSC (VSC1), one DC/DC converter (DCDC1), and 2 diode rectifiers for black starting the module, as shown in Fig. 5.6. This unit has one ac-port (U1,V1,W1) and two dc-ports (D1 $\pm$ ,D2 $\pm$ ), which allows to emulate an ac/dc back-to-back converter. The ac-port is connected to the ac-microgrid, while the two dc-ports are connected (in parallel) to the dc-microgrid. Therefore, it is possible to emulate an IC and to interconnect the ac- and dc-side of the hybrid ac/dc-microgrid.

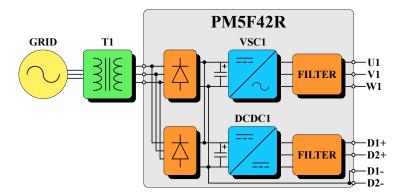


Figure 5.6: Topology of Triphase unit PM5F42R utilized to emulate the IC.

#### 5.4.1 Topology and Communication Network

As mentioned before, the IC is connected to node 2 of the ac-microgrid and to node 4 of the dc-microgrid, as shown in Fig. 5.7. Moreover, the IC has communication links to dc-DG<sub>2</sub>, dc-DG<sub>4</sub>, dc-DG<sub>6</sub>, ac-DG<sub>1</sub> and ac-DG<sub>2</sub> (see red arrows).

# 5.5 Hybrid ac/dc-Microgrid

With the information provided in Sections 5.2, 5.3 and 5.4, it is possible to build a 24.0kW hybrid ac/dc-experimental microgrid. This microgrid has three main stages:

• One *ac*-microgrid composed of 3 *ac*-DGs.

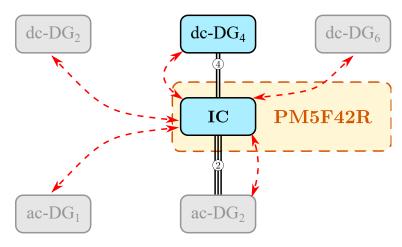


Figure 5.7: Proposed topology and communication network of the IC.

- One dc-microgrid composed of 6 dc-DGs.
- One *IC* to connect the two sub-microgrids.

#### 5.5.1 Topology

A simplified diagram of the hybrid ac/dc-microgrid implemented in the Microgrid's Control Laboratory, University of Chile, is presented in Fig. 5.8, and a photograph of it is depicted in Fig. 5.9. General parameters for the hybrid ac/dc-microgrid are given in Table 5.1 and Table 5.2, while the parameters of the controllers are given in Table 5.3 (for the power-sharing strategy) and Table 5.4 (for the operation cost minimization strategy). The parameters of the controllers were tuned following the procedure described in Chapter 6.

Table 5.1: Experimental ac/dc-microgrid, general parameters.

Description	dc	ac	IC
# of DGs	6	3	1
Nominal Voltage (V)	150	110*	-
Frequency (Hz)	_	50	-
Nominal Power (kW/unit)	2.5	3.0	3.0
Switching frequency (kHz)		16	
Communication rate (Hz)		100	

<sup>\*</sup> Phase-to-neutral RMS voltage

Table 5.2: Experimental ac/dc-microgrid, parameters of the lines.

Line	Ω	Line	Ω	Line	$\Omega$
$R_{12}$	0.67	$R_{34}$	0.50	$\overline{Z_{12}}$	0.10 + j0.79
$R_{13}$	0.78	$R_{35}$	0.94	$Z_{13}$	0.10 + j0.79
$R_{24}$	0.50	$R_{46}$	0.47	$Z_{23}$	0.10 + j0.79

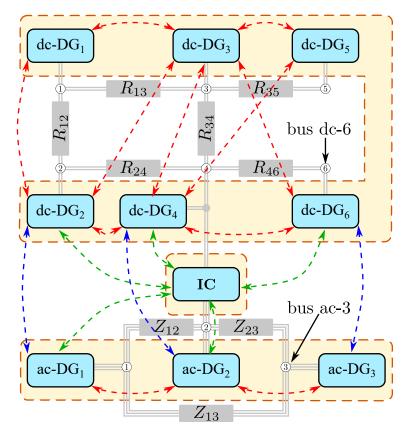


Figure 5.8: Topology of the hybrid ac/dc-microgrid under study, including the communication channels among DGs.



Figure 5.9: Experimental system based on five Triphase power converters, nominal powers 5-30kW (each one). They are configured to emulate the topology of Fig. 5.8.

Table 5.3: Parameter of the controllers for power-sharing strategy.

Param.	$dc ext{-DGs}$	(3.9)	 Param.	$ac ext{-DGs}$	(3.4)- $(3.5)$
$\gamma_{ m i}$	12.0	(1/s)	$1/\sigma_{ m i}$	50.0	(s)
$c_{ m i}$	50.0	(V/s)	$eta_{ m i}$	12.0	(1/s)
$M_{ m dc-i}$	-3E-3	(V/W)	$b_{ m i}$	500.0	(V/s)
$\omega_c$	12.566	$(\mathrm{rad/s})$	$M_{\rm ac-i}$	-2.1E-3	$(\mathrm{rad/sW})$
Param.	IC	(3.10)	$N_{\rm ac-i}$	-1.8E-3	(V/VAr)
$1/ au_{ m i}$	50.0	(1/W)	$\omega_c$	12.566	$(\mathrm{rad/s})$

Table 5.4: Parameter of the controllers for cost minimization strategy.

Param.	$dc ext{-DGs}$	(4.15)	Param.	ac-DGs	(4.13)
$k_{\mathrm{dc}-j}^a$	0.1	(1/s)	$k_{\rm ac-i}^a$	0.1	(1/s)
$k_{\mathrm{dc}-j}^b, k_{\mathrm{dc}-j}^c$	1.0	(1/s)	$k_{\text{ac-i}}^b, k_{\text{ac-i}}^c$	0.1	(1/s)
$k_{\mathrm{dc}-j}^{\mathrm{d}}, k_{\mathrm{dc}-j}^{\mathrm{e}}$	0.2		$k_{ m ac-i}^{ m d}, k_{ m ac-i}^{ m e}$	0.2	
$\mu_{\mathrm{dc}-j}^a, \mu_{\mathrm{dc}-j}^b$	2.5E-3		$\mu_{\mathrm{ac-i}}^a, \mu_{\mathrm{ac-i}}^b$	2.5E-3	
$M_{ m dc-i}$	-3E-3	(V/W)	$M_{ m ac-i}$	-2.1E-3	(rad/sW)
Param.	IC	(4.16)	$eta_{ m i}$	12.0	(1/s)
$1/ au_{ m IC}$	250.0	(1/W)	$b_{ m i}$	50.0	(V/s)
$ au_{ ext{IC}}^a, au_{ ext{IC}}^b$	0.2		$N_{ m ac-i}$	-1.8E-3	(V/VAr)
$\mu_{ ext{IC}}^a, \mu_{ ext{IC}}^b$	2.5E-3		$\overline{\omega_c}$	12.566	(rad/s)

#### 5.5.2 Communication Network

Considering the communication links described before, the *adjacency matrix* (**A**) for the hybrid ac/dc-microgrid is shown in (5.1).

The adjacency matrix is composed of nine sub-matrices. The communication links for units within the sub-microgrids, i.e., ac-DGs and dc-DGs, are shown in sub-matrices  $[\mathbf{ac}]_{3\times 3}$  and  $[\mathbf{dc}]_{6\times 6}$ , respectively, while the communication links between units from different sub-microgrids are described in sub-matrices  $[\mathbf{ac}\mathbf{-dc}]_{3\times 6}$  and  $[\mathbf{dc}\mathbf{-ac}]_{6\times 3}$ .

In this case, the hybrid ac/dc-microgrid has a single IC. However, in case more ICs are considered in the microgrid, the communication links between the ICs are represented in submatrix  $[\mathbf{ic}]_{1\times 1}$ . Finally, the communication links between the ICs and the DGs are shown in sub-matrices  $[\mathbf{ac}\mathbf{-ic}]_{3\times 1}$ ,  $[\mathbf{dc}\mathbf{-ic}]_{6\times 1}$ ,  $[\mathbf{ic}\mathbf{-ac}]_{1\times 3}$  and  $[\mathbf{ic}\mathbf{-dc}]_{1\times 6}$ .

## 5.6 Summary

This Chapter has presented the design and implementation of a 24.0kW experimental hybrid ac/dc-microgrid. This prototype is used to validate the consensus-based distributed secondary control systems proposed in Chapter 3 and Chapter 4.

The main characteristics of the elements making the hybrid ac/dc-microgrid have been described, including information regarding the parameters of the microgrid and the controllers. Furthermore, the communication links between the DGs and the IC were also identified, summarized in the adjacency matrix (5.1).

In the next Chapter, an analytical model of the closed-loop system of the microgrid described in this Chapter is presented.

# Chapter 6

# Closed-loop Model of a Hybrid ac/dc-Microgrid with the Proposed Controllers

In this Chapter, an analytical model of the closed-loop system of the microgrid presented in Chapter 5 is derived. This model is utilized for performing a small-signal analysis of the consensus-based distributed control strategies proposed in Chapter 3 for power-sharing and Chapter 4 for operation cost minimization. The following simplifications were considered since their high-bandwidth dynamics are not relevant on the time-scale of the proposed controllers:

- The inner control loop (voltage/current controllers) is not considered.
- The modulation technique of the converters (PWM, SVM) is not considered.
- The switching of the power converter devices (IGBTs, Mosfets) is not considered.

Chapter 6 is organized as follows: Section 6.1 presents the closed-loop model for the hybrid ac/dc-microgrid with a single IC. Then, the control strategies proposed in this thesis are applied to the model of the microgrid: firstly, the closed-loop model of the control strategy for power-sharing is presented in Section 6.2; secondly, the control strategy for operation cost minimization is explained in Section 6.3, and small-signal analysis of the control strategies proposed in this thesis is performed in Section 6.4 and Section 6.5.

## 6.1 Closed-loop model for a Hybrid ac/dc-Microgrid

In this Section, the analytical model of the closed-loop system considering a consensus-based distributed control strategy proposed for power-sharing in a hybrid ac/dc-microgrid (see Chapter 3) is presented. The experimental hybrid ac/dc-microgrid built in the Microgrid's Control Laboratory<sup>1</sup> (University of Chile) is considered (see Chapter 5), i.e., the topology

<sup>&</sup>lt;sup>1</sup>https://www.die.cl/sitio/home/investigacion/laboratorios/laboratorio-de-control-de-micro-redes/

with a single IC is analyzed [29]. In the following, the procedure for obtaining the closed-loop model is described and its most relevant equations are depicted. The complete description of this methodology is explained in Appendix A and Appendix B.

A general model for a hybrid ac/dc-microgrid, which is composed of  $\mathbb{N}_{ac}$  ac-DGs,  $\mathbb{N}_{dc}$  dc-DGs and a single IC, with a consensus-based distributed secondary control strategy is defined by the equations (6.1), (6.2), (6.3) and (6.5), and it is illustrated in Fig. 6.1. Since the microgrid model analyzed is a general model, the closed-loop model is independent of the objective of the controller; therefore, it works for either the power-sharing strategy (in green boxes) or the operation cost minimization strategy (in orange boxes).

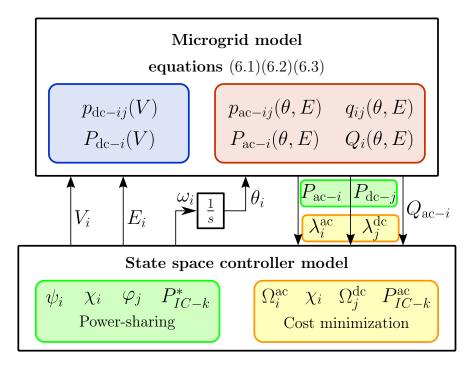


Figure 6.1: Closed-loop system of the hybrid ac/dc-microgrid.

The active power flows in the ac-microgrid are characterized by:

$$p_{\text{ac-i}j}(\theta, E) = \frac{R_{ij}E_i^2 - R_{ij}\Theta_{ij} + X_{ij}\Omega_{ij}}{R_{ij}^2 + X_{ij}^2}$$
(6.1a)

$$p_{\text{ac-i}}(\theta, E) = p_{\text{ac-Di}} + \sum_{j \in \mathbb{N}_{\text{ac}}} p_{\text{ac-i}j}(\theta, E) - s_{i} P_{\text{IC}}$$
(6.1b)

$$\dot{\theta}_{\rm i} = \omega_{\rm i} \tag{6.1c}$$

$$\Theta_{ij} := E_i E_j \cos(\theta_i - \theta_j) \tag{6.1d}$$

$$\Omega_{ij} := E_i E_j \sin(\theta_i - \theta_j) \tag{6.1e}$$

where  $p_{\text{ac-i}j}(\theta, E)$  is the active power flow in the line from bus i to bus j,  $R_{ij}$  and  $X_{ij}$  are the line resistance and reactance, respectively, and  $\Theta_{ij}$  and  $\Omega_{ij}$  are defined in (6.1d) and (6.1e), respectively. Finally,  $E_{i}$ ,  $\theta_{i}$  and  $\omega_{i}$  are the ac-voltage amplitude, angle and frequency at ac-bus i. The active power supplied by the  $i^{th}$  ac-DG ( $p_{ac-i}$ ) is given by (6.1b), where  $p_{ac-Di}$  corresponds to the active power required by the local load connected to bus i, respectively.

The reactive power flows in the ac-microgrid are given by:

$$q_{ij}(\theta, E) = \frac{X_{ij}E_i^2 - X_{ij}\Theta_{ij} - R_{ij}\Omega_{ij}}{R_{ij}^2 + X_{ij}^2}$$
(6.2a)

$$q_{\rm i} = q_{\rm Di} + \sum_{j \in \mathbb{N}_{\rm ac}} q_{\rm ij}(\theta, E) \tag{6.2b}$$

where  $q_{ij}(\theta, E)$  is the reactive power flow in the line from bus i to bus j and the remaining variables were previously explained. The reactive power supplied by the i<sup>th</sup> ac-DG  $(q_i)$  is given by (6.2b), where  $q_{Di}$  corresponds to the reactive power required by the local load connected to bus i.

The power flows in the dc-microgrid (blue box in Fig. 6.1) are described by:

$$p_{dc-ji}(V) = y_{ji}V_{j}(V_{j} - V_{i})$$
(6.3a)

$$p_{\mathrm{dc}-j}(V) = p_{\mathrm{dc}-Dj} + \sum_{i \in \mathbb{N}_{\mathrm{dc}}} p_{\mathrm{dc}-ji}(V) + s_j P_{\mathrm{IC}}$$

$$(6.3b)$$

where  $p_{dc-ji}(V)$  is the power flow in the dc-line from bus j to bus i,  $y_{ji}$  the line conductance, and  $V_j$  and  $V_i$  the dc-voltages at buses j and i, respectively. The active power supplied by the  $j^{th}$  dc-DG  $(p_{dc-j})$  is given by (6.3b), where  $p_{dc-Dj}$  corresponds to the power required by the local load connected to bus j.

The active power contribution of the IC is reflected in (6.1b) and (6.3b) by the inclusion of the variable  $s_i$ , which is 1 if the IC is connected to bus i and 0 otherwise.

Now, using the equations obtained for the power injections of the DGs, i.e., (6.1b), (6.2b) and (6.3b), a linearized model of the closed-loop system is derived. The operating point used for the linearization is obtained from a case study in which the DGs and the IC are within limits, and the power through the IC is not zero. In vector notation we have:

$$\Delta p_{\rm ac} = G_1^{\rm ac} \Delta E + G_2^{\rm ac} \Delta \theta + G_0^{\rm ac} \Delta P_{\rm IC}$$
 (6.4a)

$$\Delta q_{\rm ac} = G_3^{\rm ac} \Delta E + G_4^{\rm ac} \Delta \theta \tag{6.4b}$$

$$\Delta p_{\rm dc} = G_1^{\rm dc} \Delta V + G_0^{\rm dc} \Delta P_{\rm IC} \tag{6.4c}$$

where  $p_{ac}, q_{ac} \in \mathbb{R}^n$  are the vectors of active and reactive power generated by the ac-DGs (respectively),  $p_{dc} \in \mathbb{R}^m$  is the vector of power generated by the dc-DGs,  $E \in \mathbb{R}^n$  is the vector of bus potentials in the ac-microgrid,  $\theta \in \mathbb{R}^n$  is the vector of bus angles,  $V \in \mathbb{R}^m$  is the vector of bus potentials in the dc-microgrid, and  $P_{IC}$  is the power flow through the IC, which is positive when it flows from the dc-microgrid to the ac-microgrid.

Matrices  $G_1^{\text{ac}}$ ,  $G_2^{\text{ac}}$ ,  $G_3^{\text{ac}}$ ,  $G_4^{\text{ac}} \in \mathbb{R}^{n \times n}$  are composed of partial derivatives of  $p_{\text{ac}}$  and  $q_{\text{ac}}$  with respect to E and  $\theta$ , and depend on both the operating point  $(E^0, \theta^0)$  and the admittance matrix of the ac electrical system  $(Y^{\text{ac}})$ . Matrix  $G_1^{\text{dc}} \in \mathbb{R}^{m \times m}$  is composed of partial derivatives of  $p_{\text{dc}}$  with respect to V, and depend on both the operating point  $(V^0)$  and the conductance matrix of the dc electrical system  $(Y^{\text{dc}})$ . Finally,  $G_0^{\text{ac}} = (0 - 1 \ 0)^T$  is the vector describing the electrical connection of the ac-microgrid and the IC, and  $G_0^{\text{dc}} = (0 \ 0 \ 0 \ 1 \ 0 \ 0)^T$  is the vector describing the electrical connection of the dc-microgrid and the IC.

The low-pass-filter (LPF) measurements typically utilized in the primary (droop) control loop are considered in this closed-loop model, with cut-off frequencies of  $\omega_{c-i}$  and  $\omega_{c-j}$ :

$$\dot{P}_{\text{ac-i}} = -\omega_{c-i}(P_{\text{ac-i}} - p_{\text{ac-i}}) \tag{6.5a}$$

$$\dot{Q}_{\mathrm{ac-i}} = -\omega_{c-i}(Q_{\mathrm{ac-i}} - q_{\mathrm{ac-i}}) \tag{6.5b}$$

$$\dot{P}_{dc-j} = -\omega_{c-j}(P_{dc-j} - p_{dc-j}) \tag{6.5c}$$

where  $p_{\text{ac-i}}$ ,  $q_{\text{ac-i}}$  and  $p_{\text{dc-j}}$  are the instantaneous power to be filtered out by the LPFs, and  $P_{\text{ac-i}}$ ,  $Q_{\text{ac-i}}$  and  $P_{\text{dc-j}}$  are the output of the LPFs.

Linearizing (6.5) and using (6.4), in vector notation we obtain:

$$\Delta \dot{P}_{ac} = [\omega_c] G_1^{ac} \Delta E + [\omega_c] G_2^{ac} \Delta \theta + [\omega_c] G_0^{ac} \Delta P_{IC} - [\omega_c] \Delta P_{ac}$$
(6.6a)

$$\Delta \dot{Q}_{ac} = [\omega_c] G_3^{ac} \Delta E + [\omega_c] G_4^{ac} \Delta \theta - [\omega_c] \Delta Q_{ac}$$
(6.6b)

$$\Delta \dot{P}_{\rm dc} = [\omega_c] G_1^{\rm dc} \Delta V + [\omega_c] G_0^{\rm dc} \Delta P_{\rm IC} - [\omega_c] \Delta P_{\rm dc}$$
(6.6c)

where  $\omega_c \in \mathbb{R}^n$  is the vector of cutoff frequencies, and  $[\omega_c] \in \mathbb{R}^{n \times n}$  denotes the diagonal matrix with  $\omega_c$  on the diagonal. This notation is used in the rest of the Chapter for referring to diagonal matrices. The expressions obtained for the power injections (6.6) will be used in the following Sections to obtain the closed-loop model for the hybrid ac/dc-microgrid with the proposed control strategies.

#### 6.2 Control Strategy Proposed for Power-sharing

Now, the consensus-based distributed secondary control strategy for power-sharing is applied to the hybrid ac/dc-microgrid with a single IC. The process for obtaining the analytical closed-loop model is detailed in Appendix A.

The controllers proposed in Chapter 3 are applied to the hybrid ac/dc-microgrid model obtained in (6.6) to later derive the closed-loop model for the proposed control strategy. The expressions for the controllers are repeated for reader convenience.

#### Controller for the i<sup>th</sup> ac-DG $\forall i \in \mathbb{N}_{ac}$ :

Frequency reference (3.4a):

$$\dot{\theta}_{i} = \omega_{i} = \omega^{*} + M_{\text{ac-}i} P_{\text{ac-}i} + \psi_{i} \tag{6.7a}$$

Frequency secondary control (3.4b):

$$\dot{\psi}_{i} = -\sigma_{i} \left(\omega_{i} - \omega^{*}\right) - \sigma_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(P_{ac-i} - P_{ac-j}\right) - \sigma_{i} \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left(P_{ac-i} - P_{dc-j}\right)$$
(6.7b)

 $Voltage\ reference\ (3.5a):$ 

$$E_{\rm i} = E^* + N_{\rm ac-i} \cdot Q_{\rm ac-i} + \chi_{\rm i} \tag{6.7c}$$

 $Voltage\ secondary\ control\ (3.5b)$ :

$$\dot{\chi}_{i} = -\beta_{i} (E_{i} - E^{*}) - b_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} (Q_{ac-i} - Q_{ac-j})$$
(6.7d)

#### Controller for the $j^{th}$ dc-DG $\forall j \in \mathbb{N}_{dc}$ :

 $Voltage\ reference\ (3.9a)$ :

$$V_j = V^* + M_{\mathrm{dc}-j} \cdot P_{\mathrm{dc}-j} + \varphi_j \tag{6.8a}$$

 $Voltage\ secondary\ control\ (3.9b):$ 

$$\dot{\varphi}_{j} = -\gamma_{j} \left( V_{j} - V^{*} \right) - c_{j} \sum_{i \in \mathbb{N}_{dc}} a_{ji} \left( P_{dc-j} - P_{dc-i} \right) - c_{j} \sum_{i \in \mathbb{N}_{ac}} a_{ji} \left( P_{dc-j} - P_{ac-i} \right)$$
 (6.8b)

#### Controller for the *IC*:

 $Secondary\ controller(3.10)$ :

$$\dot{P}_{\mathrm{IC}-k} = -\tau_k \sum_{\mathbf{i} \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{\mathbf{i}k} a_{jk} \left( P_{\mathrm{ac-i}} - P_{\mathrm{dc-}j} \right)$$

$$\tag{6.9a}$$

Based on the equations for the microgrid (6.1), (6.2) and (6.3), and the controllers (6.7), (6.8) and (6.9) (see Appendix A), the closed-loop model for the studied hybrid ac/dc-microgrid with the proposed control strategy has 28 state variables, as follows:

- 3 angles from the ac-microgrid  $(\theta_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 active powers from the ac-microgrid  $(P_{ac-i}, \forall i \in \mathbb{N}_{ac})$ .
- 3 reactive powers from the ac-microgrid  $(Q_{\text{ac-i}}, \forall i \in \mathbb{N}_{\text{ac}})$ .
- 3 frequency secondary control variables from the ac-microgrid  $(\psi_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 voltage secondary control variables from the ac-microgrid ( $\chi_i$ ,  $\forall i \in \mathbb{N}_{ac}$ ).
- 6 active powers from the dc-microgrid  $(P_{dc-j}, \forall j \in \mathbb{N}_{dc})$ .
- 6 voltage secondary control variables from the dc-microgrid  $(\varphi_j, \forall j \in \mathbb{N}_{dc})$ .
- 1 *IC* power (*P*<sub>IC</sub>)

Then, the state equations for the closed-loop model of the studied hybrid ac/dc-microgrid with the proposed control strategy are:

$$\Delta \dot{\theta} = G_8^{ac} \Delta P_{ac} + \Delta \psi$$

$$\Delta \dot{P}_{ac} = G_9^{ac} \Delta Q_{ac} + G_{10}^{ac} \Delta \chi + G_{11}^{ac} \Delta \theta + G_{12}^{ac} \Delta P_{IC} + G_{13}^{ac} \Delta P_{ac}$$

$$\Delta \dot{Q}_{ac} = G_5^{ac} \Delta Q_{ac} + G_6^{ac} \Delta \chi + G_7^{ac} \Delta \theta$$

$$\Delta \dot{\psi} = G_{14}^{ac} \Delta P_{ac} + G_{15}^{ac} \Delta \psi + G_{16}^{ac} \Delta P_{dc}$$

$$\Delta \dot{\chi} = G_{17}^{ac} \Delta Q_{ac} + G_{18}^{ac} \Delta \chi$$

$$\Delta \dot{P}_{dc} = G_2^{dc} \Delta P_{dc} + G_3^{dc} \Delta \varphi + G_4^{dc} \Delta P_{IC}$$

$$\Delta \dot{\varphi} = G_5^{dc} \Delta P_{dc} + G_6^{dc} \Delta \varphi + G_7^{dc} \Delta P_{ac}$$

$$\Delta \dot{P}_{IC} = G_1^{IC} \Delta P_{ac} + G_2^{IC} \Delta P_{dc}$$
(6.10)

where matrices G are calculated in a steady-state operating point and can be found in Appendix A.

The closed-loop model for the hybrid ac/dc-microgrid with the control strategy for power-sharing from (6.10) is used to validate the tuning of the controllers and to perform a small-signal stability analysis in Section 6.4.

# 6.3 Control Strategy Proposed for Operation Cost Minimization

In this section, the consensus-based distributed secondary control strategy for operation cost minimization is applied to the hybrid ac/dc-microgrid with a single IC. In this case, to obtain the closed-loop model for this controller, we assume all the units (i.e., ac-DGs, dc-DGs and IC) are operating within limits; therefore, the equations which enforce unit constraints are ignored. The process for obtaining the analytical closed-loop model is detailed in Appendix B.

The controllers proposed in Chapter 4 are applied to the hybrid ac/dc-microgrid model obtained in (6.6) to later derive the closed-loop model for the proposed control strategy. The expressions for the controllers are repeated for reader convenience.

#### Controller for the i<sup>th</sup> ac-DG $\forall i \in \mathbb{N}_{ac}$ :

 $Frequency \ reference \ (4.13a):$ 

$$\dot{\theta}_{i} = \omega_{i} = \omega^{*} + M_{ac-i} P_{ac-i}^{G} + \Omega_{i}^{ac}$$

$$(6.11a)$$

Frequency secondary control (4.13b):

$$\dot{\Omega}_{i}^{ac} = -k_{ac-i}^{a} \left(\omega_{i} - \omega^{*}\right) - k_{ac-i}^{b} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{ac}\right) - k_{ac-i}^{c} \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{dc}\right)$$
(6.11b)

 $Voltage\ reference\ (3.5a):$ 

$$E_{i} = E^* + N_{ac-i} \cdot Q_{ac-i}^{G} + \chi_{i}$$

$$(6.11c)$$

 $Voltage\ secondary\ control\ (3.5b):$ 

$$\dot{\chi}_{i} = -\beta_{i} (E_{i} - E^{*}) - b_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} (Q_{ac-i}^{G} - Q_{ac-j}^{G})$$
(6.11d)

#### Controller for the $j^{th}$ dc-DG $\forall j \in \mathbb{N}_{dc}$ :

 $Voltage\ reference\ (4.15a):$ 

$$V_{i} = V^{*} + M_{\text{dc}-i} P_{\text{dc}-i}^{G} + \Omega_{i}^{\text{dc}}$$
(6.12a)

 $Voltage\ secondary\ control\ (4.15b):$ 

$$\dot{\Omega}_{j}^{\mathrm{dc}} = -k_{\mathrm{dc}-j}^{a} \left(V_{j} - V^{*}\right) - k_{\mathrm{dc}-j}^{b} \sum_{i \in \mathbb{N}_{\mathrm{dc}}} a_{ij} \left(\lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{dc}}\right) - k_{\mathrm{dc}-j}^{c} \sum_{i \in \mathbb{N}_{\mathrm{ac}}} a_{ij} \left(\lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{ac}}\right) \quad (6.12b)$$

#### Controller for the *IC*:

 $Secondary\ controller(4.16a)$ :

$$\dot{P}_{\text{IC}-k}^* = -\tau_{\text{IC}-k} \sum_{i \in \mathbb{N}_{\text{ac}}} \sum_{j \in \mathbb{N}_{\text{dc}}} a_{ik} a_{jk} \left( \lambda_i^{\text{ac}} - \lambda_j^{\text{dc}} \right)$$
(6.13a)

Based on the equations for the microgrid (6.1), (6.2) and (6.3), and the controllers (6.11), (6.12) and (6.13) (see Appendix B), the closed-loop model for the studied hybrid ac/dc-microgrid with the proposed control strategy has 28 state variables, as follows:

- 3 angles from the ac-microgrid  $(\theta_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 active powers from the *ac*-microgrid  $(P_{\text{ac-i}}^{G}, \forall i \in \mathbb{N}_{\text{ac}})$ .
- 3 reactive powers from the *ac*-microgrid  $(Q_{\text{ac-i}}^{G}, \forall i \in \mathbb{N}_{\text{ac}})$ .
- 3 frequency secondary control variables from the ac-microgrid  $(\Omega_i^{ac}, \forall i \in \mathbb{N}_{ac})$ .
- 3 voltage secondary control variables from the ac-microgrid  $(\chi_i, \forall i \in \mathbb{N}_{ac})$ .
- 6 active powers from the dc-microgrid  $(P_{dc-j}^G, \forall j \in \mathbb{N}_{dc})$ .
- 6 voltage secondary control variables from the dc-microgrid  $(\Omega_j^{dc}, \forall j \in \mathbb{N}_{dc})$ .
- 1 IC power  $(P_{IC}^{ac})$ .

Then, the state equations for the closed-loop model of the studied hybrid ac/dc-microgrid with the proposed control strategy are:

$$\begin{split} \Delta \dot{\theta} &= G_8^{\rm ac} \Delta P_{\rm ac}^{\rm G} + \Delta \Omega^{\rm ac} \\ \Delta \dot{P}_{\rm ac}^{\rm G} &= G_9^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{10}^{\rm ac} \Delta \chi + G_{11}^{\rm ac} \Delta \theta + G_{12}^{\rm ac} \Delta P_{\rm IC}^{\rm ac} + G_{13}^{\rm ac} \Delta P_{\rm ac}^{\rm G} \\ \Delta \dot{Q}_{\rm ac}^{\rm G} &= G_5^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_6^{\rm ac} \Delta \chi + G_7^{\rm ac} \Delta \theta \\ \Delta \dot{Q}_{\rm ac}^{\rm ac} &= G_{14}^{\rm ac} \Delta P_{\rm ac}^{\rm G} + G_{15}^{\rm ac} \Delta \Omega^{\rm ac} + G_{16}^{\rm ac} \Delta P_{\rm dc}^{\rm G} \\ \Delta \dot{Q}_{\rm ac}^{\rm ac} &= G_{14}^{\rm ac} \Delta P_{\rm ac}^{\rm G} + G_{15}^{\rm ac} \Delta \Omega^{\rm ac} + G_{16}^{\rm ac} \Delta P_{\rm dc}^{\rm G} \\ \Delta \dot{Q}_{\rm ac}^{\rm G} &= G_{15}^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{18}^{\rm ac} \Delta \chi \end{split} \tag{6.14}$$

$$\Delta \dot{P}_{\rm dc}^{\rm G} &= G_2^{\rm dc} \Delta P_{\rm dc}^{\rm G} + G_3^{\rm dc} \Delta \Omega^{\rm dc} + G_4^{\rm dc} \Delta P_{\rm IC}^{\rm ac} \\ \Delta \dot{P}_{\rm ac}^{\rm ic} &= G_1^{\rm IC} \Delta P_{\rm ac}^{\rm G} + G_2^{\rm dc} \Delta P_{\rm dc}^{\rm G} \\ \Delta \dot{P}_{\rm ac}^{\rm ic} &= G_1^{\rm IC} \Delta P_{\rm ac}^{\rm G} + G_2^{\rm IC} \Delta P_{\rm dc}^{\rm G} \end{split}$$

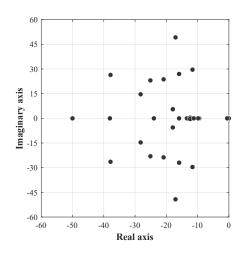
The closed-loop model for the hybrid ac/dc-microgrid with the control strategy for operation cost minimization from (6.14) is used to validate the tuning of the controllers and to perform a small-signal stability analysis in Section 6.5.

# 6.4 Small-signal Analysis of the Control Strategy for Powersharing

In this Section, a small-signal stability analysis is performed to the closed-loop model for the hybrid ac/dc-microgrid with the control strategy for power-sharing obtained in Section 6.2. Now, the closed-loop model is used to derive the stable limits for gains of the proposed controller and verify their tuning. Moreover, a sensitivity analysis for parameters of the controllers is performed.

#### 6.4.1 Eigenvalues and Participation Factors

The eigenvalues obtained for the linearized closed-loop system obtained in Section 6.2 are plotted in Fig. 6.2 and their coordinates, natural frequency  $\omega_n$  and damping ratio  $\zeta$  are shown in Table 6.1. The steady-state operating point conditions at the operating point determined by the loading conditions in Table 6.2 were obtained from a system simulation, and the parameters of the controllers are the ones shown in Table 5.3. Since the secondary control strategy for power-sharing is active, all the DGs are generating 79% of their maximum active power (i.e.,  $P_{\rm dc}^{\rm max} = 2.5 {\rm kW}$  and  $P_{\rm ac}^{\rm max} = 3.0 {\rm kVA}$ ). It is important to emphasize that the conclusions drawn regarding stability and participation factors and sensitivity are, strictly speaking, valid only for the tuning and the operating point described in this Section.



			Eigenvalues		
F	Real		Complex		$\zeta(\%)$
$\lambda_1$	0.0	$\lambda_{6,7}$	$-11.6 \pm j29.6$	5.1	36.5
$\lambda_2$	-0.6	$\lambda_{8,9}$	$-12.4 \pm j \ 0.4$	2.0	99.9
$\lambda_3$	-9.5	$\lambda_{11,12}$	$-15.9 \pm j27.0$	5.0	50.7
$\lambda_4$	-9.9	$\lambda_{14,15}$	$-17.1 \pm j49.2$	8.3	32.8
$\lambda_5$	-11.2	$\lambda_{16,17}$	$-17.9 \pm j \ 5.6$	3.0	95.4
$\lambda_{10}$	-13.4	$\lambda_{18,19}$	$-20.9 \pm j23.7$	5.0	66.1
$\lambda_{13}$	-15.9	$\lambda_{21,22}$	$-25.1 \pm j23.1$	5.4	73.6
$\lambda_{20}$	-24.0	$\lambda_{23,24}$	$-28.2 \pm j14.6$	5.1	88.8
$\lambda_{27}$	-38.1	$\lambda_{25,26}$	$-37.8 \pm j26.4$	7.3	82.0
$\lambda_{28}$	-50.0				

Figure 6.2: Eigenvalues of the system, strategy for power-sharing.

Table 6.1: Eigenvalues coordinates of the system, strategy for power-sharing.

Table 6.2: Load conditions for base-case with control strategy for power-sharing.

Load	kW	Load	kW	Load	kVA
$R_1$	2.05	$R_4$	2.05	$\overline{Z_1}$	1.7 + j0.8
$R_2$	2.05	$R_5$	2.05	$Z_2$	4.2 + j0.0
$R_3$	1.37	$R_6$	2.05	$Z_3$	3.3 + j1.1

The following approach was used for tuning the gains of the controllers. First, all the controllers were active, and full-load conditions were assumed. Second, the root locus method using the closed-loop model derived in Section 6.2 was applied to the aforementioned operat-

ing condition. The following parameters were sequentially tuned: 1) the parameters related to the local voltages  $(\beta_i, \gamma_i)$  considering the other parameters are zero, 2) the parameters associated with the active power consensus  $(\sigma_i, c_i, \tau_i)$  considering  $(\beta_i, \gamma_i)$  as defined in 1), and 3) the parameters from the reactive power consensus  $(b_i)$  considering parameters obtained in 1) and 2). Third, several simulation scenarios were carried out to fine-tune the gains, changing the operating points to different loading conditions in order to analyze the bidirectionality of the power in the IC, and the results were satisfactory. If a more refined tuning of the controllers is desired, meta-heuristic optimization techniques can be considered [139]; however, this is out of the scope of this work.

From Fig. 6.2 and Table 6.1, it can be noted that, since all the eigenvalues have a negative real part (except the eigenvalue at zero), the system is asymptotically stable for the nominal values of the gains shown in Table 5.3. Moreover, there are 9 real eigenvalues (see left column in Table 6.1), 18 complex (9 conjugate pairs) eigenvalues (see right column in Table 6.1), and one eigenvalue at the origin  $(\lambda_1)$ , which is related to the angle-frequency relationship in the ac-microgrid ( $\dot{\theta} = \omega$ ). The real eigenvalues are completely damped (by definition). On the other hand, it can be seen that the complex eigenvalues  $\lambda_{14,15}$ ,  $\lambda_{6,7}$  and  $\lambda_{11,12}$ , have a damping ratio equal to 32.8%, 36.5% and 50.7%, respectively. Although these modes are very oscillatory, they are fast ( $\omega_n > 5$ Hz) and the system's stability is not affected. The damping ratio of the other conjugate pairs is higher than 66%, thus, it can be concluded that they are well damped. The slowest eigenvalue ( $\lambda_2 = -0.563$ ) is strongly related to the power flow through the IC, as will be explained in the following by analyzing the participation factors.

The participation factors  $(p_{ij})$  are a measure of the relative participation of the i<sup>th</sup> state variable in the  $j^{th}$  eigenvalue (or mode), and vice versa [140]. Fig. 6.3 is presented to analyze the participation factors. Here, the eigenvalues for the base case discussed before are plotted, and the ones with the highest participation factors with respect to the state variables are highlighted.

The eigenvalues with the highest participation of the state variables related to the secondary controller for the dc-DGs ( $\Delta\varphi$ ) are highlighted in green (see Fig. 6.3a). It can be seen that  $\Delta\varphi$  participates mainly in real eigenvalues and in two conjugate pairs ( $\lambda_{20,21}$ ,  $\lambda_{25,26}$ ), which damping ratios are 73.6% and 82.0%, respectively. The frequency of oscillation for these conjugate pairs is also similar, which helps to understand the secondary control dynamics for the dc-DGs. The state variables for the power generated by the dc-DGs ( $\Delta P_{dc}$ ) have a strong participation in the eigenvalues highlighted in red (see Fig. 6.3b). Comparing Fig. 6.3a and Fig. 6.3b, it is clear that a strong relation between  $\Delta P_{dc}$  and  $\Delta\varphi$  exists, since they participate in almost the same eigenvalues. Therefore, it is possible to conclude that the dynamics for these two variables is similar.

The eigenvalues with the highest participation of the state variables for the power generated by the ac-DGs ( $\Delta P_{\rm ac}$ ) are highlighted in blue (see Fig. 6.3c). It can be seen that  $\Delta P_{\rm ac}$  participates mostly in real eigenvalues and in one conjugate pair ( $\lambda_{6,7}$ ), which are the complex eigenvalues with the smallest damping ratio (36.5%). Therefore, if a higher damping ratio in the power generated by the ac-DGs is desired, the tuning of the controllers should be adjusted as will be described in next Section with the sensitivity analysis.

Fig. 6.3d shows the participation of the other state variables in the eigenvalues, highlighted

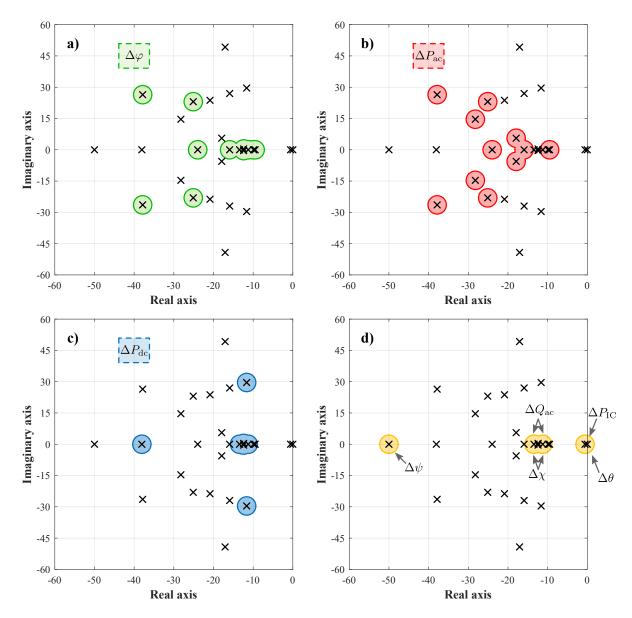


Figure 6.3: Participation factors for the eigenvalues of the system with the strategy for power-sharing.

in yellow. From the Figure it is possible to state that the eigenvalue at the origin  $(\lambda_1)$  is highly sensitive to the state variable of the angles in the ac-microgrid  $(\Delta\omega)$ . On the other hand, the slowest eigenvalue  $(\lambda_2)$  is highly sensitive to the power in the IC  $(\Delta P_{\rm IC})$ . The eigenvalues marked with  $\lambda_{10}$ ,  $\lambda_{13}$  are highly sensitive to the reactive power-sharing, i.e., to the state variables of the reactive power  $(\Delta Q_{\rm ac})$  and the voltage secondary controller of the ac-DGs  $(\Delta\chi)$ . Finally, the eigenvalue with the fastest dynamic  $(\lambda_{28})$  is strongly sensitive to the frequency secondary controller of the ac-DGs  $(\Delta\psi)$ .

#### 6.4.2 Sensitivity Analysis

Now, a sensitivity analysis is performed to the closed-loop system obtained in Section 6.2 for the hybrid ac/dc-microgrid with the control strategy proposed for power-sharing. The

sensitivity analysis helps to understand how the parameters of the controllers affect the dynamics of the system, in the vicinity of the operating point. Specifically, four control objectives are analyzed: (i) dc-voltage regulation, (ii) ac-voltage regulation, (iii) active power consensus, and (iv) reactive power consensus. In all cases, the linearized model from Section 6.2 is considered at the original operating point and the parameters of the controllers are modified from 50% (in blue) to 150% (in red) of the value shown in Table 5.3 (identified with a black ' $\mathbf{x}$ '). The results for the sensitivity analysis are shown in Fig. 6.4.

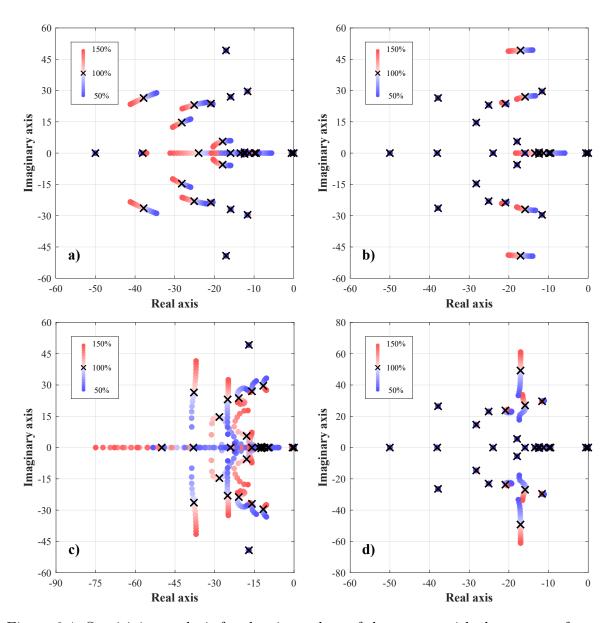


Figure 6.4: Sensitivity analysis for the eigenvalues of the system with the strategy for power-sharing. a) dc-voltage regulation. b) ac-voltage regulation. c) Active power consensus. d) Reactive power consensus.

First, Fig. 6.4a shows the trajectories of the eigenvalues when a sweep is applied to the gain for dc-voltage regulation (6.0 <  $\gamma_i$  < 18.0). The expression for the dc-DG's secondary controller is repeated in (6.15) for the convenience of the reader, and the modified gain is highlighted in red. Using the information from the participation factors analyzed in the

previous Section (see Fig. 6.3), it is possible to conclude that the eigenvalues affected by the modification in the gains for dc-voltage regulation are the ones that are more sensitive to the secondary controller of the dc-DGs (see Fig. 6.3a) and to the power generated by the dc-DGs (see Fig. 6.3b). On the other hand, from the trajectories of the complex eigenvalues, we can conclude that frequency and damping ratio of these modes increase for an increase in  $\gamma_i$ .

$$\rho_{i}\dot{\varphi}_{i} = -\gamma_{i}(V_{i} - V^{*}) - c_{i} \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left( P_{dc-i} - P_{dc-j} \right) - c_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left( P_{dc-i} - P_{ac-j} \right)$$
(6.15)

Second, Fig. 6.4b shows the trajectories of the eigenvalues when a sweep is applied to the gain for ac-voltage regulation (6.0 <  $\beta_i$  < 18.0). The expression for the ac-DG's secondary controller is repeated in (6.16) for the convenience of the reader, and the modified gain is highlighted in red. Comparing with the information from the participation factors (see Fig. 6.3) it can be concluded that the eigenvalues affected by the sweep in the gain are not strictly related to a state variable. Moreover, the trajectories show that the eigenvalues move along the real-axis; however, they are maintained within the stable region of the complex plane (left semi-plane).

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}}(E_{\mathbf{i}} - E^*) - b_{\mathbf{i}} \sum_{j \in \mathbb{N}_{ac}} a_{\mathbf{i}j} (Q_{\mathbf{i}} - Q_j)$$
(6.16)

Third, Fig. 6.4c shows the trajectories of the eigenvalues when a sweep is simultaneously applied to the gains for the active power consensus (25.0 <  $c_i$ ,  $\sigma_i$ ,  $\tau_k$  < 75.0). The expressions for the secondary controller of the dc-DGs, ac-DGs and IC are repeated in (6.17), respectively, for the convenience of the reader, and the modified gains are highlighted in red. It is evident that the active power consensus affects almost all the eigenvalues from the system. Moreover, it can be noted that the eigenvalues that have a longer trajectory are those that are more sensitive to the state variables of the active power. On the other hand, the complex eigenvalues with the smallest damping ratio tend to move to the right-hand side; however, they remain in the stable region for the range of the gains considered in this analysis. It can also be noted that some complex eigenvalues move away from the real-axis, while others move closer. Thus, the increase/reduction of the gains improves or worsens the damping ratio of different oscillatory modes. What is clear is that an increase in gains also increases the natural frequency of the eigenvalues.

$$\rho_{i}\dot{\varphi}_{i} = -\gamma_{i}\left(V_{i} - V^{*}\right) - \frac{\boldsymbol{c_{i}}}{\sum_{j \in \mathbb{N}_{dc}}} a_{ij}\left(P_{dc-i} - P_{dc-j}\right) - \frac{\boldsymbol{c_{i}}}{\sum_{j \in \mathbb{N}_{ac}}} a_{ij}\left(P_{dc-i} - P_{ac-j}\right)$$
(6.17a)

$$\dot{\psi}_{i} = -\boldsymbol{\sigma}_{i} \left[ (\omega_{i} - \omega^{*}) + \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left( P_{ac-i} - P_{ac-j} \right) + \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left( P_{ac-i} - P_{dc-j} \right) \right]$$
(6.17b)

$$\dot{P}_{\mathrm{IC}-k}^* = -\tau_k \sum_{\mathbf{i} \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{\mathbf{i}k} a_{jk} \left( P_{\mathrm{ac-i}} - P_{\mathrm{dc-}j} \right) \tag{6.17c}$$

Finally, Fig. 6.4d shows the trajectories of the eigenvalues when a sweep is applied to the gain for the reactive power consensus (250.0  $< b_i < 750.0$ ). The expressions for the secondary

controller of the ac-DGs is repeated in (6.18) for the convenience of the reader, and the modified gain is highlighted in red. Comparing with the information from the participation factors (see Fig. 6.3) it can be concluded that the eigenvalues affected by the sweep in the gain  $b_i$  are not strictly related to a state variable. Moreover, the trajectories show that the eigenvalues move along the imaginary-axis. On the other hand, the eigenvalues affected by the change in the gain related to the reactive power consensus are the same affected by the ac-voltage regulation (see Fig. 6.4b), which is explained by the Q-E relation in the ac-microgrid.

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}} \left( E_{\mathbf{i}} - E^* \right) - \frac{\mathbf{b_i}}{\sum_{j \in \mathbb{N}_{ac}}} a_{\mathbf{i}j} \left( Q_{\mathbf{i}} - Q_j \right)$$

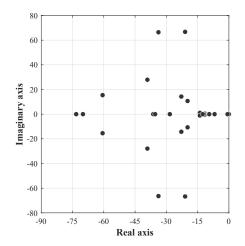
$$(6.18)$$

# 6.5 Small-signal Analysis of the Control Strategy for Operation Cost Minimization

In this Section, a small-signal stability analysis is performed to the closed-loop model for the hybrid ac/dc-microgrid with the control strategy for operation cost minimization obtained in Section 6.3. The closed-loop model is used to derive the stable limits for gains of the proposed controller and verify their tuning. Moreover, a sensitivity analysis for parameters of the controllers is performed.

#### 6.5.1 Eigenvalues and Participation Factors

The eigenvalues obtained for the linearized closed-loop system derived in Section 6.3 are plotted in Fig. 6.5 and their coordinates, natural frequency  $\omega_n$  and damping ratio  $\zeta$  are shown in Table 6.3. The steady-state operating point conditions at the operating point determined by the loading conditions in Table 6.4 were obtained from a system simulation, and the parameters of the controllers are the ones shown in Table 5.4. The power generated by the DGs (in p.u.) is shown in Table 6.5. It is important to emphasize that the conclusions drawn regarding stability and participation factors and sensitivity are, strictly speaking, valid only for the tuning and the operating point described in this Section.



Eigenvalues							
F	Real		Complex	$\omega_n(\mathrm{Hz})$	$\zeta(\%)$		
$\overline{\lambda_1}$	0.0	$\lambda_{6,7}$	$-11.7 \pm j \ 0.6$	1.9	99.9		
$\lambda_2$	-0.7	$\lambda_{10,11}$	$-13.9 \pm j \ 1.1$	2.2	99.7		
$\lambda_3$	-6.9	$\lambda_{12,13}$	$-19.8 \pm j10.7$	3.6	88.0		
$\lambda_4$	-9.3	$\lambda_{14,15}$	$-21.0 \pm j66.7$	11.1	30.0		
$\lambda_5$	-9.5	$\lambda_{16,17}$	$-22.8 \pm j14.3$	4.3	84.8		
$\lambda_8$	-12.0	$\lambda_{19,20}$	$-33.8 \pm j66.4$	11.9	45.3		
$\lambda_9$	-12.6	$\lambda_{23,24}$	$-39.1 \pm j27.9$	7.6	81.4		
$\lambda_{18}$	-28.3	$\lambda_{25,26}$	$-60.6 \pm j15.4$	9.9	96.9		
$\lambda_{21}$	-35.4						
$\lambda_{22}$	-36.3						
$\lambda_{27}$	-70.0						
$\lambda_{28}$	-73.2						

Figure 6.5: Eigenvalues of the system, strategy for cost minimization.

Table 6.3: Eigenvalues coordinates of the system, strategy for cost minimization.

Table 6.4: Load conditions for base-case with control strategy for cost minimization.

Load	kW	Load	kW	Load	kW
$R_1$	1.03	$\overline{R_4}$	1.02	$\overline{Z_1}$	1.90
$R_2$	1.57	$R_5$	1.02	$Z_2$	2.57
$R_3$	1.37	$R_6$	1.02	$Z_3$	2.64

Table 6.5: Power generated by the DGs, in p.u.

dc-DG <sub>1</sub>	dc-DG <sub>2</sub>	dc-DG <sub>3</sub>	dc-DG <sub>4</sub>	dc-DG <sub>5</sub>	dc-DG <sub>6</sub>	$ac ext{-}\mathrm{DG}_1$	ac-DG <sub>2</sub>	ac-DG <sub>3</sub>
0.631	0.864	0.993	0.268	0.175	0.654	0.985	0.520	0.284

The following approach was used for tuning the gains of the controllers. First, all the controllers were considered active, and full-load conditions were assumed. Second, the root locus method using the closed-loop model derived in Section 6.3 was applied to the aforementioned operating condition. The following parameters were sequentially tuned: 1) the parameters related to the local voltages  $(\beta_i, k_{dc-j}^a)$  considering the other parameters are zero, 2) the parameters associated with the  $\lambda$  consensus  $(k_{dc-j}^{b,c}, k_{ac-i}^{b,c}, \tau_{IC-k})$  considering  $(\beta_i, k_{dc-j}^a)$  as defined in 1), and 3) the parameters corresponding to the reactive power consensus  $(b_i)$  considering parameters obtained in 1) and 2). Third, several simulation scenarios were carried out to fine-tune the gains, changing the operating points to different loading conditions in order to analyze the bidirectionality of the power in the IC, and the results were satisfactory. If a more refined tuning of the controllers is required, meta-heuristic optimization techniques can be considered; however, this is out of the scope of this work.

From Fig. 6.5 and Table 6.3, it can be noted that, since all the eigenvalues have a negative real part (except the eigenvalue at zero), the system is asymptotically stable for the nominal values of the gains shown in Table 5.4. Moreover, there are 11 real eigenvalues (see left column in Table 6.3), 16 complex (8 conjugate pairs) eigenvalues (see right column in Table 6.3), and one eigenvalue at the origin  $(\lambda_1)$ , which is related to the angle-frequency relationship in the ac-microgrid ( $\dot{\theta} = \omega$ ). Although the real eigenvalues are completely damped (by definition), it can be seen that the complex eigenvalues are also mostly well damped. Complex pairs

 $\lambda_{14,15}$  and  $\lambda_{19,20}$ , have a damping ratio equal to 30.0% and 45.3%, respectively, while the damping ratio of the other conjugate pairs is higher than 81%. On the other hand, the slowest eigenvalue ( $\lambda_2 = -0.655$ ) is strongly related to the power flow through the IC, as will be explained in the following by analyzing the participation factors.

The Fig. 6.6 is presented for analyzing the participation factors. Here, the eigenvalues for the base case discussed before are plotted, and the ones with the highest participation factors with respect to the state variables are highlighted.

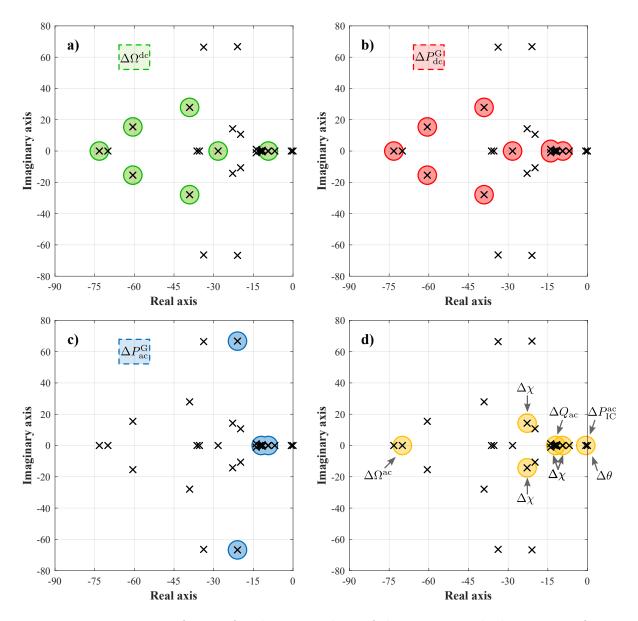


Figure 6.6: Participation factors for the eigenvalues of the system with the strategy for cost minimization.

The eigenvalues with the highest participation of the state variables related to the secondary controller for the dc-DGs ( $\Delta\Omega^{dc}$ ) are highlighted in green (see Fig. 6.6a). It can be seen that  $\Delta\Omega^{dc}$  participates mostly in real eigenvalues and in two conjugate pairs ( $\lambda_{23,24}$ ,  $\lambda_{25,26}$ ), which damping ratio is 81.4% and 96.9%, respectively. The frequency of oscillation

for these conjugate pairs is also similar (7.6Hz and 9.9Hz, respectively), which give us an insight of the secondary control dynamics for the dc-DGs.

The state variables for the power generated by the dc-DGs ( $\Delta P_{\rm dc}^{\rm G}$ ) have a strong participation in the eigenvalues highlighted in red (see Fig. 6.6b). Comparing Fig. 6.6a and Fig. 6.6b, it is clear that a strong relation between  $\Delta P_{\rm dc}^{\rm G}$  and  $\Delta \Omega^{\rm dc}$  exists, since they participate in almost the same eigenvalues. Therefore, it is possible to conclude that the dynamics for these two variables is similar, as described before for the control strategy for power-sharing.

The eigenvalues with the highest participation of the state variables for the power generated by the ac-DGs ( $\Delta P_{\rm ac}^{\rm G}$ ) are highlighted in blue (see Fig. 6.6c). It can be seen that  $\Delta P_{\rm ac}^{\rm G}$  participates mostly in real eigenvalues and in one conjugate pair ( $\lambda_{14,15}$ ), which are the complex eigenvalues with the smallest damping ratio (30.0%). Therefore, if a higher damping ratio in the power generated by the ac-DGs is desired, the tuning of the controllers should be adjusted as will be described in next Section with the sensitivity analysis.

Fig. 6.6d shows the participation of the other state variables in the eigenvalues, highlighted in yellow. From the Figure it is possible to state that the eigenvalue at the origin ( $\lambda_1$ ) is highly sensitive to the state variable of the angles in the ac-microgrid ( $\Delta\omega$ ). On the other hand, the slowest eigenvalue ( $\lambda_2$ ) is highly sensitive to the power in the IC ( $\Delta P_{\rm IC}^{\rm ac}$ ). The eigenvalue marked with  $\lambda_9$  is highly sensitive to the reactive power ( $\Delta Q_{\rm ac}^{\rm G}$ ), while the eigenvalues marked with  $\lambda_5$ ,  $\lambda_9$ ,  $\lambda_{16,17}$  are highly sensitive to the voltage secondary controller of the ac-DGs ( $\Delta\chi$ ). Finally, the eigenvalue with the fastest dynamic ( $\lambda_{27}$ ) is strongly sensitive to the frequency secondary controller of the ac-DGs ( $\Delta\Omega^{\rm ac}$ ).

#### 6.5.2 Sensitivity Analysis

Now, a sensitivity analysis is performed on the closed-loop system obtained in Section 6.3 for the hybrid ac/dc-microgrid with the control strategy proposed for cost minimization. The sensitivity analysis helps to understand how the parameters of the controllers affect the dynamics of the system, in the vicinity of the operating point. Specifically, four control objectives are analyzed: (i) dc-voltage regulation, (ii) ac-voltage regulation, (iii) incremental cost ( $\lambda$ ) consensus, and (iv) reactive power consensus. In all cases, the linearized model from Section 6.3 is considered at the original operating point and the parameters of the controllers are modified from 50% (in blue) to 150% (in red) of the value shown in Table 5.4 (black 'x'). The results for the sensitivity analysis are shown in Fig. 6.7.

First, Fig. 6.7a shows the trajectories of the eigenvalues when a sweep is applied to the gain for dc-voltage regulation (0.05  $< k_{dc-j}^a < 0.15$ ). The expression for the dc-DG's secondary controller is repeated in (6.19) for the convenience of the reader, and the modified gain is highlighted in red. Using the information from the participation factors analyzed in the previous Section (see Fig. 6.6), it is possible to conclude that the eigenvalues affected by the modification in the gains for dc-voltage regulation are the ones that are more sensitive to the secondary controller of the dc-DGs (see Fig. 6.6a) and to the power generated by the dc-DGs (see Fig. 6.6b). On the other hand, from the trajectories of the complex eigenvalues, we can

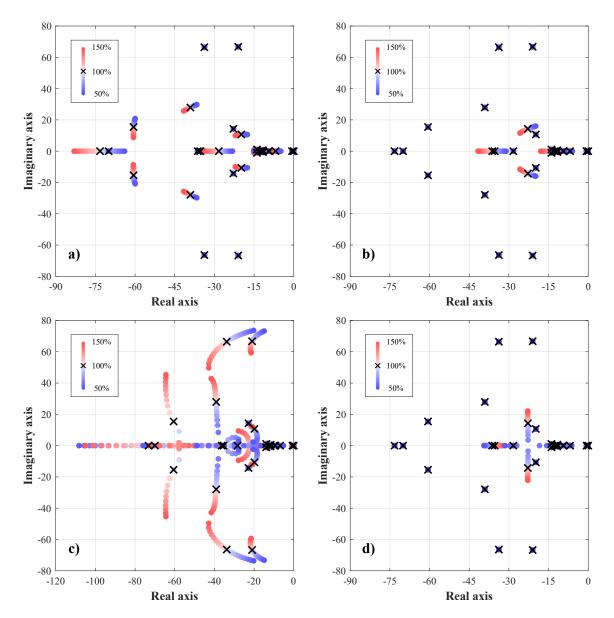


Figure 6.7: Sensitivity analysis for the eigenvalues of the system with the strategy for cost minimization. a) dc-voltage regulation. b) ac-voltage regulation. c) Incremental cost  $(\lambda)$  consensus. d) Reactive power consensus.

conclude that frequency and damping ratio of these modes increase for an increase in  $k_{dc-j}^a$ .

$$\dot{\Omega}_{j}^{\mathrm{dc}} = -\boldsymbol{k_{\mathrm{dc-}j}^{a}}(V_{j} - V^{*}) - k_{\mathrm{dc-}j}^{b} \sum_{i \in \mathbb{N}_{\mathrm{dc}}} a_{ij} \left(\lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{dc}}\right) - k_{\mathrm{dc-}j}^{c} \sum_{i \in \mathbb{N}_{\mathrm{ac}}} a_{ij} \left(\lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{ac}}\right)$$
(6.19)

Secondly, Fig. 6.7b shows the trajectories of the eigenvalues when a sweep is applied to the gain for ac-voltage regulation (6.0 <  $\beta_{\rm i}$  < 18.0). The expression for the ac-DG's secondary controller is repeated in (6.20) for the convenience of the reader, and the modified gain is highlighted in red. Comparing with the information from the participation factors (see Fig. 6.6) it can be concluded that the eigenvalues affected by the sweep in  $\beta_{\rm i}$  are highly

sensitive to the state variables for the voltage secondary controller in the ac-DGs ( $\Delta \chi$ ) and for the reactive power( $\Delta Q_{\rm ac}^{\rm G}$ ), as shown in Fig. 6.6d. Moreover, the trajectories show that the eigenvalues mainly move along the real-axis; however, they are maintained within the stable region of the complex plane (left semi-plane).

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}}(E_{\mathbf{i}} - E^*) - b_{\mathbf{i}} \sum_{j \in \mathbb{N}_{ac}} a_{\mathbf{i}j} \left( Q_{ac-\mathbf{i}}^{\mathbf{G}} - Q_{ac-\mathbf{j}}^{\mathbf{G}} \right)$$

$$(6.20)$$

Thirdly, Fig. 6.7c shows the trajectories of the eigenvalues when a sweep is simultaneously applied to the gains for the incremental cost consensus  $(0.5 < k_{\rm dc-j}^{b,c} < 1.5, 0.05 < k_{\rm ac-i}^{b,c} < 0.15, 125 < \tau_{\rm IC} < 375)$ . The expressions for the secondary controller of the dc-DGs, ac-DGs and IC are repeated in (6.21), respectively, and the modified gains are highlighted in red. It is evident that the incremental cost consensus affects almost all the eigenvalues from the system. Moreover, it can be noted that the eigenvalues that have a longer trajectory are those that are more sensitive to the state variables of the active power due to the cost is a function of the power. On the other hand, the complex eigenvalues with the smallest damping ratio tend to move to the right-hand side when the gains decrease; however, they remain in the stable region for the range of the gains considered in this analysis. It can also be noted that some complex eigenvalues move away from the real-axis, while others move closer. Thus, it is not possible to conclude if the increase/reduction of the gains improves or worsens the damping ratio of the oscillatory modes. What is clear is that an increase in gains also increases the natural frequency of eigenvalues.

$$\dot{\Omega}_{j}^{\text{dc}} = -k_{\text{dc}-j}^{a} \left( V_{j} - V^{*} \right) - \frac{\mathbf{k_{dc-j}^{b}}}{\mathbf{k_{dc-j}^{b}}} \sum_{i \in \mathbb{N}_{dc}} a_{ij} \left( \lambda_{j}^{\text{dc}} - \lambda_{i}^{\text{dc}} \right) - \frac{\mathbf{k_{dc-j}^{c}}}{\mathbf{k_{dc-j}^{c}}} \sum_{i \in \mathbb{N}_{ac}} a_{ij} \left( \lambda_{j}^{\text{dc}} - \lambda_{i}^{\text{ac}} \right)$$
(6.21a)

$$\dot{\Omega}_{i}^{ac} = -k_{ac-i}^{a} \left(\omega_{i} - \omega^{*}\right) - \frac{\mathbf{k_{ac-i}^{b}}}{\mathbf{k_{ac-i}^{b}}} \sum_{j \in \mathbb{N}_{-i}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{ac}\right) - \frac{\mathbf{k_{ac-i}^{c}}}{\mathbf{k_{ac-i}^{c}}} \sum_{j \in \mathbb{N}_{+i}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{dc}\right)$$
(6.21b)

$$\dot{P}_{\text{IC}-k}^* = -\tau_{\text{IC}-k} \sum_{i \in \mathbb{N}_{\text{ac}}} \sum_{j \in \mathbb{N}_{\text{dc}}} a_{ik} a_{jk} \left( \lambda_{\text{ac}-i} - \lambda_{\text{dc}-j} \right)$$

$$(6.21c)$$

Finally, Fig. 6.7d shows the trajectories of the eigenvalues when a sweep is applied to the gain for the reactive power consensus (250 <  $b_{\rm i}$  < 750). The expressions for the secondary controller of the ac-DGs is repeated in (6.22) for the convenience of the reader, and the modified gain is highlighted in red. Comparing with the information from the participation factors (see Fig. 6.6) it can be concluded that the eigenvalues affected by the sweep in the gain  $b_{\rm i}$  are highly sensitive to the state variables for the voltage secondary controller in the ac-DGs ( $\Delta \chi$ ) and for the reactive power( $\Delta Q_{\rm ac}^{\rm G}$ ), as shown in Fig. 6.6d. Moreover, the trajectories show that the complex eigenvalues move along the imaginary-axis. On the other hand, the eigenvalues affected by the change in the gain related to the reactive power consensus are the same affected by the ac-voltage regulation (see Fig. 6.7b), which is explained by the Q-E relation in the ac-microgrid.

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}} \left( E_{\mathbf{i}} - E^* \right) - \frac{\mathbf{b_i}}{\mathbf{b_i}} \sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{\mathbf{i}j} \left( Q_{\mathrm{ac-i}}^{\mathrm{G}} - Q_{\mathrm{ac-j}}^{\mathrm{G}} \right)$$

$$(6.22)$$

#### 6.6 Summary

In this Chapter, the closed-loop model for a hybrid ac/dc-microgrid with a single IC was obtained. The model was augmented with the two consensus based distributed secondary controllers proposed in this thesis. First, the control strategy proposed for power-sharing between the DGs was included, obtaining an analytical closed-loop model composed by 28 state variables. Secondly, the control strategy proposed for operation cost minimization was included, obtaining again an analytical closed-loop model composed by 28 state variables. The state variables were the same for the two analyzed cases. The obtained closed-loop model for the hybrid ac/dc-microgrid was used to derive the stable limits for gains of proposed controllers and verify their tuning.

Later, a small-signal analysis was performed for the two closed-loop models obtained. The eigenvalues from the characteristic equation were extensively analyzed, identifying their sensitivity with respect to the state variables. The later was performed using the participation factors. Moreover, a sensitivity analysis was also performed to identify how the increase or decrease of controller's gains affects the state variables. Four cases were analyzed for each control strategy proposed in this thesis, identifying the effect of the gains in the oscillatory modes.

In the next Chapter, the distributed secondary control strategy for power-sharing proposed in Chapter 3 is validated through experimental and simulation tests.

# Chapter 7

# Validation of the Strategy Proposed for Power-Sharing

#### 7.1 Introduction

This Chapter presents the experimental and simulation tests performed for validating the distributed secondary control strategy for power-sharing proposed in Chapter 3. For the experimental tests, the experimental rig described in Chapter 5 is considered. For the simulation tests two systems were considered: the system described in Chapter 5 for the first simulation test and a modified microgrid considering multiple ICs for the second simulation test. This could not be experimentally implemented in the lab, because a single IC was available in the Triphase microgrid. For all tests, all the control layers of the microgrid (i.e., inner, primary and secondary control loops) are active.

This Chapter is organized as follows: experimental tests are presented in Sections 7.2 to 7.5. Specifically, the tests were performed for validating the operation of the proposed control strategy against load impacts (Section 7.2), connection/disconnection of DGs (Section 7.3) and communication issues such as loss of communication links (Section 7.4) and communication delays (7.5). Simulation tests are presented in Sections 7.6 and 7.7 to compare the performance of the proposed strategy against those achieved by the control strategies presented in [21] and [134], and analyze the performance of the proposed strategy considering a hybrid ac/dc-microgrid with multiple ICs, respectively.

## 7.2 Experimental Test #1: Load Steps

The structure of the system under study is shown in Fig. 7.1. In this test, load changes are applied to both sides of the hybrid ac/dc-microgrid in order to validate:

- (i) the proposed DSC,
- (ii) power-sharing among all DGs, and

(iii) bidirectional power flow through the IC.

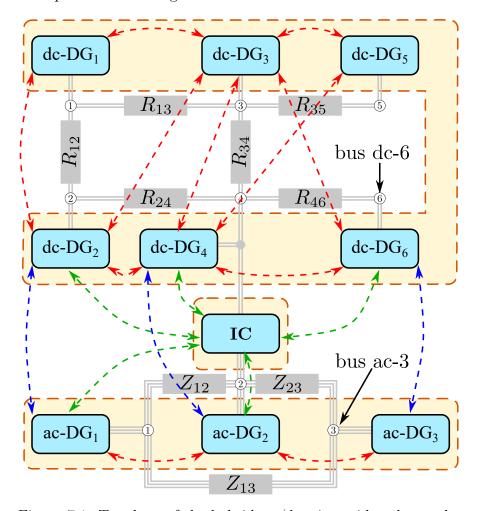


Figure 7.1: Topology of the hybrid ac/dc-microgrid under study.

For this test, the local load condition of the 9 DGs is summarized in Table 7.1, where the load steps applied are highlighted in black. The total load of the hybrid ac/dc-microgrid is 15.3kW (9.6kW on dc-side and 5.7kW on ac-side) and 1.9kVAr, i.e., 63.75% of the nominal active power (24.0kW) and 21.1% of the nominal reactive power (9.0kVAr). The power on the dc-side is 64.0% of the nominal power of this side (15.0kW); meanwhile, the active power on the ac-side corresponds to 63.3% of 9.0kW. The results are shown in Fig. 7.2.

Table 7.1: Experimental ac/dc-microgrid, load conditions for test #1.

		dc-s	side loa	ads, in	kW		ac-si	de loads, in	kVA
Time	$R_1$	$R_2$	$R_3$	$R_4$	$R_5$	$R_6$	$Z_1$	$Z_2$	$\overline{Z_3}$
0s < t < 30s	2.05	0.00	1.37	2.05	2.05	2.05	0.0 + j0.8	2.4 + j0.0	3.3 + j1.1
30s < t < 60s	2.05	2.05	1.37	2.05	2.05	2.05	0.0 + j0.8	2.4 + j0.0	$1.8 {+} j0.0$
60s < t < 90s	2.05	0.00	1.37	2.05	2.05	2.05	0.0 + j0.8	2.4 + j0.0	3.3 + j1.1
90s < t < 120s	2.05	0.00	1.37	2.05	0.00	2.05	1.7 + j0.8	2.4 + j0.0	3.3 + j1.1
120s < t < 150s	2.05	0.00	1.37	2.05	2.05	2.05	0.0 + j0.8	2.4 + j0.0	3.3 + j1.1

Because the secondary control loop is enabled, both ac-DGs and dc-DGs share almost perfectly the per unit (p.u.) power generated. The p.u. active powers of the nine DGs

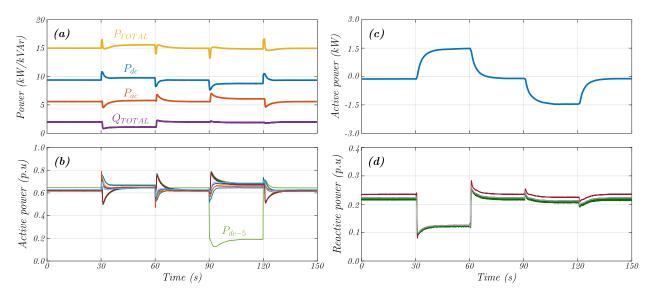


Figure 7.2: Power in the hybrid ac/dc-microgrid for Test #1. (a) Total power (active and reactive). (b) Active power generated by ac-DGs ( $P_{ac-i}$ , i = 1, 2, 3) and dc-DGs ( $P_{dc-j}$ ,  $j = 1, \ldots, 6$ ), in p.u. (c) Active power through the IC ( $P_{IC}$ ). (d) Reactive power generated by ac-DGs ( $Q_{ac-i}$ , i = 1, 2, 3), in p.u.

are shown in Fig. 7.2b. As it is well known, the sharing of active power in the dc-side is compromised by the voltage regulation [see (3.9b)]. However, for t < 30s, the power-sharing of each dc-DG is still very good, with values between 0.646p.u. (dc-DG<sub>5</sub>) and 0.615p.u. (dc-DG<sub>2</sub>) and a total average (dc-side) of 0.628p.u. On the ac-side, the sharing of active power is almost perfect [see (3.4b)] with a value of about 0.624p.u. in each DG.

The power transferred through the IC is shown in Fig. 7.2c. For t < 30 the active powers (in p.u.) on two sides of the microgrid are almost identical (64.0% dc and 63.3% ac). Therefore, the power transferred through the IC is negligible [see Fig. 7.2c]. The reactive power-sharing on the ac-side is shown in Fig. 7.2d. In this case, there is a compromise between voltage regulation and reactive power-sharing [see (3.5b)]. The maximum reactive power corresponds to that of ac-DG<sub>1</sub> (for the whole test); meanwhile, ac-DG<sub>2</sub> and ac-DG<sub>3</sub> have almost equal values of normalized reactive power-sharing.

Secondary variables of the hybrid ac/dc-microgrid are shown in Fig. 7.3. It is concluded that the three secondary variables, i.e., the dc-voltage [see Fig. 7.3a], the ac-voltage [see Fig. 7.3b] and the frequency [see Fig. 7.3c] are maintained within the tolerance bands shown in black dashed lines (allowable range for the voltages is  $V_{nom} \pm 5\%$  and for the frequency is  $f_{nom} \pm 2\%$ ). For t < 30s the lowest dc output voltage corresponds to dc-DG<sub>5</sub> and the highest corresponds to dc-DG<sub>2</sub> (no load is connected to the output of dc-DG<sub>2</sub>).

At t = 30s a load change is applied on both the ac-side and the dc-side. The load at node 2 on the dc-side is increased to 2.05kW, while the load at node 3 on the ac-side is decreased to 1.8 + j0.0kVA. In this case, the p.u. load on the dc-side is higher than that on the ac-side. This is reflected in the power flow through the IC, as shown in Fig. 7.2c. For 30s < t < 60s, the active power transferred through the IC is  $P_{IC}^* = 1.5$ kW (the reactive power-sharing does not change). At t = 60s, the initial loading condition is resumed, i.e.,  $P_{IC}^* \approx 0$ kW.

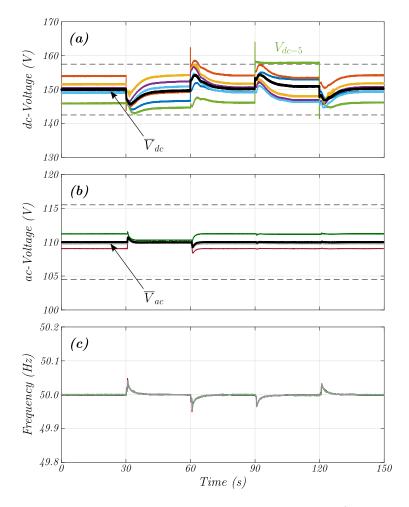


Figure 7.3: Variables of the secondary control on the hybrid ac/dc-microgrid for Test #1. (a) Voltages on the dc-DGs  $(V_j, j = 1, ..., 6)$ . (b) Phase-to-neutral RMS voltages on the ac-DGs  $(V_i, i = 1, 2, 3)$ . (c) Frequency of the voltages on the ac-DGs  $(f_i, i = 1, 2, 3)$ .

At t=90s, a new load change is applied on both ac- and dc-sides. The load at node 5 (dc-side) is step-decreased to 0.00kW, while the load at node 1 (ac-side) is step-increased to 1.7 + j0.8kVA. Now, the percentage of ac-load is higher than that on the dc-side [see Fig. 7.2.(c)]. Therefore, for 90s < t < 120s,  $P_{\rm IC}^* = -1.5$ kW and the reactive power-sharing on the ac-side does not change [see Fig. 7.2d].

It is important to note that, for 90s < t < 120s, the dc-DG<sub>5</sub> [green line in Fig. 7.2b] is not participating in the power-sharing. In this time period, the load  $R_5$  is disconnected from node 5 on the dc-microgrid (see Fig. 5.8). Therefore, a high voltage is obtained at the dc-DG<sub>5</sub> output, when (3.9a)-(3.9b) are applied. Hence, it is not possible for this DG to participate in the power-sharing without surpassing the dc-voltage upper limit [see Fig. 7.3a]. This problem can be solved using the tertiary control system by, for instance, changing the value of  $V^*$  for all or some of the dc-DGs [see (3.9a)-(3.9b)]. However, tertiary control system is outside the scope of this thesis.

Finally, at t=120s the loads are step-changed back to the initial condition until the end of the test (t=150s), i.e.,  $P_{\rm IC}^* \approx 0 {\rm kW}$ , and all DGs participate in power-sharing.

#### 7.3 Experimental Test #2: Unit Loss

In this test, the DGs on both sides of the hybrid microgrid and the IC are disconnected and then reconnected to test the plug-and-play capability of the proposed strategy. For this test, the load condition is summarized in Table 7.2 and it is not changed during the test. The total load of the hybrid ac/dc-microgrid is 16.5kW (9.6kW on dc-side and 6.9kW on ac-side) and 1.9kVAr, which represents 68.8% of the nominal active power (24.0kW) and 21.1% of the nominal reactive power (9.0kVAr), as shown in Fig. 7.4a. The power on the dc-side corresponds to 64.0% of the nominal power of this side (15.0kW), while the active power on the ac-side is 76.7% of the nominal power of this side (9.0kW).

Table 7.2: Experimental ac/dc-microgrid, load conditions for test #2.

	dc-s	side loa	ads, in	kW		ac-si	de loads, in	kVA
$R_1$	$R_2$	$R_3$	$R_4$	$R_5$	$R_6$	$Z_1$	$Z_2$	$\overline{Z_3}$
2.05	0.00	1.37	2.05	2.05	2.05	2.2 + j1.1	2.4 + j0.0	2.3 + j0.8

At t=0s, both ac-DGs and dc-DGs are perfectly sharing the load power [see Fig. 7.4b]. Because the p.u. load on the ac-side is larger than that on the dc-side, the power flow in the IC is negative ( $P_{\rm IC}^* \approx -0.8 {\rm kW}$ ), as shown in Fig. 7.4c. The reactive power-sharing on the ac-side is shown in Fig. 7.4d. On the other hand, secondary variables of the hybrid ac/dc-microgrid are shown in Fig. 7.5. The secondary variables, i.e., dc-voltage [Fig. 7.5a], ac-voltage [Fig. 7.5b] and frequency [Fig. 7.5c] are maintained within the accepted limits.

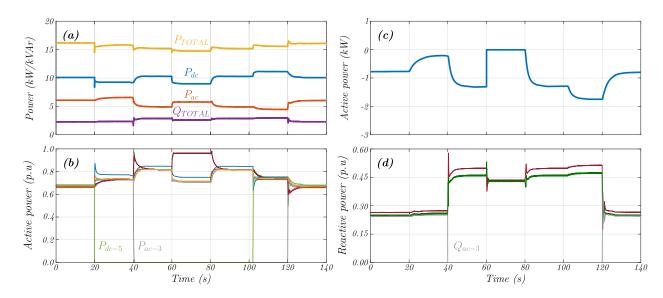


Figure 7.4: Power on the hybrid ac/dc-microgrid for Test #2. (a) Total power (active and reactive). (b) Active power generated by ac-DGs ( $P_{ac-i}$ , i = 1, 2, 3) and dc-DGs ( $P_{dc-j}$ ,  $j = 1, \ldots, 6$ ), in p.u. (c) Active power through the IC ( $P_{IC}$ ). (d) Reactive power generated by ac-DGs ( $Q_i$ , i = 1, 2, 3), in p.u.

At t = 20s, dc-DG<sub>5</sub> fails and it is disconnected from both the microgrid and the communication network, and its power and voltage are reduced to zero (see green line in Fig. 7.4b and Fig. 7.5a, respectively) and the other dc-DGs and ac-DGs maintain power-sharing and restoration of the secondary variables, as shown in Fig. 7.4b, Fig. 7.4d and Fig. 7.5. Due to

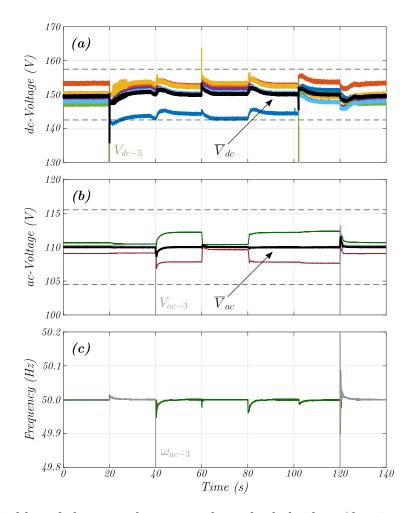


Figure 7.5: Variables of the secondary control on the hybrid ac/dc-microgrid for Test #2. (a) Voltages on the dc-DGs  $(V_j, j = 1, ..., 6)$ . (b) Phase-to-neutral RMS voltages on the ac-DGs  $(V_i, i = 1, 2, 3)$ . (c) Frequency of the voltages on the ac-DGs  $(f_i, i = 1, 2, 3)$ .

the absence of dc-DG<sub>5</sub>, the power transferred from the dc-side to the ac-side is reduced, as shown in Fig. 7.4c.

At t = 40s,  $ac\text{-DG}_3$  fails and it is disconnected from both the microgrid and the communication network, and its power, voltage and frequency are all reduced to zero (see grey line in Fig. 7.4b, Fig. 7.4d, Fig. 7.5b and Fig. 7.5c, respectively) and the remaining  $dc\text{-DG}_3$  and  $ac\text{-DG}_3$  maintain power-sharing and restoration of the secondary variables, as shown in Fig. 7.4b, Fig. 7.4d and Fig. 7.5. Now, the absence of  $ac\text{-DG}_3$  produces an increase in the power transferred from the dc-side to the ac-side, as shown in Fig. 7.4c.

At t = 60s, the *IC* fails and it is disconnected from both the microgrid and the communication network, and its power is reduced to zero [see Fig. 7.4c]. This splits the hybrid microgrid into two independent systems; therefore, the power-sharing is now performed only among the units in the same sub-microgrid, as shown in Fig. 7.4b. Because the p.u. load on the ac-side is higher than that on the dc-side, the p.u. powers generated by the ac-DGs are larger than those generated by the dc-DGs. On the other hand, the secondary variables are maintained within the operational limits all the time (see Fig. 7.5).

Finally, the failed units are reconnected to resume normal operation. The IC is reconnected at t = 80s, while dc-DG<sub>5</sub> is reconnected at t = 100s and ac-DG<sub>3</sub> at t = 120s. The microgrid continues operating as expected after each re-connection, maintaining both power-sharing and regulation of the secondary variables.

#### 7.4 Experimental Test #3: Communication Loss

In this test, the base case corresponds to the scenario where all units are connected to the communication network, as described in Fig. 7.6a. The base load condition is 6.0kW for the ac-microgrid (66.6% of its nominal power) and 9.6kW for the dc-microgrid (64.0% of its nominal power), as shown in Table 7.3. For emulating the loss of communication links between DGs, units ac-DG<sub>1</sub> and dc-DG<sub>2</sub> are disconnected from the communication network at t = 40s (see Fig. 7.6b), and then re-connected to the communication network at t = 240s (see Fig. 7.6a). The other DGs remain communicating for all the test.

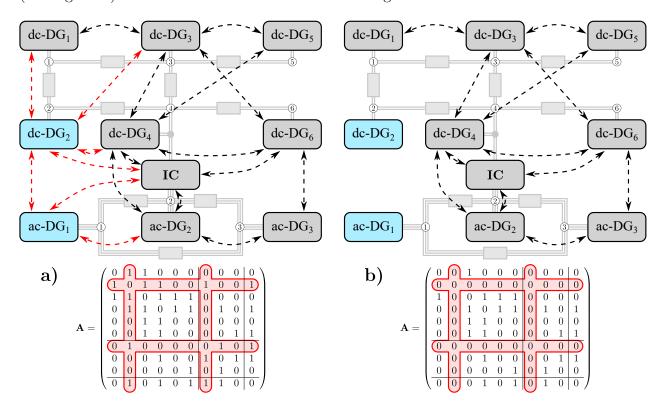


Figure 7.6: Communication network for test #3. a) Normal operation. b) Communication loss operation.

Table 7.3: Experimental ac/dc-microgrid, load conditions for test #3.

Load	kW	Load	kW	Load	kVA
$\overline{R_1}$	2.05	$\overline{R_4}$	2.05	$\overline{Z_1}$	2.5 + j1.2
$R_2$	0.00	$R_5$	2.05	$Z_2$	2.4 + j0.0
$R_3$	1.37	$R_6$	2.05	$Z_3$	1.1 + j0.0

The total power generated in this test is shown in 7.7a. The test begins with all the control loops activated; therefore, all the DGs are sharing both the active and reactive power, as

shown in Fig. 7.7b and Fig. 7.7d, respectively. As the load (in p.u.) on the ac-side is different from that of the dc-side, there is a transfer of power through the IC, as shown in Fig. 7.7c.

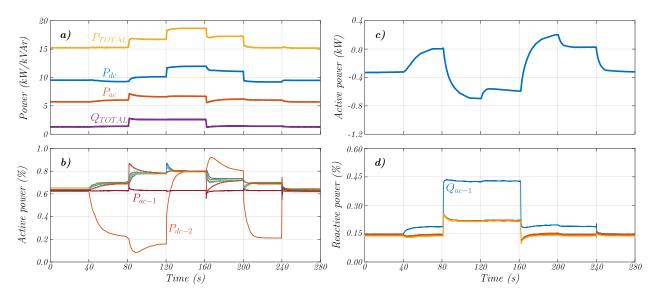


Figure 7.7: Power on the hybrid ac/dc-microgrid for Test #3. (a) Total power (active and reactive). (b) Active power generated by ac-DGs ( $P_{ac-i}$ , i = 1, 2, 3) and dc-DGs ( $P_{dc-j}$ ,  $j = 1, \ldots, 6$ ), in p.u. (c) Active power through the IC ( $P_{IC}$ ). (d) Reactive power generated by ac-DGs ( $Q_i$ , i = 1, 2, 3), in p.u.

As mentioned before, in t=40s units  $ac\text{-DG}_1$  and  $dc\text{-DG}_2$  are disconnected from the communication network. Therefore, the disconnected units stop performing power-consensus with the other units and the distributed controllers only take care of the local variables regulation. Since the frequency is a global variable on the ac-side,  $ac\text{-DG}_1$  continues supplying the same amount of power, while  $dc\text{-DG}_2$  harshly reduces the generated power (see Fig. 7.7b) due to now it focuses on regulating the local dc-voltage. Results for the secondary variables are shown in Fig. 7.8. The power transferred by the IC also changes when the units disconnected from the communication network stop performing power-consensus, as shown in Fig. 7.7c.

Several load steps are applied on both sides of the hybrid microgrid, as follows:

- t = 80s: load power in the ac-microgrid is increased to 8.4kW (93.3%).
- t = 120s: load power in the dc-microgrid is increased to 11.65kW (77.3%).
- t = 160s: load power in the ac-microgrid is decreased to 6.0kW (66.6%).
- t = 200s: load power in the dc-microgrid is decreased to 9.6kW (64.0%).

In all cases, the units connected to the communication network continue performing the power-consensus task, while the disconnected units only regulate their local variables. The IC continues transferring power between the sub-microgrids, helping to the ac-DGs to reach the power-consensus with the dc-DGs, and vice versa.

As soon as the units ac-DG<sub>1</sub> and dc-DG<sub>2</sub> are re-connected to the communication network,

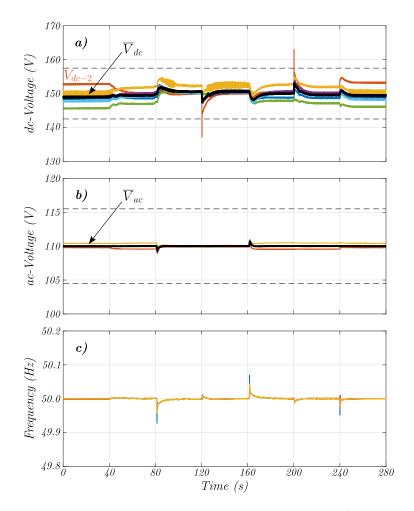


Figure 7.8: Variables of the secondary control on the hybrid ac/dc-microgrid for Test #3. (a) Voltages on the dc-DGs  $(V_j, j = 1, ..., 6)$ . (b) Phase-to-neutral RMS voltages on the ac-DGs  $(V_i, i = 1, 2, 3)$ . (c) Frequency of the voltages on the ac-DGs  $(f_i, i = 1, 2, 3)$ .

at t = 240s, they start participating in the power consensus and they continue performing the secondary variable regulation. Therefore, the proposed control strategy is validated against communication loss issues.

## 7.5 Experimental Test #4: Communication Delays

In this test, a constant delay  $\tau_d$  is introduced in the consensus terms of the controllers for analyzing the performance of the controller against communication delays, as shown in (7.1)-(7.4). Different delays are considered in this test, which are considered as short-delay, medium delay and long delay. A delay of one second is considered to be a large delay, as described in [107, 108]; such a delay may be due to weather conditions or line of sight requirements in rural/remote areas [141]. The tested cases are:

- (i) base case  $(\tau_d = 0.0s)$ ,
- (ii) small time-delay ( $\tau_{\rm d}=0.1s$ ),

- (iii) medium time-delay ( $\tau_{\rm d} = 0.5s$ ), and
- (iv) large time-delay ( $\tau_{\rm d} = 1.0s$ ).

$$\sigma_{i}\dot{\psi}_{i} = -(\omega_{i} - \omega^{*}) + \psi_{ac-i} + \psi_{dc-i}$$

$$(7.1a)$$

$$\psi_{\text{ac-i}} = -\sum_{j \in \mathbb{N}_{\text{ac}}} a_{ij} \left( P_{\text{ac-i}} - P_{\text{ac-j}}(t - \tau_{\text{d}}) \right)$$
 (7.1b)

$$\psi_{\text{dc-i}} = -\sum_{j \in \mathbb{N}_{\text{dc}}} a_{ij} \left( P_{\text{ac-i}} - P_{\text{dc-}j} (t - \tau_{\text{d}}) \right)$$
 (7.1c)

$$\rho_{i}\dot{\varphi}_{i} = -\gamma_{i}\left(V_{i} - V^{*}\right) + \varphi_{dc-i} + \varphi_{ac-i} \tag{7.2a}$$

$$\varphi_{\mathrm{dc-i}} = -c_{\mathrm{i}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{\mathrm{i}j} \left( P_{\mathrm{dc-i}} - P_{\mathrm{dc-j}} (t - \tau_{\mathrm{d}}) \right)$$

$$(7.2b)$$

$$\varphi_{\mathrm{ac-i}} = -c_{\mathrm{i}} \sum_{j \in \mathbb{N}_{\mathrm{ac}}} a_{\mathrm{i}j} \left( P_{\mathrm{dc-i}} - P_{\mathrm{ac-}j} (t - \tau_{\mathrm{d}}) \right)$$
 (7.2c)

$$\tau_k \dot{P}_{IC-k}^* = -\sum_{i \in \mathbb{N}_{ac}} \sum_{j \in \mathbb{N}_{dc}} a_{ik} a_{jk} \left( P_{ac-i} - P_{dc-j} (t - \tau_d) \right)$$
 (7.3)

$$\varrho_{\mathbf{i}}\dot{\chi}_{\mathbf{i}} = -\beta_{\mathbf{i}} \left( E_{\mathbf{i}} - E^* \right) - b_{\mathbf{i}} \sum_{j \in \mathbb{N}_{ac}} a_{\mathbf{i}j} \left( Q_{\mathbf{i}} - Q_j (t - \tau_{\mathbf{d}}) \right)$$
 (7.4)

The base load condition is 5.9kW for the ac-microgrid (65.5% of its nominal power) and 11.6kW for the dc-microgrid (77.3% of its nominal power), as shown in Table 7.4.

Table 7.4: Experimental ac/dc-microgrid, load conditions for test #4.

Load	kW	Load	kW	Load	kVA
$R_1$	2.05	$R_4$	2.05	$\overline{Z_1}$	1.7 + j0.0
$R_2$	2.05	$R_5$	2.05	$Z_2$	2.4 + j0.0
$R_3$	1.37	$R_6$	2.05	$Z_3$	1.8 + j0.0

At t=0, since all the control loops are activated and the system is at steady-state, both ac-DGs and dc-DGs are sharing the load power for all the studied values of  $\tau_{\rm d}$ , as shown in Fig. 7.9a-d and Fig. 7.11a-d for active and reactive power, respectively, and the power is being transferred through the IC as shown in Fig.7.9e-h. On the other hand, the secondary variables are also regulated within limits for all the studied values of  $\tau_{\rm d}$ , as shown in Fig. 7.10a-d, Fig. 7.10e-h and Fig. 7.11e-h, for frequency, dc-voltage and ac-voltage, respectively.

A load step is applied in the ac-side between t = 30s and t = 60s. At t = 30s, loads  $Z_1$  and  $Z_3$  are increased to 2.9 + j1.4kVA and 2.8 + j1.4kVA, respectively. The initial power load condition is resumed at t = 60s, i.e.,  $Z_1 = 1.7 + j0.0kVA$  and  $Z_3 = 1.8 + j0.0kVA$ .

It can be seen that the control algorithm continues working correctly for all the studied values of  $\tau_{\rm d}$ : the power consensus is achieved (see Fig. 7.9a-d and Fig. 7.11a-d for active and reactive power, respectively) and the secondary variables are maintained within limits (see Fig. 7.10a-d, Fig. 7.10e-h and Fig. 7.11e-h, for frequency, dc-voltage and ac-voltage,

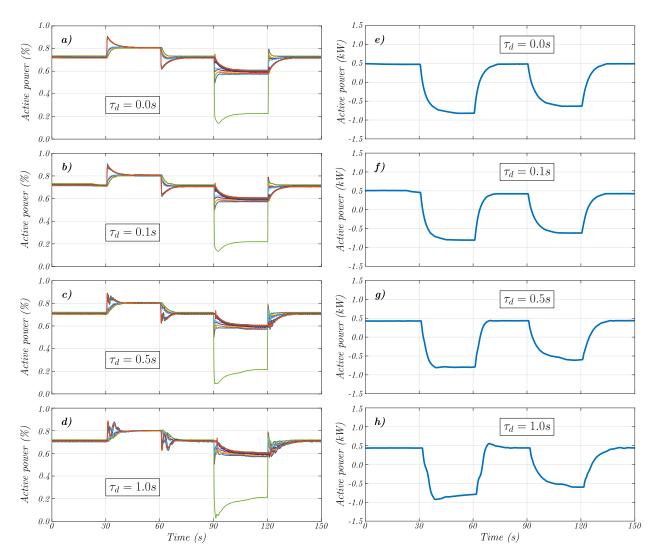


Figure 7.9: Test #4: (a-d) Active power generated by ac-DGs ( $P_{ac-i}$ , i = 1, 2, 3) and dc-DGs ( $P_{dc-j}$ ,  $j = 1, \ldots, 6$ ), in p.u. (e-h) Active power through the IC ( $P_{IC}$ ).

respectively). On the other hand, the power transferred by the IC is adjusted to help maintaining the power consensus among DGs (see Fig.7.9e-h).

Between t = 90s and t = 120s, a load step is applied in the dc-side. At t = 90s, loads  $R_2$  and  $R_5$  on the dc-side are disconnected. As discussed in test #1 (Section 7.2), dc-DG<sub>5</sub> is disconnected from the power-sharing controller when load  $R_5$  is disconnected (see Fig. 7.9a-d), in order to maintain its voltage within limits. Then, the base load condition is resumed at t = 120s, i.e.,  $R_2 = 2.05kW$  and  $R_5 = 2.05kW$ . As in the previous case, the control algorithm continues working correctly for all the studied values of  $\tau_d$ .

It is worth noting that, even though the control algorithm reaches the steady-state condition after applying the load steps at both sides of the hybrid ac/dc-microgrid, the transient response becomes more oscillatory as the delay increases. Moreover, the overshoot of the transient response also becomes larger for long delays. Therefore, the proposed control scheme is able to handle communication issues as communication delays; however, special attention has to be given to the transient response in order to avoid surpassing the operational limits

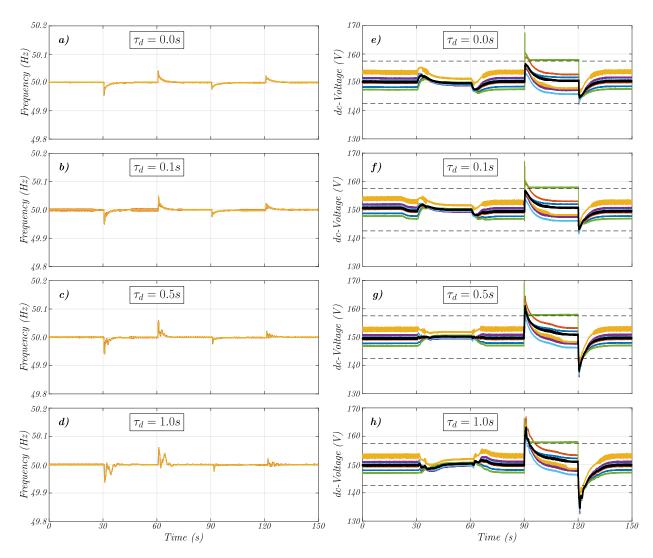


Figure 7.10: Test #4: (a-d) Frequency of the voltages on the ac-DGs  $(f_i, i = 1, 2, 3)$ . (e-h) Voltages on the dc-DGs  $(V_j, j = 1, ..., 6)$ .

of the DGs.

# 7.6 Simulation Test #5: Comparison With Performance of Other Strategies

As mentioned before, the performance of the proposed control strategy is compared to the performances obtained with strategies proposed in [21] and [134]. The strategies proposed in [21, 134] are based on normalized droop curves on both sides of the IC to generate the power reference for the IC, while the work in [134] also restores the secondary variables. Load impacts and plug-and-play capabilities are studied using simulation work. For further details about the parameters and the topology of the simulated microgrid see Section 5.5.

The results obtained using the control strategy presented in [21] are depicted in the left column of Fig. 7.12, while the results obtained with the strategy presented in [134] are shown in the central column of Fig. 7.12. The results obtained with the strategy proposed in this

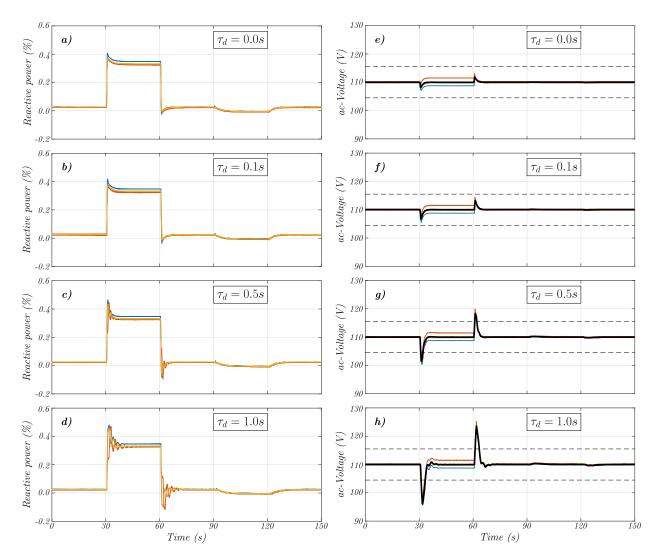


Figure 7.11: Test #4: (a-d) Reactive power generated by ac-DGs ( $Q_i$ , i = 1, 2, 3), in p.u. (e-h) Phase-to-neutral RMS voltages on the ac-DGs ( $V_i$ , i = 1, 2, 3).

work are illustrated in the right column of Fig. 7.12.

For simulation purposes, using strategies proposed in [21] and [134], each ac-DG and dc-DG is controlled using the strategies presented in Section 3.3 and Section 3.4, respectively. However, for [21] secondary control is not applied. Additionally, for [21] and [134] the elements of the adjacency matrix given in (5.1) are all zero since neither [21] nor [134] consider power consensus. On the other hand, the adjacency matrix used in the proposed strategy is shown in (5.1).

To check the effects produced by the connection and disconnection of the IC, it is disconnected from the microgrid in two periods of time, 35s < t < 75s and 115s < t < 155s. Additionally, there are three load power scenarios in this test:

•  $P_{dc} < P_{ac}$  : 15s < t < 55s

•  $P_{dc} > P_{ac}$  : 95s < t < 135s

#### • $P_{\mathrm{d}c} \approx P_{ac}$ : for the rest of the time

First, when the IC is controlled using the strategy presented in [21], the power-sharing is not achieved between DGs located on different sides of the hybrid ac/dc-microgrid, even when the IC is connected [see Fig. 7.12a], and the secondary variables are not restored [see Fig. 7.12c and (7.5)]. Therefore, the power transferred through the IC [see Fig. 7.12b] is not utilized to achieve sharing of the per unit power on both sides.

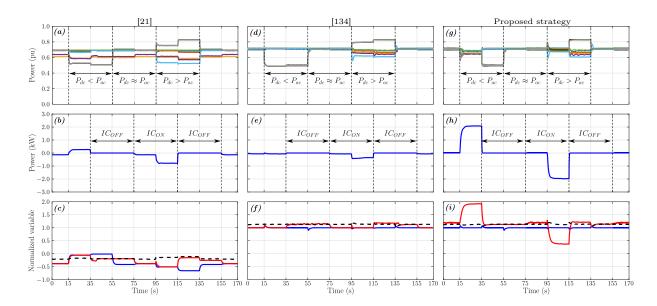


Figure 7.12: Test #5: Top row: Active power in p.u. generated by ac-DGs and dc-DGs. Middle row: Active power transferred by the IC. Bottom row: Normalized dc-voltage (in red) and normalized frequency (in blue) on both sides of the IC, the black dashed lines show the normalized average dc-voltage in the dc-microgrid.

Second, when the IC is controlled using the strategy proposed on [134], power-sharing is not achieved among DGs located on different sides of the hybrid ac/dc-microgrid [see Fig. 7.12d] although the secondary variables are restored to their nominal values [see Fig. 7.12(f)]. Furthermore, after the secondary variables are restored to their nominal values, the power transferred through the IC [see Fig. 7.12(e)] is negligible (the difference between the normalized values tends to be zero due to the secondary controllers).

Finally, if the IC is controlled using the strategy proposed in this work, it is capable of achieving good power-sharing among the ac- and dc-DGs [see Fig. 7.12(g)] while the IC is connected. If the IC is disconnected, the power-sharing is achieved within each submicrogrid, dividing the controller into two independent sub-controllers. The IC and the proposed control system make it possible to achieve power-sharing between the ac-side and the dc-side, as shown in Fig. 7.12(h), while the secondary variables are well regulated [see Fig. 7.12(i)]. Although the normalized average voltage on the dc-side (see black dashed lines in the bottom row of Fig. 7.12) is not equal to the nominal value, the voltages are maintained within the operation limits due to the compromise between power consensus and voltage regulation.

The normalized frequencies  $(f_{pu})$  and voltages  $(V_{pu})$  shown in the bottom row of Fig. 7.12

are calculated using:

$$f_{\text{pu}} = \frac{f - \frac{1}{2}(f_{\text{max}} + f_{\text{min}})}{\frac{1}{2}(f_{\text{max}} - f_{\text{min}})}, \ V_{\text{pu}} = \frac{V - \frac{1}{2}(V_{\text{max}} + V_{\text{min}})}{\frac{1}{2}(V_{\text{max}} - V_{\text{min}})}.$$
(7.5)

A summary of the comparison tests for strategies proposed in [21], [134] and the proposed strategy (P. S.) is shown in Table 7.5. By using [21], it is not possible to address the same control tasks that can be addressed by the proposed strategy. This is evident since [21] only equalizes the normalized dc-voltage and frequency at two ports of the IC without any secondary control strategy. Therefore, the IC only cares about its local variables and the power-sharing solely depends on the droop controls applied, which is a drawback when microgrids have a large number of buses. By using [134], the secondary control is applied restoring the secondary variables. However, the power consensus between ac- and dc-DGs is not achieved since the IC only equalizes its local normalized variables. In contrast, the proposed strategy restores secondary variables while achieving power consensus between ac- and dc-DGs. This power consensus is reached as long the IC is working, i.e., as long as the path for transferring energy exists.

Table 7.5: Comparison test summary.

	[21]	[134]	P. S.
Power-consensus, <i>IC</i> working	Х	Х	1
Frequency restoration	X	1	1
dc-voltage restoration	X	✓	✓

### 7.7 Simulation Test #6: Multiple ICs Operation

In this subsection, the performance of the proposed control methodology is evaluated when three ICs and six DGs, on each side of the hybrid ac/dc-microgrid, are considered. The studied topology is shown in Fig. 7.13.

Two scenarios are analyzed. Firstly, the operation of the proposed controller presented in Section 3.5 is evaluated (left column in Fig. 7.14), i.e., power-consensus among ICs is not considered in this test. Secondly, the proposed control methodology presented in Section 3.6 (including power-consensus among ICs), is simulated with the results being shown in the right column of Fig. 7.14.

For t < 10s, the ICs are not connected to the hybrid ac/dc-microgrid, i.e., the ac-side is not connected to the dc-side and the power-sharing among DGs on both sides of the microgrid is not active. Note that for t < 60s, the load power on the ac-side is higher than the load power on the dc-side [see Fig. 7.14a&d]. At t = 10s,  $IC_1$  is connected and active power is transferred from the dc-side to ac-side initiating power-consensus between the ac- and dc-DGs. In this case, the control system performance is similar for both scenarios, considering that a single IC is connected.

At t = 20s,  $IC_2$  is connected. The power-consensus among ac- and dc-DGs is maintained but the power-consensus between  $IC_1$  and  $IC_2$  is not achieved in the first scenario, but

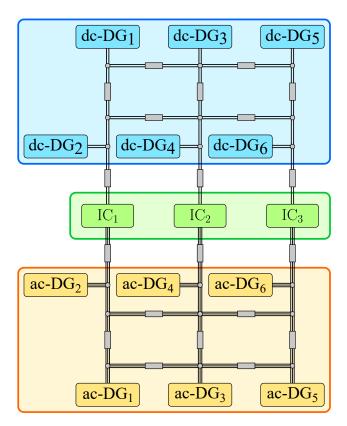


Figure 7.13: Test #6: Topology of the simulated microgrid.

it is achieved in the second one (see Fig. 7.14b&e. At t=30s,  $IC_3$  is activated and the same results in terms of power-sharing among the ICs are obtained. The power-consensus among ICs in the first scenario is not achieved because, when the IC is connected, the power-consensus among DGs on both sides has already been achieved. Therefore  $P_{IC}^*$  is not modified for  $IC_1$  (see (3.10)).

Then,  $IC_1$ ,  $IC_2$  and  $IC_3$  are disconnected at t = 40s, t = 50s and t = 60s, respectively. In both scenarios the power-consensus among DGs is maintained and the power-sharing among ICs is also achieved. In the first scenario, the latter is achieved because the parameters of the controllers are the same for each IC; otherwise power-sharing among ICs is not ensured. Finally, at t = 70s, a load impact is applied and the load power on the dc-side is higher than the load power on the ac-side. At t = 80s, all the ICs are connected at the same time and both power-consensuses are achieved. Note that, in both scenarios, the secondary variables are correctly regulated, as shown in Fig. 7.14c&f.

It is important to clarify that the proposed power consensus strategy for the ICs [see (3.12)] does not produce a circulating current/power between them. Therefore, it is not necessary to utilize auxiliary ac control signals as it is discussed in [82]. The methodology utilized to achieve power-consensus among the ICs eliminate any possible mismatch in the power flow transferred through the ICs.

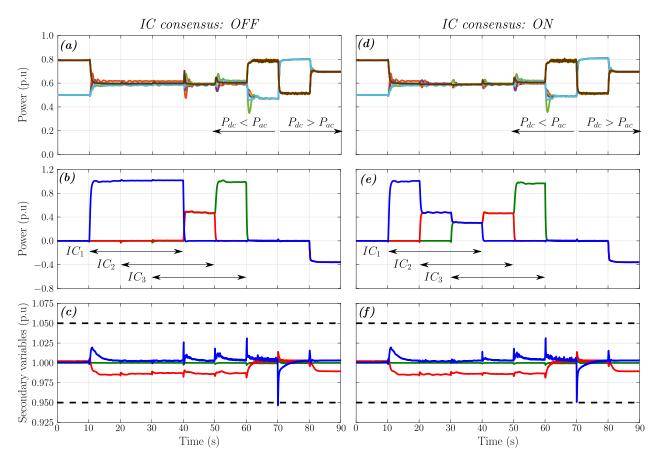


Figure 7.14: Test #6: (a)&(d) Active power in p.u. generated by the ac- and dc-DGs. (b)&(e) Active power in p.u. transferred through the ICs. (c)&(f) Secondary variables in per unit, ac-side frequency (green), ac-voltage (red) and dc-voltage (blue).

## 7.8 Summary

In this Chapter, experimental and simulation results were presented to validate the distributed consensus-based secondary control strategy for power-sharing proposed in this thesis. The experimental validation was performed on a 24kW scaled-down prototype of a hybrid ac/dc-microgrid, while the validation through simulation was performed in the same system and in a new one based on the experimental prototype.

In summary, the experimental and simulation results demonstrated the advantages of the proposed method, which are:

- Active power consensus is achieved among the ac-DGs and dc-DGs when a load impact is applied on either side of the hybrid ac/dc-microgrid and when a DG is connected/disconnected to/from the microgrid.
- The secondary control variables, i.e., average dc- and ac-voltages, and frequency, are restored to their nominal values in steady-state under all the analyzed cases.
- The IC participates actively in the proposed secondary control strategy, transferring power from the dc-side to the ac-side (or vice versa) according to the loading of the

sub-microgrids.

- The DGs can be easily connected to or disconnected from the microgrid as well as the distributed secondary control system, demonstrating the plug-and-play capability of the proposed strategy (see Test #2 results).
- The proposed scheme is able to handle some of the most common communication issues that can be found in a microgrid, such as the loss of communication channels or communication delays (see Test #3 and Test #4 results).
- Since the consensus-based control strategy proposed in this work considers the hybrid ac/dc-microgrid as a single entity, it behaviour is better than the ones reported in the literature that consider the hybrid ac/dc-microgrid as three independent systems which interact between them [21, 134] (see Test #5 results).
- By including the power-consensus term among the ICs it is possible to avoid unnecessary losses and saturation of the ICs due to the circulating currents (see Test #6 results).

## Chapter 8

## Validation of the Strategy Proposed for Operation Cost Minimization

#### 8.1 Introduction

In this Chapter, simulation tests are performed for validating the consensus-based distributed control strategy for operation cost minimization proposed in Chapter 4. Two systems were simulated: the system described in Section 5 for testing the strategy proposed for a microgrid with a single IC, and a modified microgrid for evaluating the strategy proposed for a hybrid ac/dc-microgrid with multiple ICs.

The Chapter is organized as follows: Section 8.2 presents the validation of the control strategy for a hybrid ac/dc-microgrid with a single IC proposed in Section 4.3; the cost function and the constraints defined in the formulation of the optimization problem are successfully analyzed. The performance of the aforementioned control strategy against load steps is validated in Section 8.3. Then, Section 8.4 shows the validation of the control strategy proposed for a microgrid with multiple ICs proposed in Section 4.5; the cost function and the power limit constraints defined in the formulation of the optimization problem are successfully analyzed. The validation of the control strategy mentioned before against multiple load changes is presented in Section 8.5. Finally, a comparison between the two consensus-based distributed secondary control strategies proposed in this work is depicted in Section 8.6.

## 8.2 Simulation Test #7: Single IC Performance

First, the control strategy proposed for minimizing the operation cost in a microgrid with a single IC is analyzed (see Section 4.3). The microgrid presented in Chapter 5 is simulated, maintaining the parameters (Table 5.4) and communication network (adjacency matrix) (5.1) described in that Chapter. Due to the control strategy analyzed in this Chapter is focused on minimizing the operation cost of the hybrid ac/dc-microgrid, the parameters for the cost functions of the DGs [see (4.4)] are depicted in Table 8.1 (for the ac-DGs) and Table 8.2 (for the dc-DGs). On the other hand, the operation limits for the DGs are:

• 
$$P_{\text{ac}-i}^{\text{G-}} = 0.0kW$$
,  $P_{\text{ac}-i}^{\text{G+}} = 3.0kW$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$   
•  $P_{\text{dc}-j}^{\text{G-}} = 0.0kW$ ,  $P_{\text{dc}-j}^{\text{G+}} = 2.5kW$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$  (8.1)

Table 8.1: Parameters for the cost functions of the ac-DGs [eq. (4.4a)].

Unit	$\mid a_{ m ac-i} \mid$	$b_{ m ac-i}$	$c_{ m ac-i}$
$ac ext{-}\mathrm{DG}_1$	0.44	1.1	70
ac-DG <sub>2</sub>	0.49	2.1	60
$ac$ -DG $_3$	0.31	3.1	55

Table 8.2: Parameters for the cost functions of the dc-DGs [eq. (4.4b)].

Unit	$\mid a_{\mathrm{dc}-j} \mid$	$b_{{ m dc}-j}$	$c_{\mathrm{dc}-j}$	Unit	$a_{\mathrm{dc}-j}$	$b_{{ m dc}-j}$	$c_{\mathrm{dc}-j}$
dc-DG <sub>1</sub>	0.28	2.6	57	dc-DG <sub>4</sub>	0.51	2.8	68
dc-DG <sub>2</sub>	0.39	1.8	64	dc-DG <sub>5</sub>	0.21	3.3	61
dc-DG <sub>3</sub>	0.46	1.2	58	dc-DG <sub>6</sub>	0.79	0.9	62

The losses in the IC are assumed by  $k^{LOSS} = 4\%$  (see Section 4.2.1), and its operation limits are given by (8.2). For simplicity, losses are assumed constant for the entire operating range.

• 
$$P_{\rm IC}^{\rm ac-} = -2.0kW$$
,  $P_{\rm IC}^{\rm ac+} = 2.0kW$  (8.2)

The local-load of the 9 DGs remains constant during this test, and it is summarized in Table 8.3. The total load of the hybrid ac/dc-microgrid is 18.48kW (9.55kW on dc-side and 8.93kW on ac-side) and 1.9kVAr, i.e., 77% of the nominal active power (24.0kW) and 21.1% of the nominal reactive power (9.0kVAr). The power on the dc-side is 63.7% of the nominal power of this side (15.0kW); meanwhile, the active power on the ac-side corresponds to 99.2% of 9.0kW. The operating conditions analyzed in this test are summarized in Table 8.4, and the results are shown in Fig. 8.1 and Fig. 8.2.

Table 8.3: Hybrid ac/dc-microgrid with a single IC, load conditions for test #7.

Load	kW	Load	kW	Load	kVA
$R_1$	2.05	$R_4$	1.03	$\overline{Z_1}$	2.22 + j0.8
$R_2$	2.05	$R_5$	1.02	$Z_2$	4.07 + j0.0
$R_3$	1.37	$R_6$	2.05	$Z_3$	2.64 + j1.1

Table 8.4: Summary for the operating conditions analyzed in simulation test #7.

Operating condition	Time
No optimization	0s - 100s
Optimization within microgrids	100s - 200s
Full optimization enabled	200s - 300s
ac-DGs limits constraint active	300s - 400s
dc-DGs limits constraint active	400s - 500s
IC limits constraint active	500s - 600s

The simulation starts with the inner, primary and secondary controllers enabled. Regarding the distributed secondary control loop, only the terms responsible for the secondary variables restoration are not equal to zero, i.e., the optimization is not enabled; thus, the parameters for the controllers under the *no optimization* condition are:

• In (4.13b): 
$$k_{\text{ac-i}}^a > 0$$
,  $k_{\text{ac-i}}^b = k_{\text{ac-i}}^c = 0$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$   
• In (4.15b):  $k_{\text{dc-}j}^a > 0$ ,  $k_{\text{dc-}j}^b = k_{\text{dc-}j}^c = 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$   
• In (4.16a):  $1/\tau_{\text{IC}} = 0$  (8.3)

At t = 0s, due to the droop controllers are independently operating within each microgrid, the ac-DGs are perfectly sharing the active power generated while the frequency is maintained at the nominal value (see Fig. 8.1a and Fig. 8.1c, respectively). On the other hand, it is not possible to achieve an accurate power-sharing for the dc-DGs while regulating the dc-voltages (see Fig. 8.1d and Fig. 8.1f, respectively). Since the microgrids are working independently, the power through the IC is zero (see Fig. 8.2a). The objective function for the minimization problem is the total operating cost of the microgrid  $(C_T)$ , given by (8.4). Under this scenario, the total operating cost is  $C_T = 611.28$  (\$/h), as shown in Fig. 8.2c.

$$C_T (\$/h) = \sum_{i \in \mathbb{N}_{ac}} C_{ac-i} \left( P_{ac-i}^{G} \right) + \sum_{j \in \mathbb{N}_{dc}} C_{dc-j} \left( P_{dc-j}^{G} \right)$$

$$(8.4)$$

At t = 100s, the *optimization within each microgrid* operating condition is enabled (independently); therefore, now the parameters for the controllers are:

• In (4.13b): 
$$k_{\text{ac-i}}^a, k_{\text{ac-i}}^b > 0$$
,  $k_{\text{ac-i}}^c = 0$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$   
• In (4.15b):  $k_{\text{dc-}j}^a, k_{\text{dc-}j}^b > 0$ ,  $k_{\text{dc-}j}^c = 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$   
• In (4.16a):  $1/\tau_{\text{IC}} = 0$  (8.5)

The generated power changes with respect to the previous condition (see Fig. 8.1a and Fig. 8.1d). Moreover, now the ac-DGs and dc-DGs achieve a  $\lambda$  consensus with the units within the same microgrid, as shown in Fig. 8.2d. It can be seen that the cheapest microgrid corresponds to the dc-microgrid ( $\lambda^{ac} > \lambda^{dc}$ ). Since the IC is not participating in the control task, the power transfer remains equal to zero (see Fig. 8.2a). On the other hand, the optimization reduces the total operating cost of the microgrid [ $C_T = 609.78$  (\$/h)].

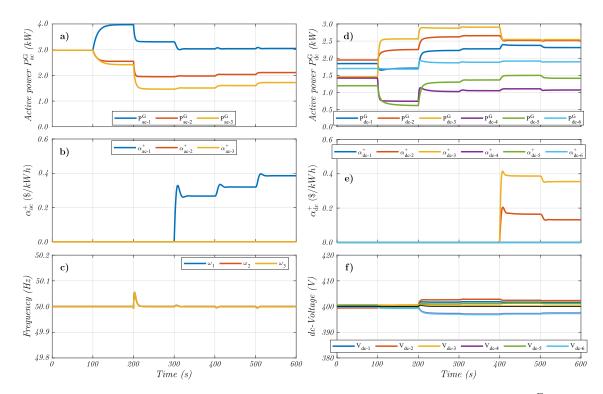


Figure 8.1: Simulation Test #7: (a)&(d) Active power generated by ac-DGs ( $P_{\text{ac}-i}^{\text{G}}$ ) and dc-DGs ( $P_{\text{dc}-j}^{\text{G}}$ ). (b)&(e) Lagrange multiplier for generated power constraints ( $\alpha_{\text{ac}-i}^+$  and  $\alpha_{\text{dc}-j}^+$ ). (c)&(f) Secondary variables (frequency and dc-voltage).

The full optimization operating condition is enabled at t = 200s. The parameters for the controllers are:

• In (4.13b):  $k_{\text{ac-i}}^a, k_{\text{ac-i}}^b, k_{\text{ac-i}}^c > 0$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$ • In (4.15b):  $k_{\text{dc-}j}^a, k_{\text{dc-}j}^b, k_{\text{dc-}j}^c > 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$ • In (4.16a):  $1/\tau_{\text{IC}} > 0$  (8.6)

Now, the power generated in the cheapest microgrid  $(P_{\text{dc}-j}^{\text{G}})$  increases and the power generated in the most expensive microgrid  $(P_{\text{ac}-i}^{\text{G}})$  decreases (see Fig. 8.1d and Fig. 8.1a, respectively). The IC starts transferring power from the cheapest microgrid to the most expensive one in order to maintain the power-balance constraint and to minimize the total operating cost  $[C_T = 608.93 \, (\$/h)]$ , as shown in Fig. 8.2c. The change in the generated powers produces a  $\lambda$  consensus between the two microgrids ( $\lambda^{\text{ac}} \approx \lambda^{\text{dc}}$ ), as shown in Fig. 8.2d.

Since the power limit constraints for the DGs and the IC have not been enabled yet, some units are working above their maximum allowed capacities (see Fig. 8.1a for  $ac\text{-DG}_1$ , Fig. 8.1d for  $dc\text{-DG}_2$  and  $dc\text{-DG}_3$ , and Fig. 8.2a for the IC). Therefore, the constraints for the ac-DGs, dc-DGs and IC are enabled at t=300s, t=400s and t=500s, respectively. It can be seen in Fig. 8.1b that the Lagrange multiplier  $(\alpha_{ac-1}^+)$  associated with the power limit constraint for the  $P_{ac-1}^G$  increases its value when this constraint is enabled (t=300s), which produces a decrease in  $P_{ac-1}^G$  and fixes it to the upper limit  $(P_{ac-1}^G = P_{ac-1}^{G+} = 3kW)$ . As a new constraint has been enabled, the total operating cost for the microgrid is  $C_T = 608.99$  (\$/h).

The same behaviour described before can be seen in Fig. 8.1e for dc-DG<sub>2</sub> and dc-DG<sub>3</sub>.

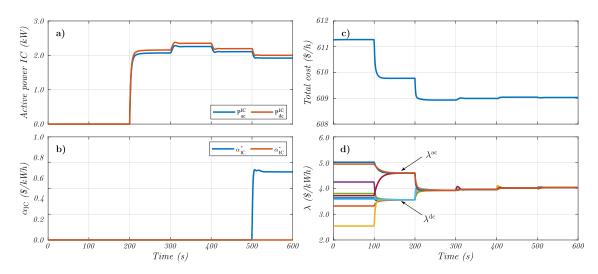


Figure 8.2: Simulation Test #7: (a) Active power through the IC. (b) Lagrange multiplier for transfer power constraints ( $\alpha_{\rm IC}^{\rm ac+}$ ). (c) Total operating cost. (d) Lagrange multiplier  $\lambda$ .

Here, the Lagrange multipliers ( $\alpha_{\text{dc}-2}^+$  and  $\alpha_{\text{dc}-3}^+$ ) increase their values when the constraints are activated (t = 400s), which produces a decrease in those powers until they touch the upper limit ( $P_{\text{dc}-2}^{\text{G}} = P_{\text{dc}-3}^{\text{G}} = P_{\text{dc}-2}^{\text{G}+} = P_{\text{dc}-3}^{\text{G}+} = 2.5kW$ ), and an increase in the total operating cost [ $C_T = 609.04 \, (\$/h)$ ].

Finally, the Lagrange multiplier associated with the power transfer constraint for the IC ( $\alpha_{\rm IC}^+$ ) can be seen in Fig. 8.2b; it also increases its value when the constraint is activated (t=500s), until the power through the IC touches the upper limit. In this case, since the power through the IC flows from the dc-side to the ac-side ( $P_{\rm IC}^{\rm ac}>0$ ), the power on the dc-side of the IC is higher than that on the ac-side of the IC ( $P_{\rm IC}^{\rm dc}>P_{\rm IC}^{\rm ac}$ ); therefore, the power constraint is considered on the side with the highest power ( $P_{\rm IC}^{\rm dc}=P_{\rm IC}^{\rm dc+}=2kW$ ). The total operating cost of the microgrid also slightly increases since a new constraint has been activated  $[C_T=609.06 \ (\$/h)]$ .

The secondary variables are restored and maintained within limits during all the test, as shown in Fig. 8.1c for the frequency in the ac-microgrid and in Fig. 8.1f for the average voltage in the dc-microgrid.

Summarizing, Table 8.5 shows the total operating cost for the hybrid ac/dc-microgrid under all the studied scenarios. It can be seen that, as expected, the minimum operating cost is achieved when the full optimization is enabled and the constraints are not being considered. On the other hand, the total operating cost increases as new constraints are being incorporated to the controller.

## 8.3 Simulation Test #8: Single IC With Load Steps

Now, the performance of the proposed control strategy against load steps is analyzed. The microgrid used in the previous Section is simulated, maintaining the parameters and communication network (adjacency matrix) described before. The parameters for the cost functions of the DGs [see (4.4)] are also the same as before (see Table 8.1 and Table 8.2). The operation

Table 8.5: Summary for the total operating cost under the studied scenarios.

Operating condition	Time	Cost (\$/h)
No optimization	0s - 100s	611.28
Opt. within microgrids	100s - 200s	609.78
Full optimization	200s - 300s	608.93
ac-DGs limits constraint	300s - 400s	608.99
dc-DGs limits constraint	400s - 500s	609.04
IC limits constraint	500s - 600s	609.06

limits for the DGs are also maintained (see (8.1) and (8.2)). The losses in the IC are assumed by  $k^{LOSS} = 4\%$  (see Section 4.2.1).

The initial local-load of the 9 DGs is summarized in Table 8.6. The total load of the microgrid is 13.11 kW (8.55kW on dc-side and 4.56kW on ac-side), i.e., 54.6% of the nominal active power. The power on the dc-side is 57.0% of the nominal power of this side; meanwhile, the active power on the ac-side corresponds to 50.7%. In this case, the reactive power loads are not considered since the proposed control strategy is not affecting the reactive power on the ac-microgrid. The results are shown in Fig. 8.3 and Fig. 8.4.

Table 8.6: Hybrid ac/dc-microgrid with a single IC, load conditions for test #8.

Load	kW	Load	kW	Load	kVA
$R_1$	1.03	$R_4$	1.03	$Z_1$	1.90 + j0.0
$R_2$	2.05	$R_5$	1.02	$Z_2$	1.65 + j0.0
$R_3$	1.37	$R_6$	2.05	$Z_3$	1.01 + j0.0

The simulation starts with all the controllers and the limit constraints enabled, i.e., the full optimization is active and the parameters for the controllers are as depicted in (8.6). For 0 < t < 50s, the DGs are achieving a  $\lambda$  consensus (see Fig. 8.4d) and the total operating cost of the microgrid (see Fig. 8.4c) is minimum  $[C_T = 588.66 \ (\$/h)]$ . The power through the IC (see Fig. 8.4a) is negligible ( $P_{\rm IC}^{\rm ac} \approx 0$ ) and the DGs are operating within limits (see Fig. 8.3a for  $P_{\rm ac-i}^{\rm G}$ , Fig. 8.3d for  $P_{\rm ac-j}^{\rm G}$ , Fig. 8.3b for  $\alpha_{\rm ac-i}^+$ , and Fig. 8.3e for  $\alpha_{\rm dc-j}^+$ ), while the secondary variables are restored (see Fig. 8.3c for frequency and Fig. 8.3f for dc-voltage).

At t=50s, a load step is applied in the ac-microgrid. The ac-load is step-increased to 8.91kW (99.0% of the nominal power at this side). Due to the increase in the load power, the generators increase their generated power (see Fig. 8.3a for  $P_{\rm ac-i}^{\rm G}$  and Fig. 8.3d for  $P_{\rm dc-j}^{\rm G}$ ) and the IC transfers power from the dc-side to the ac-side (see Fig. 8.4a). The cheapest generators touch the upper limits (see Fig. 8.3b and Fig. 8.3e for the Lagrangian multipliers  $\alpha_{\rm ac-1}^+$  and  $\alpha_{\rm dc-3}^+$ , respectively), and the IC transfers the maximum allowed power ( $P_{\rm IC}^{\rm dc+}=2kW$ ), as explained by the increase in the Lagrange multiplier  $\alpha_{\rm IC}^+$  shown in Fig. 8.4b. The secondary variables are maintained in the allowed range and the  $\lambda$  consensus is maintained by the DGs (see Fig. 8.4d). The total operating cost for the new load condition is  $C_T = 605.09$  (\$/h). At t = 200s, the initial loading condition is resumed, i.e.,  $P_{\rm IC}^{\rm dc} \approx P_{\rm IC}^{\rm ac} \approx 0kW$ .

A load change is applied at both sides at t = 350s. The ac-load is step-decreased to 3.66kW (40.7% of 9.0kW), while the dc-load is step-increased to 15.00kW (100.0% of the

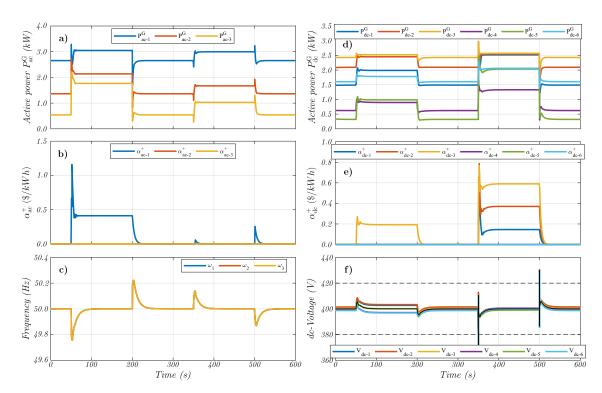


Figure 8.3: Simulation Test #8: (a)&(d) Active power generated by ac-DGs ( $P_{\text{ac}-i}^{\text{G}}$ ) and dc-DGs ( $P_{\text{dc}-j}^{\text{G}}$ ). (b)&(e) Lagrange multiplier for generated power constraints ( $\alpha_{\text{ac}-i}^+$  and  $\alpha_{\text{dc}-j}^+$ ). (c)&(f) Secondary variables (frequency and dc-voltage).

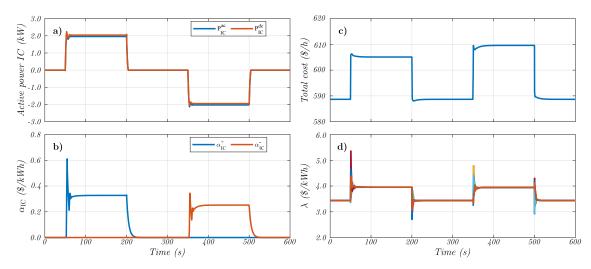


Figure 8.4: Simulation Test #8: (a) Active power through the IC. (b) Lagrange multiplier for transfer power constraints ( $\alpha_{\rm IC}^{\rm ac+}$ ). (c) Total operating cost. (d) Lagrange multiplier  $\lambda$ .

nominal power at this side). The power generated at both sides is increased; however, only that of the dc-microgrid touch the upper limits of the generators (see Fig. 8.3d for  $P_{\text{dc}-j}^{\text{G}}$  and Fig. 8.3e for the Lagrangian multipliers  $\alpha_{\text{dc}-1}^+$ ,  $\alpha_{\text{dc}-2}^+$  and  $\alpha_{\text{dc}-3}^+$ ). The power through the IC is now from the ac-side to the dc-side. Therefore, the power is now negative and it touches the lower limit ( $P_{\text{IC}}^{\text{ac}-} = -2kW$ ), as shown in Fig. 8.4b for the  $P_{\text{IC}}^{\text{ac}}$  and Fig. 8.4b for  $\alpha_{\text{IC}}^-$ . The secondary variables are within limits and the  $\lambda$  consensus is maintained by the DGs (see Fig. 8.4d). The total operating cost for the new load condition is  $C_T = 609.61$  (\$/h). At

t = 500s, the initial loading condition is resumed, i.e.,  $P_{\rm IC}^{\rm dc} \approx P_{\rm IC}^{\rm ac} \approx 0kW$ .

## 8.4 Simulation Test #9: Multiple ICs Performance

Now, the control strategy proposed for minimizing the operation cost in a microgrid with multiple ICs is analyzed (see Section 4.5). The microgrid used in Section 7.7 is considered for this part of the work (see Fig. 7.13), maintaining the parameters and communication network (adjacency matrix) described there. Due the strategy analyzed in this Chapter is focused on minimizing the operation cost of the hybrid ac/dc-microgrid, the parameters for the cost functions of the DGs [see (4.4)] are depicted in Table 8.7. On the other hand, the operation limits for the DGs are given by:

• 
$$P_{\text{ac}-i}^{\text{G}-} = 0.0kW$$
,  $P_{\text{ac}-i}^{\text{G}+} = 3.0kW$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$   
•  $P_{\text{dc}-j}^{\text{G}-} = 0.0kW$ ,  $P_{\text{dc}-j}^{\text{G}+} = 2.5kW$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$  (8.7)

Table 8.7: Parameters for the cost functions of the DGs [eq. (4.4)].

Unit	$a_{ m ac-i}$	$b_{ m ac-i}$	$c_{ m ac-i}$	Unit	$a_{\mathrm{dc}-j}$	$b_{{ m dc}-j}$	$c_{\mathrm{dc}-j}$
ac-DG <sub>1</sub>	0.55	1.5	70	dc-DG <sub>1</sub>	0.31	2.4	57
$ac\text{-}\mathrm{DG}_2$	0.49	2.4	60	dc-DG <sub>2</sub>	0.52	1.6	64
$ac ext{-}\mathrm{DG}_3$	0.44	2.3	55	dc-DG <sub>3</sub>	0.46	2.0	58
ac-DG <sub>4</sub>	0.66	1.9	67	dc-DG <sub>4</sub>	0.51	2.6	68
$ac ext{-}\mathrm{DG}_5$	0.39	2.9	43	dc-DG <sub>5</sub>	0.21	3.2	61
$ac\text{-}\mathrm{DG}_6$	0.27	3.2	76	dc-DG <sub>6</sub>	0.79	0.9	62

The losses in the ICs and their operation limits are assumed by:

• 
$$k_1^{\text{LOSS}} = 4\%$$
,  $P_{\text{IC}-1}^{\text{ac}-} = -1.2kW$ ,  $P_{\text{IC}-1}^{\text{ac}+} = 1.2kW$   
•  $k_2^{\text{LOSS}} = 6\%$ ,  $P_{\text{IC}-2}^{\text{ac}-} = -1.2kW$ ,  $P_{\text{IC}-2}^{\text{ac}+} = 1.2kW$   
•  $k_3^{\text{LOSS}} = 2\%$ ,  $P_{\text{IC}-3}^{\text{ac}-} = -1.2kW$ ,  $P_{\text{IC}-3}^{\text{ac}+} = 1.2kW$  (8.8)

The local-load of the 12 DGs remains constant during this test, and it is summarized in Table 8.8. The total load of the hybrid ac/dc-microgrid is 24.6kW (9.9kW on dc-side and 14.7kW on ac-side), i.e., 74.5% of the nominal active power (33.0kW). The load power on the dc-side ( $R_1, \ldots, R_6$ ) is 66.0% of the nominal power of this side (15.0kW); meanwhile, the active load power on the ac-side ( $Z_1, \ldots, Z_6$ ) corresponds to 81.7% of 18.0kW. In this case, the reactive power loads are not considered since the proposed control strategy is not affecting the reactive power on the ac-microgrid. The operating conditions analyzed in this test are summarized in Table 8.9, and the results for this test are shown in Fig. 8.5 and Fig. 8.6.

The simulation starts with the inner, primary and secondary controllers enabled. Regarding the distributed secondary control loop, only the optimization within each microgrid considering the operation limit constraints and the restoring terms are enabled (independently); thus, the parameters for the controllers under the *optimization within microgrids* 

Table 8.8: Hybrid ac/dc-microgrid with multiple ICs, load conditions for test #9.

Load	kW	Load	kW	Load	kW	Load	kW
$R_1$	0.9	$R_4$	2.4	$\overline{Z_1}$	2.5	$\overline{Z_4}$	2.4
$R_2$	1.3	$R_5$	1.5	$Z_2$	1.7	$Z_5$	2.1
$R_3$	2.1	$R_6$	1.7	$Z_3$	2.3	$Z_6$	3.7

Table 8.9: Summary for the operating conditions analyzed in simulation test #9.

Operating condition	Time	
Optimization within microgrids	0s - 50s	
Full optimization enabled	50s - 150s	
$\lambda^{\mathrm{IC}}$ consensus active	150s - 250s	
<i>ICs</i> limits constraint enabled	250s - 400s	

operating condition are as follows:

• In (4.27b): 
$$k_{\text{ac}-i}^{a}, k_{\text{ac}-i}^{b} > 0$$
,  $k_{\text{ac}-i}^{c} = 0$ ,  $\forall i \in \mathbb{N}_{\text{ac}}$   
• In (4.29b):  $k_{\text{dc}-j}^{a}, k_{\text{dc}-j}^{b} > 0$ ,  $k_{\text{dc}-j}^{c} = 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$   
• In (4.30a):  $\tau_{\text{IC}-k}^{a}, \tau_{\text{IC}-k}^{b} = 0$ ,  $\forall k \in \mathbb{N}_{\text{IC}}$ 

For 0s < t < 50s, the DGs are operating within limits (see Fig. 8.5a for  $P_{\mathrm{ac}-i}^{\mathrm{G}}$  and Fig. 8.5d for  $P_{\mathrm{dc}-j}^{\mathrm{G}}$ ), and the Lagrange multipliers associated to the power limits constraint are zero (see Fig. 8.5b for  $\alpha_{\mathrm{ac}-i}^+$  and Fig. 8.5e for  $\alpha_{\mathrm{dc}-j}^+$ ). The secondary variables are properly regulated (see Fig. 8.5c for frequency and Fig. 8.5f for dc-voltage). On the other hand, the power through the ICs is zero (see Fig. 8.6a) and, therefore, the losses in the ICs are also null (see Fig. 8.6c). The  $\lambda^{\mathrm{G}}$  consensus is achieved within each microgrid (see Fig. 8.6e) and the total operating cost is shown in Fig. 8.6d  $[C_T = 820.55 \ (\$/h)]$ .

At t = 50s, the full optimization operating condition of the microgrid is enabled. However, the  $\lambda^{\rm IC}$  consensus and the ICs limit constraints are maintained deactivated. Thus, the parameters for the controllers for 50s < t < 150s are as follows:

• In (4.27b): 
$$k_{\text{ac-i}}^a, k_{\text{ac-i}}^b, k_{\text{ac-i}}^c > 0$$
,  $\forall i \in \mathbb{N}_{\text{ac}}$   
• In (4.29b):  $k_{\text{dc-}j}^a, k_{\text{dc-}j}^b, k_{\text{dc-}j}^c > 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$   
• In (4.30a):  $\tau_{\text{IC-}k}^a > 0$ ,  $\tau_{\text{IC-}k}^b = 0$ ,  $\forall k \in \mathbb{N}_{\text{IC}}$ 

Enabling the  $\lambda^{\rm G}$  consensus produces an increase in the power generated in the cheapest microgrid (dc-microgrid, see Fig. 8.5d) and a decrease in that of the most expensive one (ac-microgrid, see Fig. 8.5a). Furthermore, dc-DG<sub>1</sub> and dc-DG<sub>2</sub> touch the upper limit but, in steady-state, only the former one is fixed at the maximum value ( $P_{\rm dc-1}^{\rm G} = P_{\rm dc-1}^{\rm G+} = 2.5kW$ ), as explained by the Lagrange multiplier associated to the limits constraint (see Fig. 8.5e). The secondary variables are restored after the transient and maintained within limits (see Fig. 8.5c for frequency and Fig. 8.5f for dc-voltage).

Since the power through the ICs flows from the dc-microgrid to the ac-microgrid, the power transfer is positive, as depicted in Fig. 8.6a. On the other hand, since the  $\lambda^{\rm IC}$  consensus

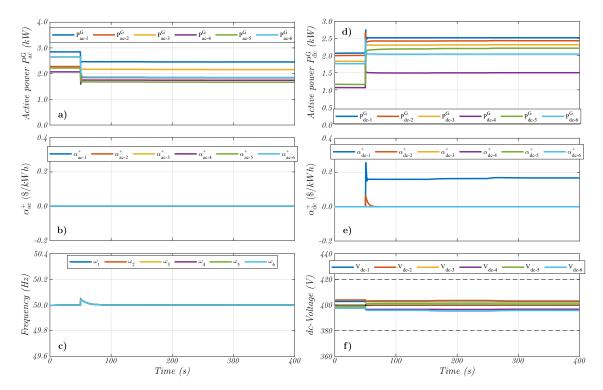


Figure 8.5: Simulation Test #9: (a)&(d) Active power generated by  $ac\text{-DGs}(P_{\text{ac}-i}^{\text{G}})$  and  $dc\text{-DGs}(P_{\text{dc}-j}^{\text{G}})$ . (b)&(e) Lagrange multiplier for generated power constraints  $(\alpha_{\text{ac}-i}^{+} \text{ and } \alpha_{\text{dc}-j}^{+})$ . (c)&(f) Secondary variables (frequency and dc-voltage).

is not active (see Fig. 8.6f), the ICs only share the power for achieving  $\lambda^{\rm G}$  consensus. In this scenario, all the ICs are within limits and the Lagrange multiplier associated to this constraint is zero (see Fig. 8.6b). It is important to highlight that the Lagrange multiplier  $\lambda_{\rm IC}$  can be positive or negative (depending on the direction of the power through the ICs) since it shifts the incremental cost function ( $\lambda^G$ ) from the DGs (see (4.27e) and (4.29e)). The total operating cost of the microgrid is reduced when activating the full optimization  $[C_T = 819.55 \ (\$/h), \text{ see Fig. 8.6d}].$ 

The  $\lambda^{\rm IC}$  consensus is activated at t=150s, and the parameters for the controllers are as shown in (8.11). It can be seen that the ICs begin to work collaboratively and they reach the same value of  $\lambda^{\rm IC}$ , as depicted in Fig. 8.6f. Moreover, a re-dispatch of the power through the ICs is produced (see Fig. 8.6a) and, therefore, the losses in the ICs change as well (see Fig. 8.6c). The re-dispatch of the power through the ICs very subtly affects the operation of the DGs (see Fig. 8.5). On the other hand, since all the optimization is now active, the total operating cost of the microgrid decreases again  $[C_T = 819.50 \ (\$/h)$ , see Fig. 8.6d].

• In (4.27b): 
$$k_{\text{ac-i}}^{a}, k_{\text{ac-i}}^{b}, k_{\text{ac-i}}^{c} > 0$$
,  $\forall i \in \mathbb{N}_{\text{ac}}$   
• In (4.29b):  $k_{\text{dc-j}}^{a}, k_{\text{dc-j}}^{b}, k_{\text{dc-j}}^{c} > 0$ ,  $\forall j \in \mathbb{N}_{\text{dc}}$   
• In (4.30a):  $\tau_{\text{IC-k}}^{a}, \tau_{\text{IC-k}}^{b} > 0$ ,  $\forall k \in \mathbb{N}_{\text{IC}}$ 

Finally, the limit constraints for the power transfer through the ICs are activated at t = 250s. The power through the IC with the lowest losses is decreased and that of the other two is increased (see Fig. 8.6a). The Lagrange multiplier associated to this constraint can be

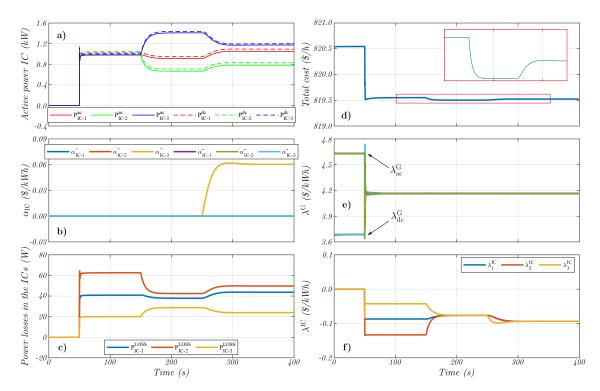


Figure 8.6: Simulation Test #9: (a) Active power through the ICs. (b) Lagrange multiplier for transfer power constraints  $(\alpha_{IC-k}^{ac+})$  and  $\alpha_{IC-k}^{ac-}$ . (c) Power losses in the ICs. (d) Total operating cost. (e) Lagrange multiplier  $\lambda^{G}$ . (f) Lagrange multiplier  $\lambda^{IC}$ .

seen in Fig. 8.6b. Again, the inclusion of the power transfer limit constraints slightly increase the total operating cost of the microgrid [ $C_T = 819.52$  (\$/h), see Fig. 8.6d] and marginally affects the operation of the DGs (see Fig. 8.5).

In summary, Table 8.10 shows the total operating cost for the hybrid ac/dc-microgrid under all the studied scenarios. It can be seen that, as expected, the minimum operating cost is achieved when the full optimization is enabled and the  $\lambda^{\rm IC}$  consensus is being considered. On the other hand, the total operating cost increases as new constraints are being incorporated to the controller.

Table 8.10: Summary for the total operating cost under the studied scenarios.

Operating condition	Time	Cost (\$/h)
Opt. within microgrids	0s - 50s	820.55
Full optimization	50s - 150s	819.55
$\lambda^{ ext{IC}}$ consensus	150s - 250s	819.50
ICs limits constraint	250s - 400s	819.52

## 8.5 Simulation Test #10: Multiple ICs With Load Steps

Now, the performance of the proposed control strategy against load steps is analyzed. The microgrid used in the previous Section is simulated, maintaining the parameters and commu-

nication network (adjacency matrix) described before. The parameters for the cost functions of the DGs [see (4.4)] are also the same as before (see Table 8.7). The operation limits for the DGs are also maintained [see (8.7) and (8.8)]. The losses in the *ICs* are given by (8.8). The initial local-load of the 12 DGs is the same as in the previous test (see Table 8.8). The results for this test are shown in Fig. 8.7 and Fig. 8.8.

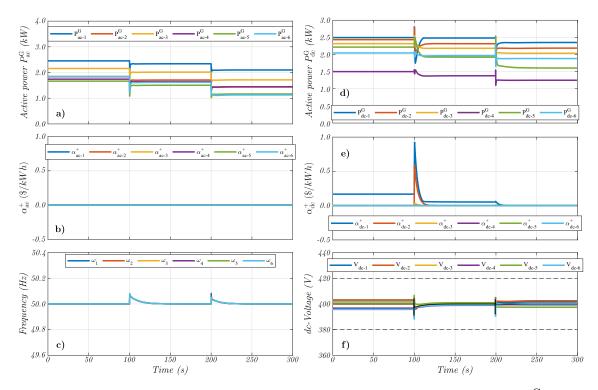


Figure 8.7: Simulation Test #10: (a)&(d) Active power generated by ac-DGs ( $P_{\text{ac}-i}^{\text{G}}$ ) and dc-DGs ( $P_{\text{dc}-j}^{\text{G}}$ ). (b)&(e) Lagrange multiplier for generated power constraints ( $\alpha_{\text{ac}-i}^+$  and  $\alpha_{\text{dc}-j}^+$ ). (c)&(f) Secondary variables (frequency and dc-voltage).

The simulation begins with all the controllers and the power limit constraints enabled, i.e., the full optimization of the microgrid is active and the parameters for the controllers are as depicted in (8.11). For 0s < t < 100s, the DGs are achieving a  $\lambda^{\rm G}$  consensus (see Fig. 8.8e) and the ICs are achieving  $\lambda^{\rm IC}$  consensus (see Fig. 8.8f).

The total operating cost of the microgrid (see Fig. 8.8d) is minimum in this scenario  $[C_T = 819.52 \, (\$/h)]$ , and the power through the  $IC_3$  is touching the upper limit (see Fig. 8.8a for  $P_{\text{IC}-3}^{\text{dc}}$  and Fig. 8.8b for  $\alpha_{\text{IC}-3}^+$ ). On the other hand, the dc-DG<sub>1</sub> is generating its maximum power and the other DGs are operating within limits (see Fig. 8.7a for  $P_{\text{ac}-i}^{\text{G}}$ , Fig. 8.7b for  $\alpha_{\text{ac}-i}^+$ , and Fig. 8.7e for  $\alpha_{\text{dc}-j}^+$ ), while the secondary variables are regulated (see Fig. 8.7c for frequency and Fig. 8.7f for dc-voltage).

At t=100s, a load step is applied at both sides of the hybrid ac/dc-microgrid. The load at the ac-side is step-decreased to 13.7kW (76.1% of the nominal power at this side), while the load power at the dc-side is step-increased to 12.1kW (80.7% of the nominal power at this side). Due to the change in the power load, the power through the ICs decreases until it is negligible ( $P_{IC-k}^{ac} \approx 0$ ), as shown in Fig. 8.8a. Moreover, the Lagrange multiplier associated to the power transfer limit ( $\alpha_{IC-3}^+$ ) slowly decreases until it becomes zero (see

Fig. 8.8b). The only element that remains touching its upper operation limit is the dc-DG<sub>1</sub>, which corresponds to the cheapest generator in the microgrid.

Despite the transient response, the secondary variables are maintained within limits all the time (see Fig. 8.7c for frequency and Fig. 8.7f for dc-voltage), and the  $\lambda$  consensuses are achieved by the DGs (see Fig. 8.8e for  $\lambda^{\rm G}$ ) and by the ICs (see Fig. 8.8f for  $\lambda^{\rm IC}$ ). The total operating cost for the new load condition is  $C_T = 812.85$  (\$\frac{1}{3}h\$), as shown in Fig. 8.8d.

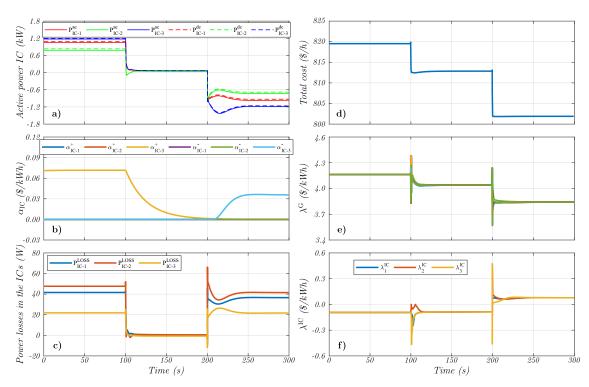


Figure 8.8: Simulation Test #10: (a) Active power through the ICs. (b) Lagrange multiplier for transfer power constraints ( $\alpha_{{\rm IC}-k}^{{\rm ac}+}$  and  $\alpha_{{\rm IC}-k}^{{\rm ac}-}$ ). (c) Power losses in the ICs. (d) Total operating cost. (e) Lagrange multiplier  $\lambda^{\rm G}$ . (f) Lagrange multiplier  $\lambda^{\rm IC}$ .

At t=200s, a new load step is applied at both sides of the hybrid ac/dc-microgrid; the load on the ac-side is step-decreased to 11.7kW (65.0% of the nominal power at this side), while the load power on the dc-side is step-increased to 13.1kW (87.3% of the nominal power at this side). Due to the change in the load, the power through the ICs now flows from the ac-microgrid to the dc-microgrid, as depicted in Fig. 8.8a. One more time, the IC with the lowest losses ( $IC_3$ ) touches its operation limits and the Lagrange multiplier associated to this constraint is increased (see Fig. 8.8b for  $\alpha_{IC-3}^-$ ).

All DGs now are working within limits (see Fig. 8.7a for  $P_{\text{ac-i}}^{\text{G}}$ , and Fig. 8.7d for  $P_{\text{dc-j}}^{\text{G}}$ ), which is reflected in the Lagrange multipliers associated to these constraints (see Fig. 8.7b for  $\alpha_{\text{ac-i}}^+$ , and Fig. 8.7e for  $\alpha_{\text{dc-j}}^+$ ). On the other hand, all the secondary variables remain regulated within limits (see Fig. 8.7c for frequency and Fig. 8.7f for dc-voltage). One more time, the  $\lambda$  consensuses are achieved by the DGs (see Fig. 8.8e for  $\lambda^{\text{G}}$ ) and by the ICs (see Fig. 8.8f for  $\lambda^{\text{IC}}$ ) all the time, and the total operating cost for the new load condition is  $C_T = 801.89$  (\$/h), as shown in Fig. 8.8d.

### 8.6 Simulation Test #11: Comparison

Finally, a comparison of the two control strategies proposed in this thesis is presented, i.e., the scheme for power-sharing presented in Chapter 3 (henceforth called DSC-I) and the algorithm for operation cost minimization depicted in Chapter 4 (called DSC-II from now on). The control strategies are applied to the two microgrids utilized in this thesis, i.e., to the one with a single IC (see Fig. 5.8) and to the other with multiple ICs (see Fig. 7.13). The parameters for the controllers are those used in Chapters 7 and 8.

The power loads remain constant during this test, and are summarized in Table 8.11 for the two topologies simulated in these tests. Furthermore, the loading condition of the hybrid ac/dc-microgrid was designed to avoid the DGs' and ICs' overload, in order to make a clean comparison without the limit constraints which penalizes the cost in the second strategy (DSC-II). Since both the reactive power consensus and the ac-voltage regulation are achieved using the same control strategy in both cases (see Section 3.3), only active power loads are considered in the ac-side. The results for these tests are shown in Fig. 8.9 and Fig. 8.10. In the figures, the results for the microgrid with a single IC are depicted in the column on the left side while the results for the microgrid with multiple ICs are presented in the column on the right side.

Table 8.11: Hybrid ac/dc-microgrid, load conditions for test #11.

#### Topology with a single IC

#### Load kWLoad kW $\overline{Z_1}$ $R_1$ 1.02 1.90 $R_2$ 1.55 2.57 $R_3$ 2.641.37 $R_4$ 1.03

 $R_5$ 

 $R_6$ 

1.03

1.03

#### Topology with multiple ICs

Load	kW	Load	kW
$R_1$	1.9	$\overline{Z_1}$	2.1
$R_2$	1.3	$Z_2$	1.7
$R_3$	1.1	$Z_3$	2.3
$R_4$	1.4	$Z_4$	2.1
$R_5$	1.5	$Z_5$	2.1
$R_6$	1.7	$Z_6$	1.7

In the single IC case, the total load of the hybrid ac/dc-microgrid is 14.14kW (7.03kW on dc-side and 7.11kW on ac-side), i.e., 58.9% of the nominal active power of this microgrid (24.0kW). The load power on the dc-side  $(R_1, \ldots, R_6)$  is 46.9% of the nominal power of this side (15.0kW); meanwhile, the active load power on the ac-side  $(Z_1, Z_2, Z_3)$  corresponds to 79.0% of 9.0kW. On the other hand, in the multiple ICs case, the total load of the hybrid ac/dc-microgrid is 20.9kW (8.9kW on dc-side and 12.0kW on ac-side), i.e., 63.3% of the nominal active power of this microgrid (33.0kW). The load power on the dc-side  $(R_1, \ldots, R_6)$  is 59.3% of the nominal power of this side (15.0kW); meanwhile, the active load power on the ac-side  $(Z_1, \ldots, Z_6)$  corresponds to 66.7% of 18.0kW.

The simulations begin with all the controllers enabled, i.e., the inner, primary and secondary control loops are active. Specifically, in both cases, for t < 100s the DSC-I strategy is working and the DGs are achieving an accurate power consensus, as depicted in Fig. 8.9a for the single IC case and Fig. 8.9d for the multiple ICs case. The power generated by the ac-DGs  $(P_{\rm ac-i}^{\rm G}, \forall i \in \mathbb{N}_{\rm ac})$  is shown in red while the power generated by the dc-DGs  $(P_{\rm dc-i}^{\rm G}, \forall j \in \mathbb{N}_{\rm dc})$  is shown in blue.

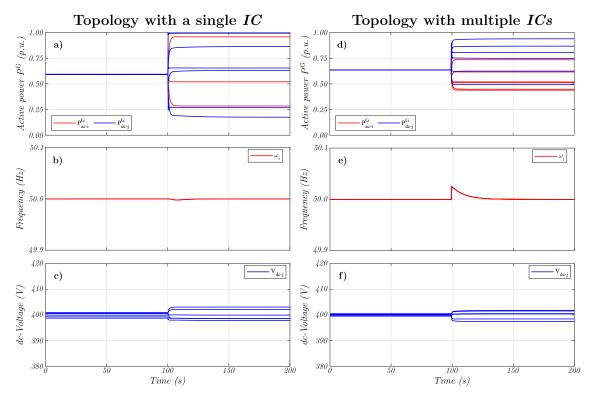


Figure 8.9: Simulation Test #11: (a)&(d) Power generated by ac-DGs ( $P_{\text{ac}-i}^{\text{G}}$ ,  $i \in \mathbb{N}_{\text{ac}}$ ) and dc-DGs ( $P_{\text{dc}-j}^{\text{G}}$ ,  $j \in \mathbb{N}_{\text{dc}}$ ). (b)&(e) Frequency of the voltages in the ac-microgrid ( $\omega_i$ ,  $i \in \mathbb{N}_{\text{ac}}$ ). (c)&(f) Voltage magnitudes in the dc-microgrid ( $V_{\text{dc}-j}$ ,  $j \in \mathbb{N}_{\text{dc}}$ ).

As described before, in both cases, the power load (in p.u.) in the ac-side is higher than that in the dc-side. Therefore, the ICs transfer power from the dc-side to the ac-side ( $P_{IC-k}^{ac} > 0$ ,  $\forall k \in \mathbb{N}_{IC}$ ), as shown in Fig. 8.10a and Fig. 8.10d. Moreover, since the power consensus (DSC-I) strategy is operating, the ICs are sharing the power in the case with multiple ICs (see Fig. 8.10d). The Lagrange multipliers of the DGs ( $\lambda^{G}$ ) are not being shared in any case (see Fig. 8.10b and Fig. 8.10e). On the other hand, the Lagrange multipliers of the ICs ( $\lambda^{IC}$ ) are not shared in the case with multiple ICs (see Fig. 8.10e).

The DSC-II strategy is activated at t=100s. It can be seen that both the DGs and the ICs immediately start to share the Lagrange multipliers (see Fig. 8.10d and Fig. 8.10e). Therefore, in order to minimize the total operating cost of the microgrid, the power generated by the DGs is re-dispatched (see Fig. 8.10a and Fig. 8.10d), increasing the amount of power being transferred through the ICs. Moreover, the power in the ICs is also re-dispatched to reduce the total operating cost, as shown in Fig. 8.10d.

At this point, it is important to remember that the main objective of the secondary control loop is to restore the variables modified by the primary control loop to their nominal values, i.e., the frequency of the voltages in the ac-microgrid and the magnitude of the voltages in the dc-microgrid. The two control strategies proposed in this work fulfill this objective, and successfully restore the secondary variables to their nominal values, as depicted in Fig. 8.9b and Fig. 8.9e for the frequency ( $\omega_i$ ,  $\forall i \in \mathbb{N}_{ac}$ ), and in Fig. 8.9c and Fig. 8.9f for the dc-voltages ( $V_{dc-j}$ ,  $\forall j \in \mathbb{N}_{dc}$ ).

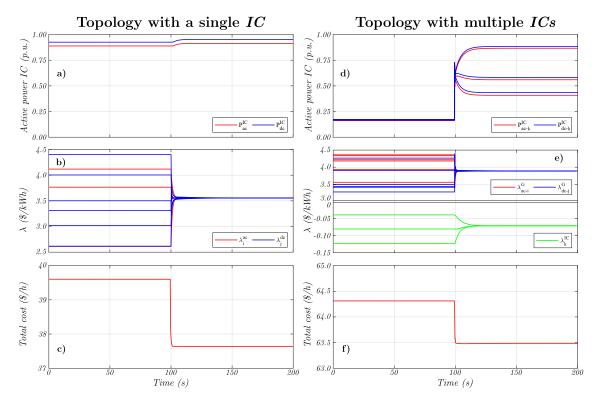


Figure 8.10: Simulation Test #11: (a)&(d) Active power through the ICs. (b)&(e) Lagrange multipliers ( $\lambda$ ). (c)&(f) Total operating cost.

From Fig. 8.10c and Fig. 8.10f it is clear that the DSC-II strategy minimizes the total operation cost of the hybrid ac/dc-microgrid. In this case, since the fixed cost in the quadratic cost function [parameters  $c_{ac-i}$  and  $c_{dc-j}$  in (4.4)] are the same when applying the two control strategies, the total cost (\$/h) shown in the Figs. corresponds to the operation cost which depends on the amount of power generated by the DGs (i.e.,  $c_{ac-i} = c_{dc-j} = 0$  is considered in the calculation). Thus, it is possible to analyze the real effect of re-dispatching the power supplied by the DGs and transferred by the ICs on the operation cost.

A summary for the total operation cost of the hybrid ac/dc-microgrid under the cases analyzed in this test is presented in Table 8.12. The total variable cost of the single IC topology is reduced by 4.97% when the DSC-II strategy is applied, i.e., it is reduced from 39.601(\$/h) to 37.364(\$/h). On the other hand, the application of the DSC-II strategy reduces the total variable cost of the multiple ICs topology from 64.312(\$/h) to 63.486(\$/h), i.e., it is reduced by 1.28%. Although the savings are not that significant in monetary terms (dollars), they can be important in percentage terms even though the microgrids are small.

Finally, these tests allow to identify two possibilities to minimize the total operating cost of the microgrids, which can be applied independently or together. First, it is possible to re-dispatch the power generated by the DGs within each microgrid, with a small variation of the power transferred through the IC, as in the single IC topology. Secondly, as shown in the multiple ICs topology, it is possible to increase the amount of power transferred from the cheapest microgrid (dc-microgrid in this case) to the most expensive one (ac-microgrid in this case), while re-dispatching the power generated by the DGs.

Table 8.12: Total operation cost of the hybrid ac/dc-microgrid for test #11.

Microgrid	Power-sharing	Cost minimization	Savings
Topology	(DSC-I)	$(DSC ext{-}II)$	%
Single IC	39.601 (\$/h)	37.364 (\$/h)	4.97
Multiple $ICs$	64.312 (\$/h)	63.486 (\$/h)	1.28

#### 8.7 Summary

In this Chapter, simulation results were presented to validate the distributed consensus-based secondary control strategy for operating cost minimization (with a single or multiple ICs) proposed in this thesis. The validation through simulation was performed in two prototypes of a hybrid ac/dc-microgrid; the first one corresponds to a 24kW downscale prototype with a single IC, and the second one in a 33kW downscale prototype of a hybrid ac/dc-microgrid with multiple ICs.

Summarizing, the simulation results demonstrated the advantages of the proposed method, which are:

- It is possible to minimize the total operating cost in a microgrid with one or multiple ICs, by solving an optimization problem online and in a distributed way.
- The DGs in a hybrid ac/dc-microgrid can be dispatched according to a global objective, which is to minimize the total operating cost.
- For the DGs or the *ICs*, it is not necessary to have the full information of the microgrid to solve the optimization problem.
- It is possible to limit either the power generated by the DGs or the power transfer through the *ICs* by implementing constraints to the optimization problem.
- The secondary control variables, i.e., average dc-voltages and frequency, are restored to their nominal values in steady-state under all the analyzed cases.
- The *IC* participates actively in the proposed secondary control strategy, helping to minimize the total operating cost of the microgrid by regulating the power transfer from the *dc*-side to the *ac*-side (or vice versa) according to the incremental cost of the sub-microgrids.

## Chapter 9

## Conclusions and Future Work

#### 9.1 Conclusions

Continuous improvement in distributed generation technologies and energy storage systems (specifically, more efficient and economical devices) is driving the electrical systems (at the distribution level) towards microgrids. On the other hand, in order to avoid unnecessary ac-to-dc and dc-to-ac conversions, much effort has been carried out to investigate hybrid ac/dc-microgrids. Accordingly, the main goal of this thesis was to propose consensus-based distributed secondary control strategies for hybrid ac/dc-microgrids, which treat the microgrid as a single entity and not as three independent ones interacting with each other (i.e., ac, dc, and IC).

Owing to the results obtained in this thesis, it can be stated that the proposed control schemes simultaneously regulate ac-voltage magnitude and frequency, as well as the dc-voltage magnitude via a distributed consensus approach, which can be augmented with an additional control objective such as active power consensus between ac-DGs and dc-DGs (and ICs) or operation cost minimization of the complete microgrid. In addition, the major findings of this Ph.D. project are highlighted below:

#### Secondary variables restoration

- As the research work demonstrated, the secondary variables modified by the primary control loop (droop control) in a hybrid ac/dc-microgrid can be simultaneously restored to their nominal values with a global distributed secondary control strategy.
- Due to the characteristics of each variable, it is possible to restore the frequency of all the ac-DGs in the ac-microgrid to the nominal value. On the other hand, it is possible to restore the average value of the voltages at each side of the hybrid ac/dc-microgrid to their nominal values, while the voltages in each ac-DG and dc-DG are spread within an acceptable range. The experimental and simulation tests carried out in this research effort exhibited a fast and smooth restoration of the variables mentioned above.

#### Active power consensus

- In this thesis, a new control algorithm to achieve active power consensus among the ac-DGs and dc-DGs in a hybrid ac/dc-microgrid has been proposed. Moreover, the control algorithm also achieves active power consensus among the ICs, avoiding circulating currents in the dc-side. The control task is performed jointly with the secondary variables restoration.
- The novel control scheme is based on the consensus strategy from the multiagent theory, and therefore the DGs need to be communicated with, at least, one neighbouring unit. Several tests were carried out in a hybrid ac/dc-microgrid prototype (24kW), such as load-steps, connection/disconnection of DGs, and communication issues. Both experimental and simulation results demonstrated the effectiveness of the proposed control scheme.

#### Real-time operation cost minimization

- In this thesis, a new control algorithm to achieve an online minimization of the operation cost in a hybrid ac/dc-microgrid, by re-dispatching the ac-DGs and dc-DGs, has been proposed. Moreover, the control algorithm also re-dispatched the power transferred by the ICs, reducing the power losses in the ac-to dc (and vice versa) conversion. The control task is performed jointly with the secondary variables restoration.
- The novel control scheme is based on the consensus strategy from the multiagent theory, and therefore the DGs need to be communicated with, at least, one neighbouring unit. Several tests were carried out in a hybrid ac/dc-microgrid prototype. Simulation results demonstrated the effectiveness of the proposed control scheme.

#### Reduced communication layer

• The proposed control schemes consider a reduced communication layer, as each DG is communicating only with its neighbouring DGs. Due to the fact that *ICs* also participate in the communication layer, the secondary control strategies can be adapted to each side separately in case the *ICs* are not available, i.e, if there is not possible to transfer power from one side to the other side. Moreover, with the control scheme for operation cost minimization, this feature allows to maintain the *ICs* operating within limits by including them as constraints in the optimization problem, without affecting neither the secondary variables restoration nor the operation cost minimization.

#### Analytical closed-loop model

• An analytical model of the closed-loop system of a hybrid ac/dc-microgrid with the proposed consensus-based secondary control strategies was derived for analyzing small-signal stability and tuning of the parameters of proposed controllers. The models were derived with the hybrid ac/dc-microgrid as a single entity, considering the interaction of the ac-DGs and dc-DGs via the power transfer through the IC.

#### 9.2 Future Work

The following are some interesting topics in which further research can be undertaken to extend the scope of this project:

- Additional objective functions could be included in the optimization problem. For example, an energy management system could be formulated in the hybrid ac/dcmicrogrid: energy storage systems with renewable energies could be added to the objective function.
- Further stability analyses could be performed to the hybrid ac/dc-microgrid with the proposed controllers (e.g., large-signal stability).
- Different methodologies for improving the tuning of the parameters could be proposed.
- The theoretical stability limits for the parameters of the controllers found with the small-signal model could be experimentally tested.

### 9.3 Publications

The details of the publications from the work developed during this project and other contributions to papers related to microgrids are listed in the following.

# 9.3.1 Papers related to this Ph.D. Project

#### Journal Papers

- E. Espina, R. Cárdenas-Dobson, J. W. Simpson-Porco, D. Sáez and M. Kazerani, "A Consensus-Based Secondary Control Strategy for Hybrid ac/dc Microgrids with Experimental Validation," in IEEE Transactions on Power Electronics, doi: 10.1109/TPEL.2020.3031539.
- 2. **E. Espina**, J. Llanos, C. Burgos-Mellado, R. Cárdenas-Dobson, M. Martínez-Gómez and D. Sáez, "Distributed Control Strategies for Microgrids: An Overview," in IEEE Access, vol. 8, pp. 193412-193448, 2020, doi: 10.1109/ACCESS.2020.3032378.
- 3. **E. Espina**, R. Cárdenas-Dobson, M. Espinoza-B., C. Burgos-Mellado and D. Sáez, "Cooperative Regulation of Imbalances in Three-Phase Four-Wire Microgrids Using Single-Phase Droop Control and Secondary Control Algorithms," in IEEE Transactions on Power Electronics, vol. 35, no. 2, pp. 1978-1992, Feb. 2020, doi: 10.1109/TPEL.2019.2917653.
- 4. C. Burgos-Mellado, J. Llanos, **E. Espina**, D. Sáez, R. Cárdenas, M. Sumner, A. Watson, "Single-Phase Consensus-Based Control for Regulating Voltage and Sharing Unbalanced Currents in 3-Wire Isolated AC Microgrids," in IEEE Access, vol. 8, pp. 164882-164898, 2020, doi: 10.1109/ACCESS.2020.3022488.

#### **Conference Papers**

- 5. E. Espina, R. Cárdenas-Dobson, D. Saez, J. W. Simpson-Porco, M. Kazerani, J. Gómez, A. Navas, "Experimental Performance Evaluation of a Distributed Secondary Control Strategy for Hybrid ac/dc-Microgrids in the event of Communication Loss/Delay," 2021 23rd European Conference on Power Electronics and Applications (EPE'21 ECCE Europe), Ghent, Belgium, 2021
- 6. E. Espina, C. Burgos-Mellado, J. Gómez, J. Llanos, E. Rute, A. Navas, M. Martínez-Gómez, R. Cárdenas, D. Sáez, "Experimental Hybrid AC/DC-Microgrid Prototype for Laboratory Research," 2020 22nd European Conference on Power Electronics and Applications (EPE'20 ECCE Europe), Lyon, France, 2020, pp. 1-9, doi: 10.23919/EPE20ECCEEurope43536.2020.9215751.
- 7. **E. Espina**, R. Cárdenas, F. Donoso, M. Urrutia and M. Espinoza, "A Novel Distributed Secondary Control Strategy Applied to Hybrid AC/DC Microgrids," 2019 21st European Conference on Power Electronics and Applications (EPE '19 ECCE Europe), Genova, Italy, 2019, pp. P.1-P.9, doi: 10.23919/EPE.2019.8915135.

## 9.3.2 Participation in other publications

#### Journal Papers

8. M. Espinoza, R. Cárdenas, J. Clare, D. Soto-Sánchez, M. Díaz, **E. Espina**, "An Integrated Converter and Machine Control System for MMC-Based High-Power Drives," in IEEE Transactions on Industrial Electronics, vol. 66, no. 3, pp. 2343-2354, March 2019, doi: 10.1109/TIE.2018.2801839.

#### Conference Papers

- 9. J. S. Gómez, J. Llanos, C. Burgos, **E. Espina**, and J. Rodriguez, "CooperativePower Conditioners for Microgrids in Mining," 2021 23rd European Conference on Power Electronics and Applications (EPE'21 ECCE Europe), Ghent, Belgium, 2021
- A. Navas-Fonseca, C. Burgos-Mellado, J. Gómez, J. Llanos, E. Espina, D. Sáez, M. Sumner, "Distributed Predictive Control using Frequency and Voltage Soft Constraints in AC Microgrids including Economic Dispatch of Generation," 2021 47th Annual Conference of the IEEE Industrial Electronics Society (IECON), Toronto, Canada, 2021
- 11. A. Navas-Fonseca, C. Burgos-Mellado, **E. Espina**, E. Rute, J. Gómez, D. Sáez, M. Sumner, "Distributed Predictive Secondary Control for Voltage Restoration and Economic Dispatch of Generation for DC Microgrids," the 4th IEEE International Conference on DC Microgrids (ICDCM), USA, 2021
- 12. **E. Espina**, M. Espinoza and R. Cárdenas, "Active power angle droop control per phase for unbalanced 4-wire microgrids," 2017 IEEE Southern Power Electronics Conference (SPEC), Puerto Varas, 2017, pp. 1-6, doi: 10.1109/SPEC.2017.8333637.

- M. Malhue, M. Díaz, F. Rojas, E. Espina and R. Cárdenas, "A Parallel Fast Delayed Signal Cancellation PLL for Unbalanced and Distorted Grid Applications," 2019 IEEE CHILEAN Conference on Electrical, Electronics Engineering, Information and Communication Technologies (CHILECON), Valparaiso, Chile, 2019, pp. 1-6, doi: 10.1109/CHILECON47746.2019.8988085.
- 14. F.Donoso, A.Mora, M.Espinoza, M.Urrutia, E. Espina and R.Cárdenas, "Predictive-based Modulation Schemes for the Hybrid Modular Multilevel Converter," 2019 21st European Conference on Power Electronics and Applications (EPE '19 ECCE Europe), Genova, Italy, 2019, pp. P.1-P.9, doi: 10.23919/EPE.2019.8914876.
- 15. M. Espinoza, R. Cárdenas, M. Díaz, F. Donoso, E. Espina, A. Letelier, A. Mora, "Effects of a Variable dc-Port Voltage on the Half-Bridge-Based Modular Multilevel Converter for Drive Systems," 2019 21st European Conference on Power Electronics and Applications (EPE '19 ECCE Europe), Genova, Italy, 2019, pp. P.1-P.10, doi: 10.23919/EPE.2019.8915500.
- 16. M. Urrutia, F. Donoso, A. Mora, E. Espina, M. Díaz and R. Cárdenas, "Enhanced Circulating-current Control for the Modular Multilevel Matrix Converter Based on Model Predictive Control," 2019 21st European Conference on Power Electronics and Applications (EPE '19 ECCE Europe), Genova, Italy, 2019, pp. 1-9.
- 17. M. Espinoza, F. Donoso, **E. Espina**, M. Díaz and R. Cárdenas, "A Novel Control Strategy for Modular Multilevel-Based Drives Considering the System Operating Point," 2018 20th European Conference on Power Electronics and Applications (EPE'18 ECCE Europe), Riga, 2018, pp. P.1-P.10.
- 18. M. Espinoza, M. Díaz, **E. Espina**, C. M. Hackl and R. Cárdenas, "Control strategies for modular multilevel converters driving cage machines," 2017 IEEE Southern Power Electronics Conference (SPEC), Puerto Varas, 2017, pp. 1-6, doi: 10.1109/SPEC.2017.8333628.

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# **APPENDICES**

# Appendix A

# Closed-loop model for a hybrid ac/dc-microgrid with the Power-sharing Strategy

In this Appendix, the procedure for obtaining the closed-loop model for the microgrid presented in Chapter 5 is presented. This model is also utilized for performing a small-signal analysis of the consensus-based distributed control strategy proposed in Chapter 3 for power-sharing.

Due to the primary purpose for obtaining the closed-loop model of the consensus-based distributed secondary control strategy proposed for power-sharing in a hybrid ac/dc-microgrid, the following simplifications were considered since their dynamics are much faster than that of the studied controllers:

- The inner control loop (voltage/current controllers) is not considered.
- The modulation of the converters is not considered.
- The switching of the switching devices is not considered.

Appendix A is organized as follows: Section A.1 presents the closed-loop model for the ac-microgrid, while the closed-loop model for the dc-microgrid is depicted in Section A.2. The closed-loop model for the IC is presented in Section A.3.

# A.1 Closed-loop model for the ac-microgrid

In this Section, the closed-loop model for the ac-microgrid is obtained, including the topology and the controllers' characteristics.

#### A.1.1 Power flow in the ac-microgrid

The topology of the *ac*-microgrid is re-presented in Fig. A.1. The set of *ac*-DGs are labelled as  $\mathbb{N}_{ac} = \{1, \ldots, n\}$ . Further details of the topology can be found in Section 5.2.

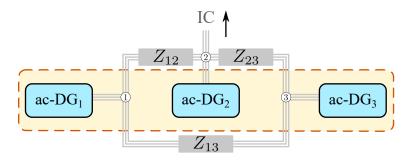


Figure A.1: Topology of the ac-microgrid.

The power flows between buses i and j in the ac-microgrid  $\forall i, j \in \mathbb{N}_{ac}$ , are given by:

$$p_{\text{ac-i}j} = \frac{1}{R_{\text{i}j}^2 + X_{\text{i}j}^2} \left[ R_{\text{i}j} E_{\text{i}}^2 - R_{\text{i}j} E_{\text{i}} E_j \cos(\theta_{\text{i}} - \theta_j) + X_{\text{i}j} E_{\text{i}} E_j \sin(\theta_{\text{i}} - \theta_j) \right]$$
(A.1a)

$$q_{\text{ac-i}j} = \frac{1}{R_{ij}^2 + X_{ij}^2} \left[ X_{ij} E_i^2 - X_{ij} E_i E_j \cos(\theta_i - \theta_j) - R_{ij} E_i E_j \sin(\theta_i - \theta_j) \right]$$
(A.1b)

where  $p_{\text{ac-i}j}$  ( $q_{\text{ac-i}j}$ ) is the active (reactive) power flow from bus i to bus j,  $R_{ij}$  and  $X_{ij}$  are characteristics for the line between the two buses,  $E_i$  is the magnitude of the voltage at bus i, and  $\theta_i$  is its angle.

On the other hand, the power injected to the bus i is given by:

$$p_{\text{ac-i}} = p_{\text{Dac-i}} + \sum_{j \in \mathbb{N}_{\text{ac}}} p_{\text{ac-i}j}$$
(A.2a)

$$q_{\mathrm{ac-i}} = q_{\mathrm{Dac-i}} + \sum_{j \in \mathbb{N}_{\mathrm{ac}}} q_{\mathrm{ac-i}j} \tag{A.2b}$$

where  $p_{\text{ac-i}}(q_{\text{ac-i}})$  is the active (reactive) power injected by  $ac\text{-DG}_i$  and  $p_{\text{Dac-i}}(q_{\text{Dac-i}})$  is the local active (reactive) power demanded at bus i.

Linearizing (A.2), in vector notation we have:

$$\Delta p_{\rm ac} = G_1^{\rm ac} \Delta E + G_2^{\rm ac} \Delta \theta + G_0^{\rm ac} \Delta P_{\rm IC}$$
 (A.3a)

$$\Delta q_{\rm ac} = G_3^{\rm ac} \Delta E + G_4^{\rm ac} \Delta \theta \tag{A.3b}$$

where  $p_{ac}, q_{ac} \in \mathbb{R}^n$  are the vectors of active and reactive power generation (respectively),  $E \in \mathbb{R}^n$  is the vector of bus potentials,  $\theta \in \mathbb{R}^n$  is the vector of bus angles,  $P_{IC}$  is the power flow through the IC, which is positive when it flows from the dc-microgrid to the ac-microgrid. Matrices  $G_1^{ac}, G_2^{ac}, G_3^{ac}, G_4^{ac} \in \mathbb{R}^{n \times n}$  are composed of partial derivatives of  $p_{ac}$  and  $q_{ac}$  with respect to E and  $\theta$ , and depend on both the operating point  $(E^0, \theta^0)$  and the admittance matrix of the electrical system (Y). Finally,  $G_0^{ac} = (0 -1 0)^T$  is the vector describing the electrical connection of the ac-microgrid and the IC.

#### A.1.2 Voltage reference

From (3.5a), the voltage reference for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$E_{i} = E^* + N_{ac-i} \cdot Q_{ac-i} + \chi_{i} \tag{A.4}$$

where  $Q_{ac-i}$  is the filtered reactive power generated by the i<sup>th</sup> ac-DG, and it is given by:

$$Q_{\text{ac-i}} = \frac{\omega_{c-i}}{s + \omega_{c-i}} q_{\text{ac-i}}$$

$$\dot{Q}_{\text{ac-i}} = \omega_{c-i} \left( q_{\text{ac-i}} - Q_{\text{ac-i}} \right) \tag{A.5}$$

where  $\omega_{c-i}$  is the cutoff frequency for the low pass filter.

Now, linearizing (A.5), the following expression for  $\Delta Q_{\rm ac}$  is obtained in vector notation:

$$\Delta \dot{Q}_{\rm ac} = [\omega_c] \Delta q_{\rm ac} - [\omega_c] \Delta Q_{\rm ac} \tag{A.6}$$

where  $\omega_c \in \mathbb{R}^n$  is the vector of cutoff frequencies, and  $[\omega_c] \in \mathbb{R}^{n \times n}$  denotes the diagonal matrix with  $\omega_c$  on the diagonal. This notation is used in the rest of the Chapter for referring to diagonal matrices.

Replacing  $\Delta q_{\rm ac}$  from (A.3b), we have:

$$\Delta \dot{Q}_{ac} = [\omega_c] (G_3^{ac} \Delta E + G_4^{ac} \Delta \theta) - [\omega_c] \Delta Q_{ac}$$

$$\Delta \dot{Q}_{ac} = [\omega_c] G_3^{ac} \Delta E + [\omega_c] G_4^{ac} \Delta \theta - [\omega_c] \Delta Q_{ac}$$
(A.7)

Now, linearizing the expression for the voltage shown in (A.4), we obtain:

$$\Delta E = [N_{\rm ac}] \Delta Q_{\rm ac} + \Delta \chi \tag{A.8}$$

Replacing  $\Delta E$  from (A.8) in (A.7), we have:

$$\Delta \dot{Q}_{ac} = [\omega_c] G_3^{ac} ([N_{ac}] \Delta Q_{ac} + \Delta \chi) + [\omega_c] G_4^{ac} \Delta \theta - [\omega_c] \Delta Q_{ac} 
\Delta \dot{Q}_{ac} = [\omega_c] (G_3^{ac} [N_{ac}] - I_n) \Delta Q_{ac} + [\omega_c] G_3^{ac} \Delta \chi + [\omega_c] G_4^{ac} \Delta \theta 
\Delta \dot{Q}_{ac} = G_5^{ac} \Delta Q_{ac} + G_6^{ac} \Delta \chi + G_7^{ac} \Delta \theta$$
(A.9)

Equation (A.9) corresponds to the state equation for the state variables  $\Delta Q_{\rm ac}$ .

## A.1.3 Frequency reference

From (3.4a), the frequency reference for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$\dot{\theta}_{i} = \omega_{i} = \omega^{*} + M_{ac-i}P_{ac-i} + \psi_{i} \tag{A.10}$$

where  $\Delta P_{\rm ac}$  is the filtered active power generated by the i<sup>th</sup> ac-DG, and it is given by:

$$P_{\text{ac-i}} = \frac{\omega_{c-i}}{s + \omega_{c-i}} p_{\text{ac-i}}$$

$$\dot{P}_{\text{ac-i}} = \omega_{c-i} \left( p_{\text{ac-i}} - P_{\text{ac-i}} \right) \tag{A.11}$$

Linearizing (A.11), the following expression for  $\Delta P_{\rm ac}$  is obtained in vector notation:

$$\Delta \dot{P}_{\rm ac} = [\omega_c] \Delta p_{\rm ac} - [\omega_c] \Delta P_{\rm ac} \tag{A.12}$$

Replacing  $\Delta p_{\rm ac}$  from (A.3a), we have:

$$\Delta \dot{P}_{ac} = [\omega_c] \left( G_1^{ac} \Delta E + G_2^{ac} \Delta \theta + G_0^{ac} \Delta P_{IC} \right) - [\omega_c] \Delta P_{ac}$$

$$\Delta \dot{P}_{ac} = [\omega_c] G_1^{ac} \Delta E + [\omega_c] G_2^{ac} \Delta \theta + [\omega_c] G_0^{ac} \Delta P_{IC} - [\omega_c] \Delta P_{ac}$$
(A.13)

Now, replacing  $\Delta E$  from (A.8):

$$\Delta \dot{P}_{ac} = [\omega_c] G_1^{ac} ([N_{ac}] \Delta Q_{ac} + \Delta \chi) + [\omega_c] G_2^{ac} \Delta \theta + [\omega_c] G_0^{ac} \Delta P_{IC} - [\omega_c] \Delta P_{ac} 
\Delta \dot{P}_{ac} = [\omega_c] G_1^{ac} [N_{ac}] \Delta Q_{ac} + [\omega_c] G_1^{ac} \Delta \chi + [\omega_c] G_2^{ac} \Delta \theta + [\omega_c] G_0^{ac} \Delta P_{IC} - [\omega_c] \Delta P_{ac} 
\Delta \dot{P}_{ac} = G_9^{ac} \Delta Q_{ac} + G_{10}^{ac} \Delta \chi + G_{11}^{ac} \Delta \theta + G_{12}^{ac} \Delta P_{IC} + G_{13}^{ac} \Delta P_{ac}$$
(A.14)

Equation (A.14) corresponds to the state equation for the state variables  $\Delta P_{\rm ac}$ .

On the other hand, linearizing (A.10), we obtain:

$$\Delta \dot{\theta} = \Delta \omega = [M_{\rm ac}] \Delta P_{\rm ac} + \Delta \psi$$

$$\Delta \dot{\theta} = \Delta \omega = G_8^{\rm ac} \Delta P_{\rm ac} + \Delta \psi \tag{A.15}$$

Equation (A.15) corresponds to the state equation for the state variables  $\Delta\theta$ .

# A.1.4 Frequency secondary control

From (3.4b), the frequency secondary controller for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$\dot{\psi}_{i} = -\sigma_{i} \left(\omega_{i} - \omega^{*}\right) - \sigma_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(P_{ac-i} - P_{ac-j}\right) - \sigma_{i} \sum_{j \in \mathbb{N}_{dc}} a_{ij} \left(P_{ac-i} - P_{dc-j}\right)$$
(A.16)

To obtain the small-signal model of this controller, it is necessary to remember the adjacency matrix, which is given by:

$$A = \begin{pmatrix} A_{\rm dc} & A_{\rm dc-ac} & A_{\rm dc-IC} \\ A_{\rm ac-dc} & A_{\rm ac} & A_{\rm ac-IC} \\ A_{\rm IC-dc} & A_{\rm IC-ac} & A_{\rm IC} \end{pmatrix}$$
(A.17)

The adjacency matrix (**A**) is composed by 9 sub-matrices, which describe the following characteristics of the communication network of the hybrid ac/dc-microgrid:

•  $A_{dc}$  : Communication links between dc-DGs.

•  $A_{dc-ac}$  : Communication links between dc-DGs and ac-DGs.

•  $A_{\text{dc-IC}}$  : Communication links between dc-DGs and ICs.

•  $A_{\text{ac-dc}}$ : Communication links between ac-DGs and dc-DGs.

•  $A_{\rm ac}$  : Communication links between ac-DGs.

•  $A_{\text{ac-IC}}$  : Communication links between ac-DGs and ICs.

 $\bullet$   $A_{\rm IC-dc}~:~$  Communication links between  $\mathit{ICs}$  and  $\mathit{dc}\text{-DGs}.$ 

•  $A_{\rm IC-ac}$  : Communication links between ICs and ac-DGs.

•  $A_{\rm IC}$  : Communication links between ICs (0, in this case is only one).

Therefore, to describe the communication network for this controller we must focus only in the communication links between ac-DGs and dc-DGs, i.e., in sub-matrices  $A_{\rm ac}$  and  $A_{\rm ac-dc}$ .

The weighted in-degree  $(d_i)$  of node  $v_i$  is defined in the literature as the  $i^{th}$  row sum of the adjacency matrix A, as follows:

$$d_{i} = \sum_{j=1}^{N} a_{ij} \tag{A.18}$$

In this thesis, I define a **modified in-degree** to represent the interaction between elements within each sub-matrix. Thus, the modified in-degrees for node  $v_i$  are given by:

$$d_{(ac-ac)-i} = \sum_{j \in \mathbb{N}_{ac}} a_{ij} = a_{i1} + a_{i2} + \dots + a_{in}$$
(A.19a)

$$d_{(ac-dc)-i} = \sum_{j \in \mathbb{N}_{dc}} a_{ij} = a_{i1} + a_{i2} + \dots + a_{im}$$
(A.19b)

In vector notation, we have:

$$D_{\text{ac-ac}} = \text{diag}(\mathbf{d}_{(\text{ac-ac})-i}) \tag{A.20a}$$

$$D_{\text{ac-dc}} = \text{dia}g(\mathbf{d}_{(\text{ac-dc})-i}) \tag{A.20b}$$

Additionally, the Laplacian matrix  $(L_{ac})$  for the ac-microgrid is defined as:

$$L_{\rm ac} = D_{\rm ac-ac} + D_{\rm ac-dc} - A_{\rm ac-ac} \tag{A.21}$$

Now, linearizing the controller from (A.16), we obtain:

$$\Delta \dot{\psi} = -[\sigma] \Delta \omega - [\sigma] L_{\rm ac} \frac{1}{S_{\rm ac}^{+}} \Delta P_{\rm ac} + [\sigma] A_{\rm ac-dc} \frac{1}{P_{\rm dc}^{+}} \Delta P_{\rm dc}$$
(A.22)

Considering the expression for  $\Delta\omega$  from (A.15), we have:

$$\Delta \dot{\psi} = -[\sigma] \left( G_8^{\text{ac}} \Delta P_{\text{ac}} + \Delta \psi \right) - [\sigma] L_{\text{ac}} \frac{1}{S_{\text{ac}}^+} \Delta P_{\text{ac}} + [\sigma] A_{\text{ac}-\text{dc}} \frac{1}{P_{\text{dc}}^+} \Delta P_{\text{dc}} 
\Delta \dot{\psi} = -[\sigma] \left( G_8^{\text{ac}} + L_{\text{ac}} \frac{1}{S_{\text{ac}}^+} \right) \Delta P_{\text{ac}} - [\sigma] \Delta \psi + [\sigma] A_{\text{ac}-\text{dc}} \frac{1}{P_{\text{dc}}^+} \Delta P_{\text{dc}} 
\Delta \dot{\psi} = G_{14}^{\text{ac}} \Delta P_{\text{ac}} + G_{15}^{\text{ac}} \Delta \psi + G_{16}^{\text{ac}} \Delta P_{\text{dc}}$$
(A.23)

Equation (A.23) corresponds to the state equation for the state variables  $\Delta \psi$ .

#### A.1.5 Voltage secondary control

From (3.5b), the voltage secondary controller for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$\dot{\chi}_{i} = -\beta_{i} (E_{i} - E^{*}) - b_{i} \sum_{j \in \mathbb{N}_{ac}} a_{ij} (Q_{ac-i} - Q_{ac-j})$$
(A.24)

Now, linearizing the controller and considering the modified in-degree explained before:

$$\Delta \dot{\chi} = -[\beta] \Delta E - [b] L'_{\text{ac}} \frac{1}{S_{\text{ac}}^{+}} \Delta Q_{\text{ac}}$$
(A.25)

where:

$$L'_{\rm ac} = D_{\rm ac-ac} - A_{\rm ac-ac} \tag{A.26}$$

Replacing the expression for  $\Delta E$  from (A.8), we obtain:

$$\Delta \dot{\chi} = -[\beta] \left( [N_{\rm ac}] \Delta Q_{\rm ac} + \Delta \chi \right) - [b] L'_{\rm ac} \frac{1}{S_{\rm ac}^{+}} \Delta Q_{\rm ac}$$

$$\Delta \dot{\chi} = -\left( [\beta] [N_{\rm ac}] + [b] L'_{\rm ac} \frac{1}{S_{\rm ac}^{+}} \right) \Delta Q_{\rm ac} - [\beta] \Delta \chi$$

$$\Delta \dot{\chi} = G_{17}^{\rm ac} \Delta Q_{\rm ac} + G_{18}^{\rm ac} \Delta \chi \tag{A.27}$$

Equation (A.27) corresponds to the state equation for the state variables  $\Delta \chi$ .

# A.2 Closed-loop model for the dc-microgrid

In this Section, the closed-loop model for the dc-microgrid is obtained, including the topology and the controllers' characteristics.

#### A.2.1 Power flow in the dc-microgrid

The topology of the dc-microgrid is re-presented in Fig. A.2. The set of dc-DGs are labelled as  $\mathbb{N}_{dc} = \{n+1, \ldots, n+m\}$ . Further details of the topology can be found in Section 5.3.

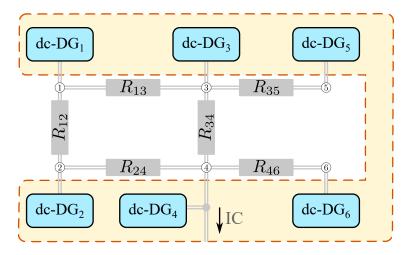


Figure A.2: Topology of the dc-microgrid.

The power injections for bus j in the dc-microgrid  $\forall j \in \mathbb{N}_{dc}$ , are given by:

$$p_{\mathrm{dc}-j} = p_{\mathrm{Ddc}-j} + \sum_{i \in \mathbb{N}_{\mathrm{dc}}} p_{ji}, \quad \forall j \in \mathbb{N}_{\mathrm{dc}}$$
 (A.28)

where  $p_{\text{Ddc}-j}$  is the local load at bus j,  $p_{ji}$  is the power flow between buses j and i, and  $p_{\text{dc}-j}$  is the power generated by the  $j^{th}$  dc-DG.

In vector notation, the power injections can be written as follows:

$$p_{\rm dc} = [V]YV + G_0^{\rm dc}P_{\rm IC}$$
 (A.29)

where  $p_{\text{dc}} \in \mathbb{R}^m$  is the vector of power generation,  $V \in \mathbb{R}^m$  is the vector of bus potentials,  $Y \in \mathbb{R}^{m \times m}$  is the admittance matrix,  $P_{\text{IC}}$  is the power in the IC and  $G_0^{\text{dc}} = (0\ 0\ 1\ 0\ 0)^T$  is the vector describing the electrical connection of the dc-microgrid and the IC.

Linearizing the power injections, we have:

$$\Delta p_{\rm dc} = ([V_0]Y + [YV_0]) \Delta V + G_0^{\rm dc} \Delta P_{\rm IC}$$
  

$$\Delta p_{\rm dc} = G_1^{\rm dc} \Delta V + G_0^{\rm dc} \Delta P_{\rm IC}$$
(A.30)

# A.2.2 Voltage reference

From (3.9a), the voltage reference for the  $j^{th}$  dc-DG ( $\forall j \in \mathbb{N}_{dc}$ ) is:

$$V_j = V^* + M_{\mathrm{dc}-j} \cdot P_{\mathrm{dc}-j} + \varphi_j \tag{A.31}$$

where  $P_{dc}$  is the filtered power generated by the  $j^{th}$  dc-DG, and it is given by:

$$P_{dc-j} = \frac{\omega_c}{s + \omega_c} p_{dc-j}$$

$$\dot{P}_{dc-j} = \omega_c \left( p_{dc-j} - P_{dc-j} \right) \tag{A.32}$$

Now, linearizing (A.32), the following expression for  $\Delta \dot{P}_{\rm dc}$  is obtained in vector notation:

$$\Delta \dot{P}_{\rm dc} = [\omega_c] \Delta p_{\rm dc} - [\omega_c] \Delta P_{\rm dc} \tag{A.33}$$

Replacing  $\Delta p_{\rm dc}$  from (A.30), we have:

$$\Delta \dot{P}_{dc} = [\omega_c] \left( G_1^{dc} \Delta V + G_0^{dc} \Delta P_{IC} \right) - [\omega_c] \Delta P_{dc}$$

$$\Delta \dot{P}_{dc} = [\omega_c] G_1^{dc} \Delta V + [\omega_c] G_0^{dc} \Delta P_{IC} - [\omega_c] \Delta P_{dc}$$
(A.34)

Now, linearizing the expression for the voltage (A.31), we obtain:

$$\Delta V = [M_{\rm dc}] \Delta P_{\rm dc} + \Delta \varphi \tag{A.35}$$

Replacing (A.35) in (A.34), we have:

$$\Delta \dot{P}_{dc} = [\omega_c] G_1^{dc} ([M_{dc}] \Delta P_{dc} + \Delta \varphi) + [\omega_c] G_0^{dc} \Delta P_{IC} - [\omega_c] \Delta P_{dc} 
\Delta \dot{P}_{dc} = [\omega_c] G_1^{dc} [M_{dc}] \Delta P_{dc} + [\omega_c] G_1^{dc} \Delta \varphi + [\omega_c] G_0^{dc} \Delta P_{IC} - [\omega_c] \Delta P_{dc} 
\Delta \dot{P}_{dc} = [\omega_c] (G_1^{dc} [M_{dc}] - I_m) \Delta P_{dc} + [\omega_c] G_1^{dc} \Delta \varphi + [\omega_c] G_0^{dc} \Delta P_{IC} 
\Delta \dot{P}_{dc} = G_2^{dc} \Delta P_{dc} + G_3^{dc} \Delta \varphi + G_4^{dc} \Delta P_{IC}$$
(A.36)

Equation (A.36) corresponds to the state equation for the state variables  $\Delta P_{\rm dc}$ .

# A.2.3 Secondary control loop

From (3.9b), the voltage secondary controller for the  $j^{th}$  dc-DG ( $\forall j \in \mathbb{N}_{dc}$ ) is:

$$\dot{\varphi}_{j} = -\gamma_{j} (V_{j} - V^{*}) - c_{j} \sum_{i \in \mathbb{N}_{dc}} a_{ji} (P_{dc-j} - P_{dc-i}) - c_{j} \sum_{i \in \mathbb{N}_{ac}} a_{ji} (P_{dc-j} - P_{ac-i})$$
 (A.37)

The modified in-degrees for node  $v_i$  are given by:

$$d_{(dc-dc)-j} = \sum_{i \in \mathbb{N}_{dc}} a_{ji} = a_{j1} + a_{j2} + \dots + a_{jm}$$
(A.38a)

$$d_{(dc-ac)-j} = \sum_{i \in \mathbb{N}_{ac}} a_{ji} = a_{j1} + a_{j2} + \dots + a_{jn}$$
(A.38b)

In vector notation, we have:

$$D_{\text{dc-dc}} = \text{diag}(\mathbf{d}_{(\text{dc-dc})-j}) \tag{A.39a}$$

$$D_{\text{dc-ac}} = \text{diag}(\mathbf{d}_{(\text{dc-ac})-j}) \tag{A.39b}$$

In addition, the Laplacian matrix  $(L_{dc})$  for the dc-microgrid is defined as:

$$L_{\rm dc} = D_{\rm dc-dc} + D_{\rm dc-ac} - A_{\rm dc-dc} \tag{A.40}$$

Now, linearizing the controller from (A.37), we obtain:

$$\Delta \dot{\varphi} = -[\gamma] \Delta V - [c] L_{\rm dc} \frac{1}{P_{\rm dc}^{+}} \Delta P_{\rm dc} + [c] A_{\rm dc-ac} \frac{1}{P_{\rm ac}^{+}} \Delta P_{\rm ac}$$
(A.41)

Using the expression for  $\Delta V$  obtained in (A.35):

$$\Delta \dot{\varphi} = -[\gamma] \left( [M_{\rm dc}] \Delta P_{\rm dc} + \Delta \varphi \right) - [c] L_{\rm dc} \frac{1}{P_{\rm dc}^{+}} \Delta P_{\rm dc} + [c] A_{\rm dc-ac} \frac{1}{P_{\rm ac}^{+}} \Delta P_{\rm ac}$$

$$\Delta \dot{\varphi} = -\left( [\gamma] [M_{\rm dc}] + [c] L_{\rm dc} \frac{1}{P_{\rm dc}^{+}} \right) \Delta P_{\rm dc} - [\gamma] \Delta \varphi + [c] A_{\rm dc-ac} \frac{1}{P_{\rm ac}^{+}} \Delta P_{\rm ac}$$

$$\Delta \dot{\varphi} = G_{5}^{\rm dc} \Delta P_{\rm dc} + G_{6}^{\rm dc} \Delta \varphi + G_{7}^{\rm dc} \Delta P_{\rm ac}$$
(A.42)

Equation (A.42) corresponds to the state equation for the state variables  $\Delta \varphi$ .

# A.3 Closed-loop model for the IC

In this Section, the closed-loop model for the IC is obtained, including the controllers' characteristics.

### A.3.1 Secondary control loop

From (3.10), the voltage secondary controller for the  $k^{th}$  IC is:

$$\dot{P}_{\mathrm{IC}-k} = -\tau_k \sum_{i \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ik} a_{jk} \left( P_{\mathrm{ac-i}} - P_{\mathrm{dc-}j} \right) \tag{A.43}$$

Using the same procedure described before, the modified in-degrees for node  $v_i$  are:

$$d_{(IC-ac)-k} = \sum_{i \in \mathbb{N}_{ac}} a_{ki} = a_{k1} + a_{k2} + \dots + a_{kn}$$
(A.44a)

$$d_{(IC-dc)-k} = \sum_{j \in \mathbb{N}_{dc}} a_{kj} = a_{k1} + a_{k2} + \dots + a_{km}$$
(A.44b)

In vector notation, we have:

$$D_{\rm IC-ac} = diag(d_{\rm (IC-ac)-i})$$
 (A.45a)

$$D_{\rm IC-dc} = diag(d_{\rm (IC-dc)-i})$$
(A.45b)

Now, linearizing the controller from (A.43), we have:

$$\Delta \dot{P}_{\rm IC} = -[\tau] D_{\rm IC-dc} A_{\rm IC-ac} \frac{1}{S_{\rm ac}^+} \Delta P_{\rm ac} + [\tau] D_{\rm IC-ac} A_{\rm IC-dc} \frac{1}{P_{\rm dc}^+} \Delta P_{\rm dc}$$

$$\Delta \dot{P}_{\rm IC} = G_1^{\rm IC} \Delta P_{\rm ac} + G_2^{\rm IC} \Delta P_{\rm dc}$$
(A.46)

Equation (A.46) corresponds to the state equation for the state variable  $\Delta P_{\rm IC}$ .

# A.4 Summary

In the following, a summary of the state variables and state equations are presented.

#### A.4.1 State variables

The closed-loop model for the studied hybrid ac/dc-microgrid with the proposed control strategy has 28 state variables, as follows:

- 3 angles from the ac-microgrid  $(\theta_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 active powers from the ac-microgrid  $(P_{ac}, \forall i \in \mathbb{N}_{ac})$ .
- 3 reactive powers from the ac-microgrid  $(Q_{ac}, \forall i \in \mathbb{N}_{ac})$ .
- 3 frequency secondary control variables from the ac-microgrid  $(\psi_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 voltage secondary control variables from the ac-microgrid  $(\chi_i, \forall i \in \mathbb{N}_{ac})$ .
- 6 active powers from the dc-microgrid  $(P_{dc}, \forall j \in \mathbb{N}_{dc})$ .
- 6 voltage secondary control variables from the dc-microgrid  $(\varphi_j, \forall j \in \mathbb{N}_{dc})$ .
- 1 IC power  $(P_{IC})$

### A.4.2 State equations

The state equations for the closed-loop model of the studied hybrid ac/dc-microgrid with the proposed control strategy are repeated below:

$$\begin{array}{llll} \Delta \dot{\theta} & = & G_8^{\rm ac} \Delta P_{\rm ac} + \Delta \psi & ({\rm A}.15) \\ \Delta \dot{P}_{\rm ac} & = & G_9^{\rm ac} \Delta Q_{\rm ac} + G_{10}^{\rm ac} \Delta \chi + G_{11}^{\rm ac} \Delta \theta + G_{12}^{\rm ac} \Delta P_{\rm IC} + G_{13}^{\rm ac} \Delta P_{\rm ac} & ({\rm A}.14) \\ \Delta \dot{Q}_{\rm ac} & = & G_5^{\rm ac} \Delta Q_{\rm ac} + G_6^{\rm ac} \Delta \chi + G_7^{\rm ac} \Delta \theta & ({\rm A}.9) \\ \Delta \dot{\psi} & = & G_{14}^{\rm ac} \Delta P_{\rm ac} + G_{15}^{\rm ac} \Delta \psi + G_{16}^{\rm ac} \Delta P_{\rm dc} & ({\rm A}.23) \\ \Delta \dot{\chi} & = & G_{17}^{\rm ac} \Delta Q_{\rm ac} + G_{18}^{\rm ac} \Delta \chi & ({\rm A}.27) \\ \Delta \dot{P}_{\rm dc} & = & G_2^{\rm dc} \Delta P_{\rm dc} + G_3^{\rm dc} \Delta \varphi + G_4^{\rm dc} \Delta P_{\rm IC} & ({\rm A}.36) \\ \Delta \dot{\varphi} & = & G_5^{\rm dc} \Delta P_{\rm dc} + G_6^{\rm dc} \Delta \varphi + G_7^{\rm dc} \Delta P_{\rm ac} & ({\rm A}.42) \\ \Delta \dot{P}_{\rm IC} & = & G_1^{\rm IC} \Delta P_{\rm ac} + G_2^{\rm IC} \Delta P_{\rm dc} & ({\rm A}.46) \end{array}$$

# Appendix B

# Closed-loop Model for a Hybrid ac/dc-Microgrid with the Operation Cost Minimization Strategy

In this Appendix, the procedure for obtaining the closed-loop model for the microgrid presented in Chapter 5 is presented. This model is also utilized for performing a small-signal analysis of the consensus-based distributed control strategy proposed in Chapter 4 for operation cost minimization.

Due to the primary purpose for obtaining the closed-loop model of the consensus-based distributed secondary control strategy proposed for operation cost minimization in a hybrid ac/dc-microgrid, the following simplifications were considered since their dynamics are much faster than that of the studied controllers:

- The inner control loop (voltage/current controllers) is not considered.
- The modulation techniques (PWM, SVM) of the converters is not considered.
- The switching of the switching devices is not considered.

Appendix B is organized as follows: Section B.1 presents the closed-loop model for the ac-microgrid, while the closed-loop model for the dc-microgrid is depicted in Section B.2. The closed-loop model for the IC is presented in Section B.3.

# B.1 Closed-loop model for the ac-microgrid

In this Section, the closed-loop model for the ac-microgrid is obtained, including the topology and the controllers' characteristics. Since the topology of the ac-microgrid is the same as the one considered for the consensus-based distributed secondary strategy for power-sharing (see Section A.1), the explanation is focused in the inclusion of the consensus-based distributed secondary controller for operation cost minimization. In this case, to obtain the closed-loop model for this controller, we assume all the units (i.e., ac-DGs, dc-DGs and IC) are operating

within limits.

#### B.1.1 Power flow in the ac-microgrid

The topology of the ac-microgrid can be seen in Fig. A.1. The set of ac-DGs are labelled as  $\mathbb{N}_{ac} = \{1, \ldots, n\}$ . Further details of the topology can be found in Section 5.2. As shown in (A.3), the power generated by the ac-DGs can be expressed in vector notation as follows:

$$\Delta p_{\rm ac}^{\rm G} = G_1^{\rm ac} \Delta E + G_2^{\rm ac} \Delta \theta + G_0^{\rm ac} \Delta P_{\rm IC}^{\rm ac}$$
(B.1a)

$$\Delta q_{\rm ac}^{\rm G} = G_3^{\rm ac} \Delta E + G_4^{\rm ac} \Delta \theta \tag{B.1b}$$

where  $p_{ac}^{G}, q_{ac}^{G} \in \mathbb{R}^{n}$  are the vectors of active and reactive power generation (respectively), the other matrices were already explained.

#### B.1.2 Voltage reference

From (3.5a), the voltage reference for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$E_{i} = E^* + N_{ac-i} \cdot Q_{ac-i}^{G} + \chi_{i}$$
(B.2)

where  $Q_{\text{ac-i}}^{\text{G}}$  is the filtered reactive power generated by the i<sup>th</sup> ac-DG. Then, as described in (A.9), in vector notation we have:

$$\Delta \dot{Q}_{ac} = \left[\omega_c\right] \left(G_3^{ac}[N_{ac}] - I_n\right) \Delta Q_{ac} + \left[\omega_c\right] G_3^{ac} \Delta \chi + \left[\omega_c\right] G_4^{ac} \Delta \theta 
\Delta \dot{Q}_{ac}^{G} = G_5^{ac} \Delta Q_{ac} + G_6^{ac} \Delta \chi + G_7^{ac} \Delta \theta$$
(B.3)

Equation (B.3) corresponds to the state equation for the state variables  $\Delta Q_{\rm ac}^{\rm G}$ .

# B.1.3 Frequency reference

From (4.13a), the frequency reference for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$\dot{\theta}_{i} = \omega_{i} = \omega^{*} + M_{ac-i}P_{ac-i}^{G} + \Omega_{i}^{ac}$$
(B.4)

Following the procedure described before (see Section A.1), in vector notation we have:

$$\Delta \dot{P}_{\rm ac}^{\rm G} = [\omega_c] G_1^{\rm ac} [N_{\rm ac}] \Delta Q_{\rm ac}^{\rm G} + [\omega_c] G_1^{\rm ac} \Delta \chi + [\omega_c] G_2^{\rm ac} \Delta \theta + [\omega_c] G_0^{\rm ac} \Delta P_{\rm IC}^{\rm ac} - [\omega_c] \Delta P_{\rm ac}^{\rm G} 
\Delta \dot{P}_{\rm ac}^{\rm G} = G_9^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{10}^{\rm ac} \Delta \chi + G_{11}^{\rm ac} \Delta \theta + G_{12}^{\rm ac} \Delta P_{\rm IC}^{\rm ac} + G_{13}^{\rm ac} \Delta P_{\rm ac}^{\rm G}$$
(B.5)

Equation (B.5) corresponds to the state equation for the state variables  $\Delta P_{\rm ac}^{\rm G}$ .

On the other hand, linearizing (B.4), we obtain:

$$\Delta \dot{\theta} = \Delta \omega = [M_{\rm ac}] \Delta P_{\rm ac}^{\rm G} + \Delta \Omega^{\rm ac}$$

$$\Delta \dot{\theta} = \Delta \omega = G_8^{\rm ac} \Delta P_{\rm ac}^{\rm G} + \Delta \Omega^{\rm ac}$$
(B.6)

Equation (B.6) corresponds to the state equation for the state variables  $\Delta\theta$ .

#### B.1.4 Frequency secondary control

Since the ac-DGs and the IC are operating within limits, the Lagrange multipliers associated to the power limit constraints are zero ( $\alpha_{\rm ac-i}^+ = \alpha_{\rm ac-i}^- = \alpha_{\rm IC}^+ = \alpha_{\rm IC}^- = 0$ ). Thus, from (4.13b), the frequency secondary controller for the i<sup>th</sup> ac-DG ( $\forall i \in \mathbb{N}_{ac}$ ) is:

$$\dot{\Omega}_{i}^{ac} = -k_{ac-i}^{a} \left(\omega_{i} - \omega^{*}\right) - k_{ac-i}^{b} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{ac}\right) - k_{ac-i}^{c} \sum_{j \in \mathbb{N}_{ac}} a_{ij} \left(\lambda_{i}^{ac} - \lambda_{j}^{dc}\right)$$
(B.7)

On the other hand, from (4.12a) and (4.12b), for  $\lambda_i^{ac}$  and  $\lambda_i^{dc}$  we have:

$$\lambda_{\rm i}^{\rm ac} = \left(\frac{k_{\rm IC} + 1}{2k_{\rm IC}}\right) \left(2a_{\rm ac-i}P_{\rm ac-i}^{\rm G} + b_{\rm ac-i}\right)$$
 (B.8a)

$$\lambda_j^{\text{dc}} = \left(\frac{k_{\text{IC}} + 1}{2}\right) \left(2a_{\text{dc}-j}P_{\text{dc}-j}^{\text{G}} + b_{\text{dc}-j}\right)$$
 (B.8b)

Linearizing (B.8), in vector notation we have:

$$\Delta \lambda^{\rm ac} = \left(\frac{k_{\rm IC} + 1}{k_{\rm IC}}\right) [a_{\rm ac}] \Delta P_{\rm ac}^{\rm G} = [a_{\rm ac}^k] \Delta P_{\rm ac}^{\rm G}$$
(B.9a)

$$\Delta \lambda^{\text{dc}} = (k_{\text{IC}} + 1) \left[ a_{\text{dc}} \right] \Delta P_{\text{dc}}^{\text{G}} = \left[ a_{\text{dc}}^{k} \right] \Delta P_{\text{dc}}^{\text{G}}$$
(B.9b)

Now, if the **modified in-degree** defined in (A.19) is considered, linearizing the controller from (B.7), in vector notation we obtain:

$$\Delta\dot{\Omega}^{\rm ac} = -[k_{\rm ac}^{\rm a}]\Delta\omega - \left\{[k_{\rm ac}^{\rm b}]\left(D_{\rm ac-ac} - A_{\rm ac-ac}\right) + [k_{\rm ac}^{\rm c}]D_{\rm ac-dc}\right\}\Delta\lambda^{\rm ac} + [k_{\rm ac}^{\rm c}]A_{\rm ac-dc}\Delta\lambda^{\rm dc} \quad (B.10)$$

Considering the expressions for  $\Delta\omega$ ,  $\Delta\lambda^{\rm ac}$  and  $\Delta\lambda^{\rm dc}$ , we have:

$$\Delta \dot{\Omega}^{\text{ac}} = -\left\{ [k_{\text{ac}}^{\text{b}}] \left( D_{\text{ac-ac}} - A_{\text{ac-ac}} \right) + [k_{\text{ac}}^{\text{c}}] D_{\text{ac-dc}} \right\} [a_{\text{ac}}^{k}] \Delta P_{\text{ac}}^{\text{G}} 
- [k_{\text{ac}}^{\text{a}}] \left( G_{8}^{\text{ac}} \Delta P_{\text{ac}}^{\text{G}} + \Delta \Omega^{\text{ac}} \right) + [k_{\text{ac}}^{\text{c}}] A_{\text{ac-dc}} [a_{\text{dc}}^{k}] \Delta P_{\text{dc}}^{\text{G}} 
- [k_{\text{ac}}^{\text{a}}] G_{8}^{\text{ac}} + [k_{\text{ac}}^{\text{b}}] \left( D_{\text{ac-ac}} - A_{\text{ac-ac}} \right) [a_{\text{ac}}^{k}] + [k_{\text{ac}}^{\text{c}}] D_{\text{ac-dc}} [a_{\text{ac}}^{k}] \right\} \Delta P_{\text{ac}}^{\text{G}} 
- [k_{\text{ac}}^{\text{a}}] \Delta \Omega^{\text{ac}} + [k_{\text{ac}}^{\text{c}}] A_{\text{ac-dc}} [a_{\text{dc}}^{k}] \Delta P_{\text{dc}}^{\text{G}} 
\Delta \dot{\Omega}^{\text{ac}} = G_{14}^{\text{ac}} \Delta P_{\text{ac}}^{\text{G}} + G_{15}^{\text{ac}} \Delta \Omega^{\text{ac}} + G_{16}^{\text{ac}} \Delta P_{\text{dc}}^{\text{G}} \tag{B.11}$$

Equation (B.11) corresponds to the state equation for the state variables  $\Delta\Omega^{\rm ac}$ .

# B.1.5 Voltage secondary control

Following the procedure described before (see Section A.1), in vector notation we have:

$$\Delta \dot{\chi} = -\left( [\beta][N_{\rm ac}] + [b] \left( D_{\rm ac-ac} - A_{\rm ac-ac} \right) \frac{1}{S_{\rm ac}^+} \right) \Delta Q_{\rm ac}^{\rm G} - [\beta] \Delta \chi$$

$$\Delta \dot{\chi} = G_{17}^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{18}^{\rm ac} \Delta \chi \tag{B.12}$$

Equation (B.12) corresponds to the state equation for the state variables  $\Delta \chi$ .

# B.2 Closed-loop model for the dc-microgrid

In this Section, the closed-loop model for the dc-microgrid is obtained, including the topology and the controllers' characteristics. Since the topology of the dc-microgrid is the same as the one considered for the consensus-based distributed secondary strategy for power-sharing (see Section A.2), the explanation is focused in the inclusion of the consensus-based distributed secondary controller for operation cost minimization. In this case, to obtain the closed-loop model for this controller, we assume all the units (i.e., ac-DGs, dc-DGs and IC) are operating within limits.

#### B.2.1 Power flow in the dc-microgrid

The topology of the dc-microgrid can be seen in Fig. A.2. The set of dc-DGs are labelled as  $\mathbb{N}_{dc} = \{n+1,\ldots,n+m\}$ . Further details of the topology can be found in Section 5.3. As shown in (A.30), the power generated by the dc-DGs can be expressed in vector notation as follows:

$$\Delta p_{\rm dc}^{\rm G} = ([V_0]Y + [YV_0]) \Delta V + (0\ 0\ 0\ 1/k_{\rm IC}\ 0\ 0)^T \Delta P_{\rm IC}^{\rm ac}$$
  
$$\Delta p_{\rm dc}^{\rm G} = G_1^{\rm dc} \Delta V + G_0^{\rm dc} \Delta P_{\rm IC}^{\rm ac}$$
(B.13)

### B.2.2 Voltage reference

From (4.15a), the voltage reference for the  $j^{th}$  dc-DG ( $\forall j \in \mathbb{N}_{dc}$ ) is:

$$V_j = V^* + M_{\text{dc}-j} P_{\text{dc}-j}^{G} + \Omega_j^{\text{dc}}$$
 (B.14)

where  $P_{dc}^{G}$  is the filtered power generated by the  $j^{th}$  dc-DG. Then, as described in (A.36), in vector notation we have:

$$\Delta \dot{P}_{\mathrm{dc}}^{\mathrm{G}} = \left[\omega_{c}\right] \left(G_{1}^{\mathrm{dc}}[M_{\mathrm{dc}}] - I_{m}\right) \Delta P_{\mathrm{dc}}^{\mathrm{G}} + \left[\omega_{c}\right] G_{1}^{\mathrm{dc}} \Delta \Omega^{\mathrm{dc}} + \left[\omega_{c}\right] G_{0}^{\mathrm{dc}} \Delta P_{\mathrm{dc}}^{\mathrm{G}} 
\Delta \dot{P}_{\mathrm{dc}}^{\mathrm{G}} = G_{2}^{\mathrm{dc}} \Delta P_{\mathrm{IC}}^{\mathrm{ac}} + G_{3}^{\mathrm{dc}} \Delta \Omega^{\mathrm{dc}} + G_{4}^{\mathrm{dc}} \Delta P_{\mathrm{IC}}^{\mathrm{ac}}$$
(B.15)

Equation (B.15) corresponds to the state equation for the state variables  $\Delta P_{\rm dc}^{\rm G}$ .

Linearizing (B.14), we obtain an expression for  $\Delta V$ :

$$\Delta V = [M_{\rm dc}] \Delta P_{\rm dc}^{\rm G} + \Delta \Omega^{\rm dc} \tag{B.16}$$

# B.2.3 Secondary control loop

Since the dc-DGs and the IC are operating within limits, the Lagrange multipliers associated to the power limit constraints are zero  $(\alpha_{\text{dc}-j}^+ = \alpha_{\text{dc}-j}^- = \alpha_{\text{IC}}^+ = \alpha_{\text{IC}}^- = 0)$ . Thus, from (4.15b), the voltage secondary controller for the  $j^{th}$  dc-DG  $(\forall j \in \mathbb{N}_{dc})$  is:

$$\dot{\Omega}_{j}^{\mathrm{dc}} = -k_{\mathrm{dc}-j}^{a} \left( V_{j} - V^{*} \right) - k_{\mathrm{dc}-j}^{b} \sum_{i \in \mathbb{N}_{\mathrm{dc}}} a_{ij} \left( \lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{dc}} \right) - k_{\mathrm{dc}-j}^{c} \sum_{i \in \mathbb{N}_{\mathrm{ac}}} a_{ij} \left( \lambda_{j}^{\mathrm{dc}} - \lambda_{i}^{\mathrm{ac}} \right)$$
(B.17)

Now, if the **modified in-degree** defined in (A.19) is considered, linearizing the controller from (B.17), in vector notation we obtain:

$$\Delta \dot{\Omega}^{\mathrm{dc}} = -[k_{\mathrm{dc}}^a] \Delta V - \left\{ [k_{\mathrm{dc}}^b] \left( D_{\mathrm{dc-dc}} - A_{\mathrm{dc-dc}} \right) + [k_{\mathrm{dc}}^c] D_{\mathrm{dc-ac}} \right\} \Delta \lambda^{\mathrm{dc}} + [k_{\mathrm{dc}}^c] A_{\mathrm{dc-ac}} \Delta \lambda^{\mathrm{ac}} \quad (B.18)$$

Considering the expressions for  $\Delta V$ ,  $\Delta \lambda^{\rm dc}$  and  $\lambda^{\rm ac}$ , we have:

$$\begin{split} \Delta \dot{\Omega}^{\text{dc}} &= -\left\{ \left[ k_{\text{dc}}^b \right] \left( D_{\text{dc-dc}} - A_{\text{dc-dc}} \right) + \left[ k_{\text{dc}}^c \right] D_{\text{dc-ac}} \right\} \left[ a_{\text{dc}}^k \right] \Delta P_{\text{dc}}^{\text{G}} \\ &- \left[ k_{\text{dc}}^a \right] \left( \left[ M_{\text{dc}} \right] \Delta P_{\text{dc}}^{\text{G}} + \Delta \Omega^{\text{dc}} \right) + \left[ k_{\text{dc}}^c \right] A_{\text{dc-ac}} \left[ a_{\text{ac}}^k \right] \Delta P_{\text{ac}}^{\text{G}} \\ \Delta \dot{\Omega}^{\text{dc}} &= -\left\{ \left[ k_{\text{dc}}^a \right] \left[ M_{\text{dc}} \right] + \left[ k_{\text{dc}}^b \right] \left( D_{\text{dc-dc}} - A_{\text{dc-dc}} \right) \left[ a_{\text{dc}}^k \right] + \left[ k_{\text{dc}}^c \right] D_{\text{dc-ac}} \left[ a_{\text{dc}}^k \right] \right\} \Delta P_{\text{dc}}^{\text{G}} \\ &- \left[ k_{\text{dc}}^a \right] \Delta \Omega^{\text{dc}} - \left[ k_{\text{dc}}^c \right] A_{\text{dc-ac}} \left[ a_{\text{ac}}^k \right] \Delta P_{\text{ac}}^{\text{G}} \\ \Delta \dot{\Omega}^{\text{dc}} &= G_5^{\text{dc}} \Delta P_{\text{dc}}^{\text{G}} + G_6^{\text{dc}} \Delta \Omega^{\text{dc}} + G_7^{\text{dc}} \Delta P_{\text{ac}}^{\text{G}} \end{split} \tag{B.19}$$

Equation (B.19) corresponds to the state equation for the state variables  $\Delta\Omega^{dc}$ .

# B.3 Closed-loop model for the IC

In this Section, the closed-loop model for the IC is obtained, including the controllers' characteristics.

## B.3.1 Secondary control loop

Since the IC is operating within limits, the Lagrange multipliers associated to the power limit constraints are zero ( $\alpha_{\rm IC}^+ = \alpha_{\rm IC}^- = 0$ ). Thus, from (4.16a), the secondary controller for the IC is:

$$\dot{P}_{\mathrm{IC}-k}^* = -\tau_{\mathrm{IC}-k} \sum_{i \in \mathbb{N}_{\mathrm{ac}}} \sum_{j \in \mathbb{N}_{\mathrm{dc}}} a_{ik} a_{jk} \left( \lambda_{\mathrm{ac-i}} - \lambda_{\mathrm{dc-}j} \right)$$
(B.20)

Now, if the **modified in-degree** defined in (A.19) is considered, linearizing the controller from (B.20), in vector notation we obtain:

$$\Delta \dot{P}_{\rm IC}^{\rm ac} = -[\tau] D_{\rm IC-dc} A_{\rm IC-ac} \Delta \lambda^{\rm ac} + [\tau] D_{\rm IC-ac} A_{\rm IC-dc} \Delta \lambda^{\rm dc} 
\Delta \dot{P}_{\rm IC}^{\rm ac} = -[\tau] D_{\rm IC-dc} A_{\rm IC-ac} [a_{\rm ac}^k] \Delta P_{\rm ac}^{\rm G} + [\tau] D_{\rm IC-ac} A_{\rm IC-dc} [a_{\rm dc}^k] \Delta P_{\rm dc}^{\rm G} 
\Delta \dot{P}_{\rm IC}^{\rm ac} = G_1^{\rm IC} [a_{\rm ac}^k] \Delta P_{\rm ac}^{\rm G} + G_2^{\rm IC} \Delta P_{\rm dc}^{\rm G}$$
(B.21)

Equation (B.21) corresponds to the state equation for the state variable  $\Delta P_{\rm IC}^{\rm ac}$ .

# B.4 Summary

In the following, a summary of the state variables and state equations are presented.

#### **B.4.1** State variables

The closed-loop model for the studied hybrid ac/dc-microgrid with the proposed control strategy for operation cost minimization has 28 state variables, as follows:

- 3 angles from the ac-microgrid  $(\theta_i, \forall i \in \mathbb{N}_{ac})$ .
- 3 active powers from the *ac*-microgrid  $(P_{ac}^{G}, \forall i \in \mathbb{N}_{ac})$ .
- 3 reactive powers from the ac-microgrid  $(Q_{ac}^{G}, \forall i \in \mathbb{N}_{ac})$ .
- 3 frequency secondary control variables from the ac-microgrid  $(\Omega_i^{ac}, \forall i \in \mathbb{N}_{ac})$ .
- 3 voltage secondary control variables from the ac-microgrid  $(\chi_i, \forall i \in \mathbb{N}_{ac})$ .
- 6 active powers from the dc-microgrid  $(P_{dc}^{G}, \forall j \in \mathbb{N}_{dc})$ .
- 6 voltage secondary control variables from the dc-microgrid  $(\Omega_i^{dc}, \forall j \in \mathbb{N}_{dc})$ .
- 1 IC power  $(P_{IC}^{ac})$ .

### B.4.2 State equations

The state equations for the closed-loop model of the studied hybrid ac/dc-microgrid with the proposed control strategy are repeated below:

$$\begin{array}{lll} \Delta \dot{\theta} & = & G_8^{\rm ac} \Delta P_{\rm ac}^{\rm G} + \Delta \Omega^{\rm ac} & (\text{B.6}) \\ \Delta \dot{P}_{\rm ac}^{\rm G} & = & G_9^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{10}^{\rm ac} \Delta \chi + G_{11}^{\rm ac} \Delta \theta + G_{12}^{\rm ac} \Delta P_{\rm IC}^{\rm ac} + G_{13}^{\rm ac} \Delta P_{\rm ac}^{\rm G} & (\text{B.5}) \\ \Delta \dot{Q}_{\rm ac}^{\rm G} & = & G_5^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_6^{\rm ac} \Delta \chi + G_7^{\rm ac} \Delta \theta & (\text{B.3}) \\ \Delta \dot{\Omega}^{\rm ac} & = & G_{14}^{\rm ac} \Delta P_{\rm ac}^{\rm G} + G_{15}^{\rm ac} \Delta \Omega^{\rm ac} + G_{16}^{\rm ac} \Delta P_{\rm dc}^{\rm G} & (\text{B.11}) \\ \Delta \dot{\chi} & = & G_{17}^{\rm ac} \Delta Q_{\rm ac}^{\rm G} + G_{18}^{\rm ac} \Delta \chi & (\text{B.12}) \\ \Delta \dot{P}_{\rm dc}^{\rm G} & = & G_2^{\rm dc} \Delta P_{\rm dc}^{\rm G} + G_3^{\rm dc} \Delta \Omega^{\rm dc} + G_4^{\rm dc} \Delta P_{\rm IC}^{\rm ac} & (\text{B.15}) \\ \Delta \dot{\Omega}^{\rm dc} & = & G_5^{\rm dc} \Delta P_{\rm dc}^{\rm G} + G_6^{\rm dc} \Delta \Omega^{\rm dc} + G_7^{\rm dc} \Delta P_{\rm ac}^{\rm G} & (\text{B.19}) \\ \Delta \dot{P}_{\rm IC}^{\rm ac} & = & G_1^{\rm IC} \Delta P_{\rm ac}^{\rm G} + G_2^{\rm IC} \Delta P_{\rm dc}^{\rm G} & (\text{B.21}) \end{array}$$